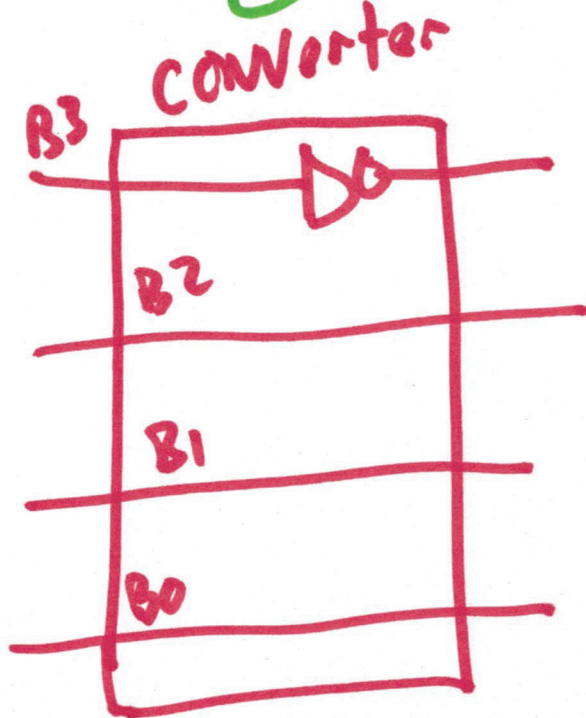


CPE 100 Digital

Logic Design

MARCH 31, 2021

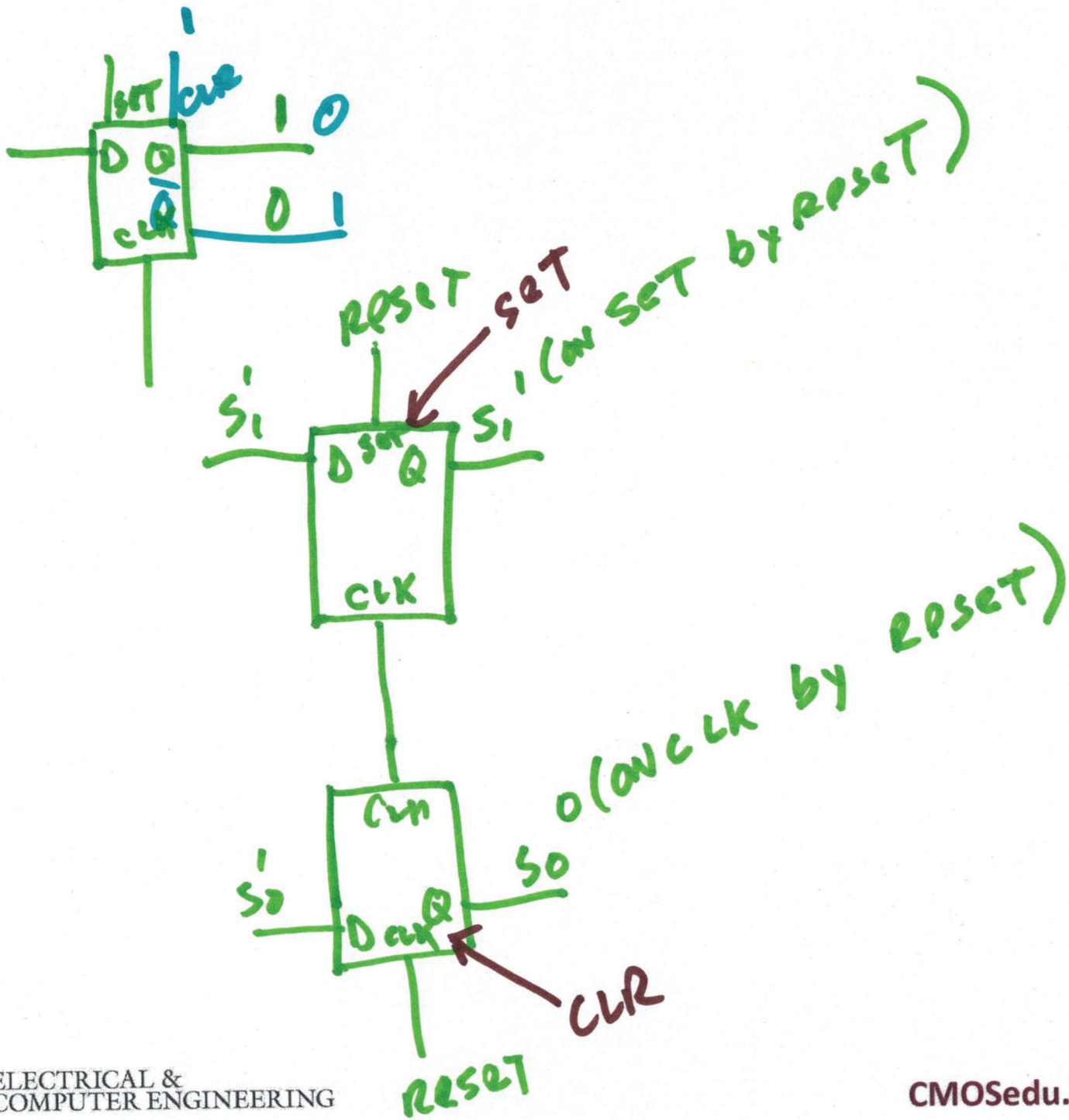
Lecture 17

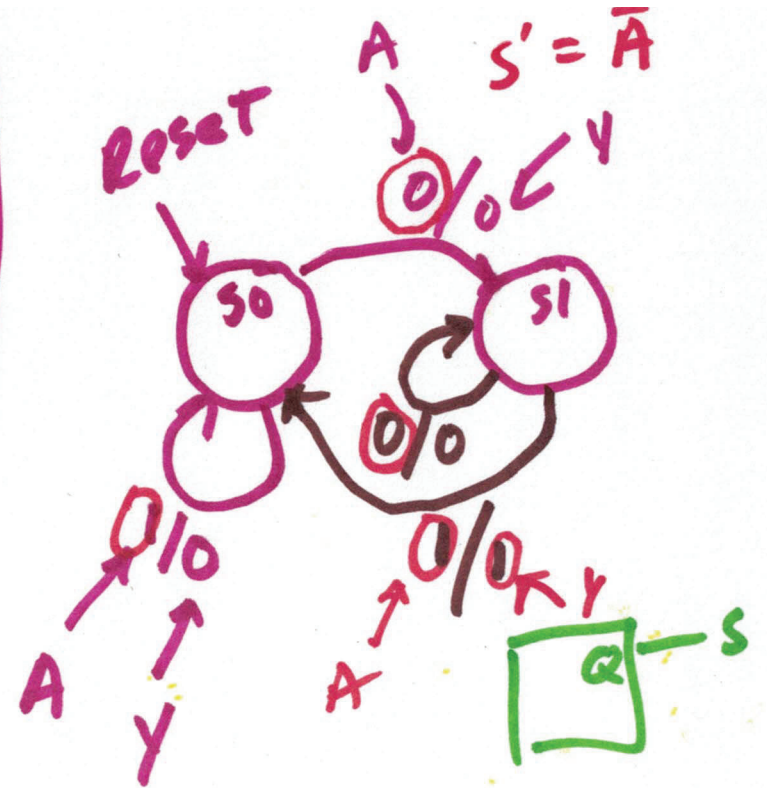
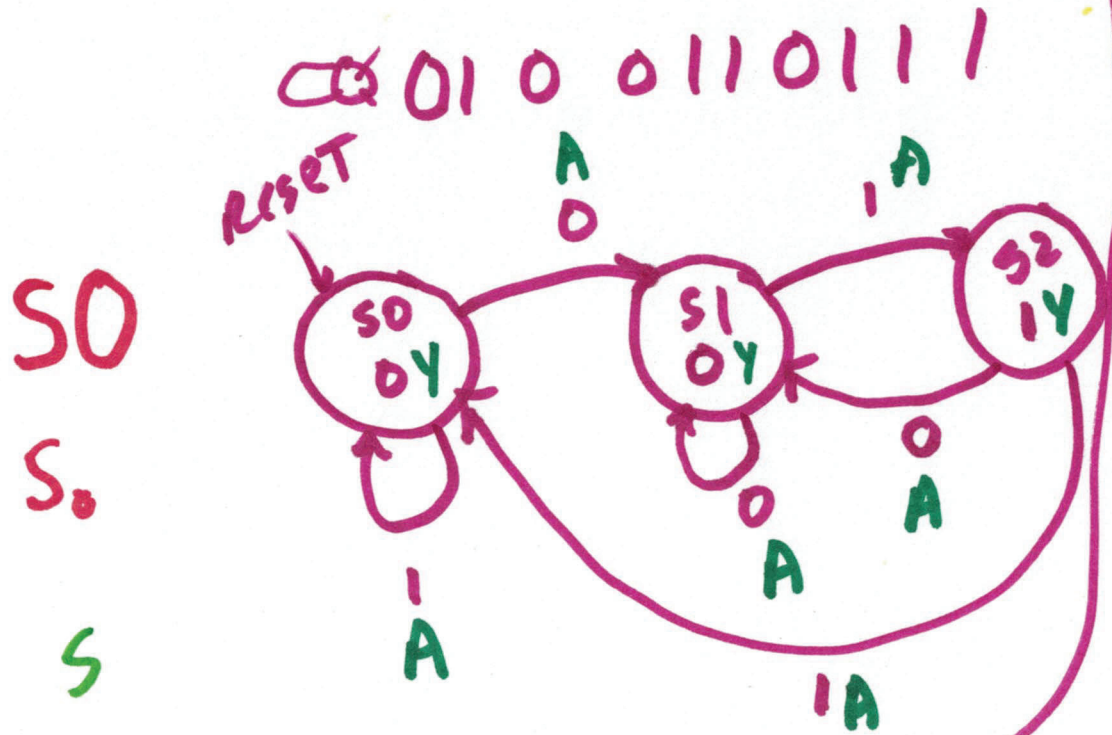


Binary Offset
15 1111 → 0111 +7
14 B₃ B₂ B₁ B₀ two's
13 out
12
11

10
9 1000 → 0000
8
7

6
5
4
3
2
1
CMOSedu.com
0000 → 1000
0 -8





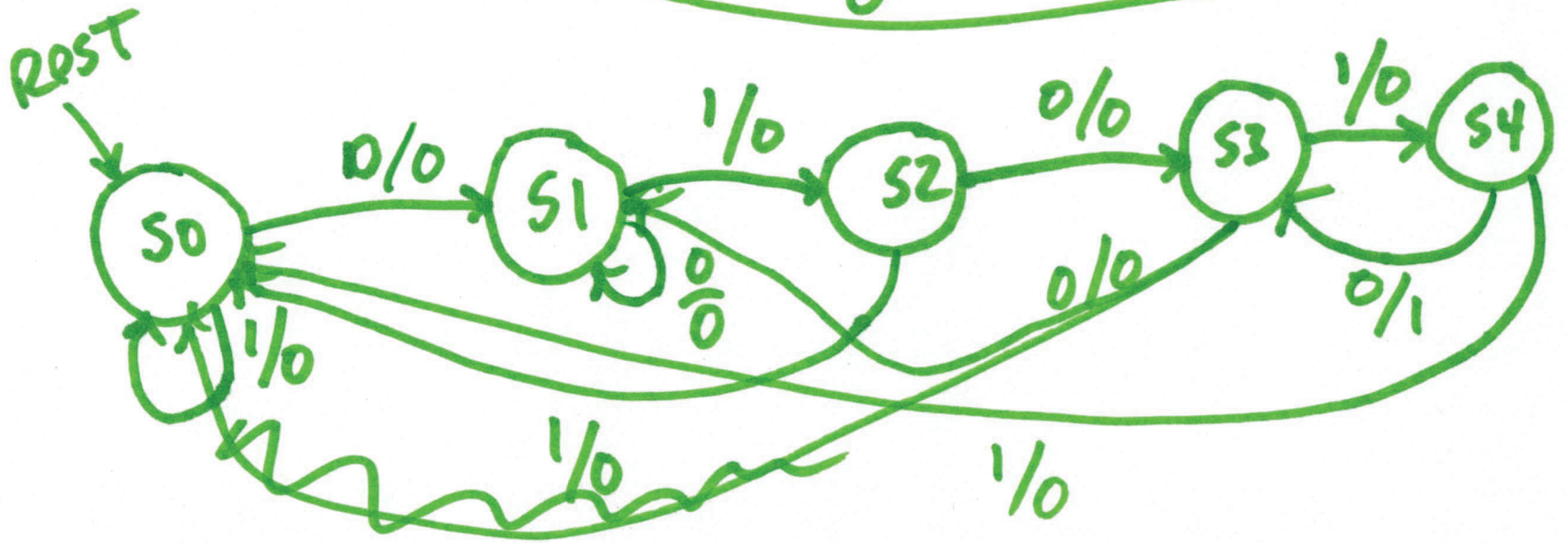
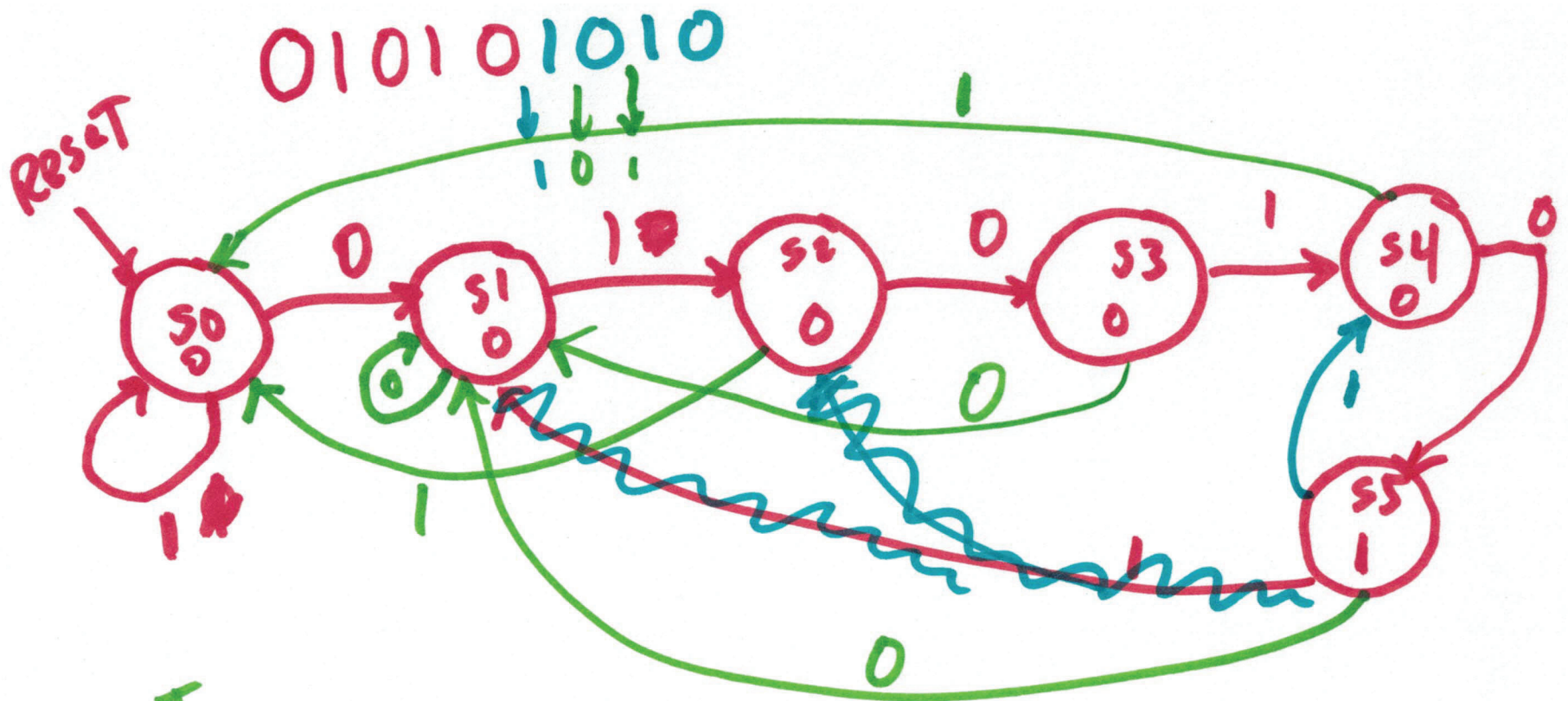
Moore

S	A	S'	Y
0S0	0	S1	0
0S0	1	S0	0
1S1	0	S1	0
1S1	1	S0	1

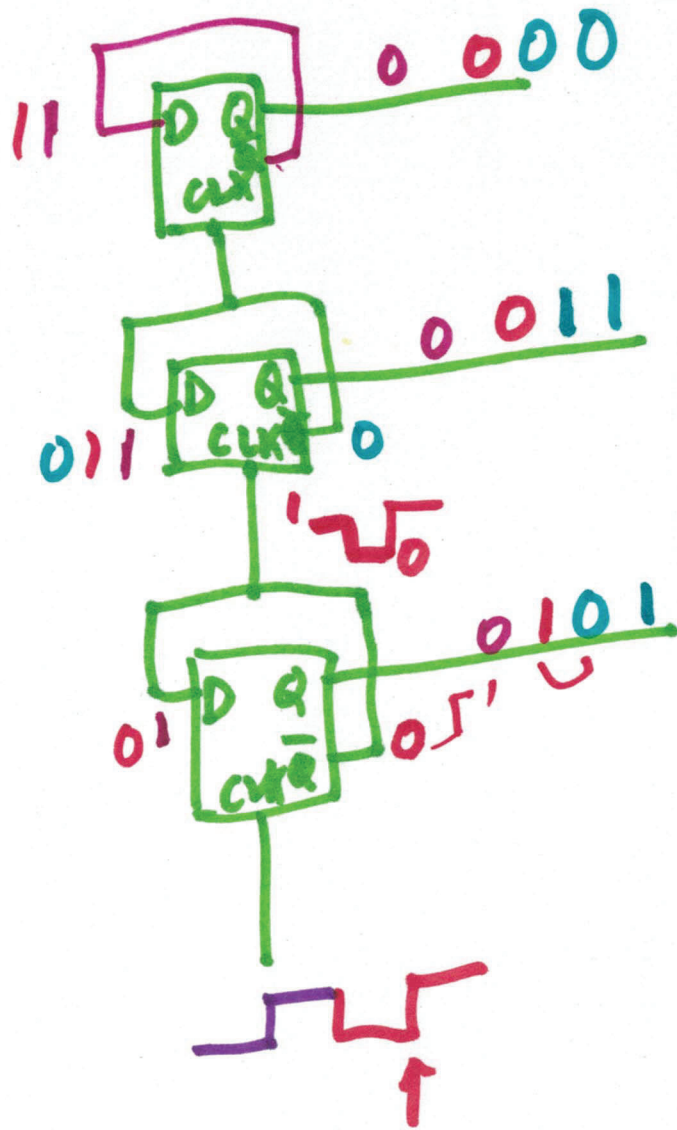
Mealy

$$\begin{aligned}
 S' &= \bar{S} \bar{A} \bar{Y} + S \bar{A} \bar{Y} \\
 &= \bar{A} \bar{Y} + \bar{A} \bar{Y} \\
 &= \bar{A} \bar{Y}
 \end{aligned}$$

3)



Counter



5)