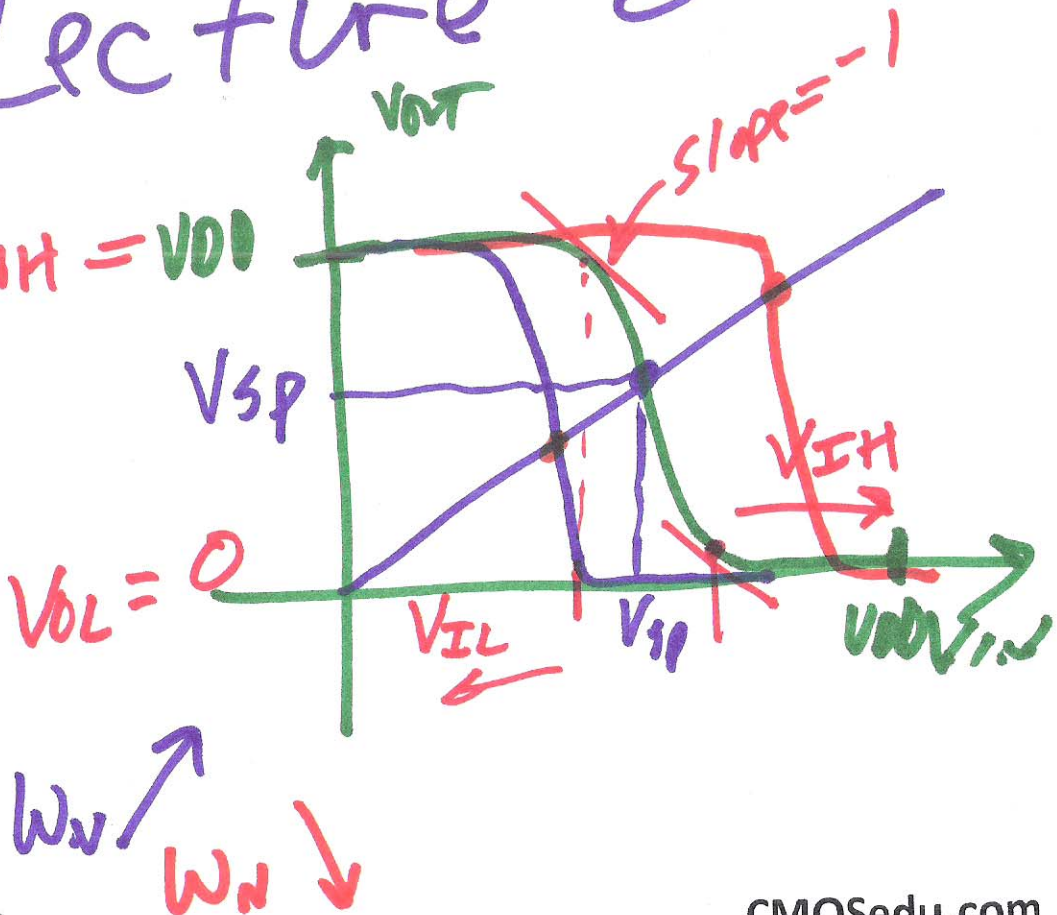
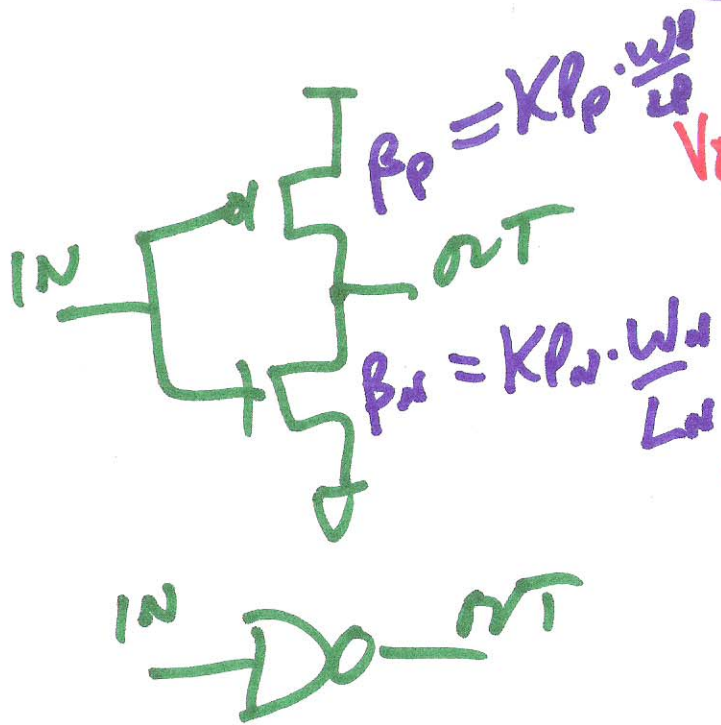


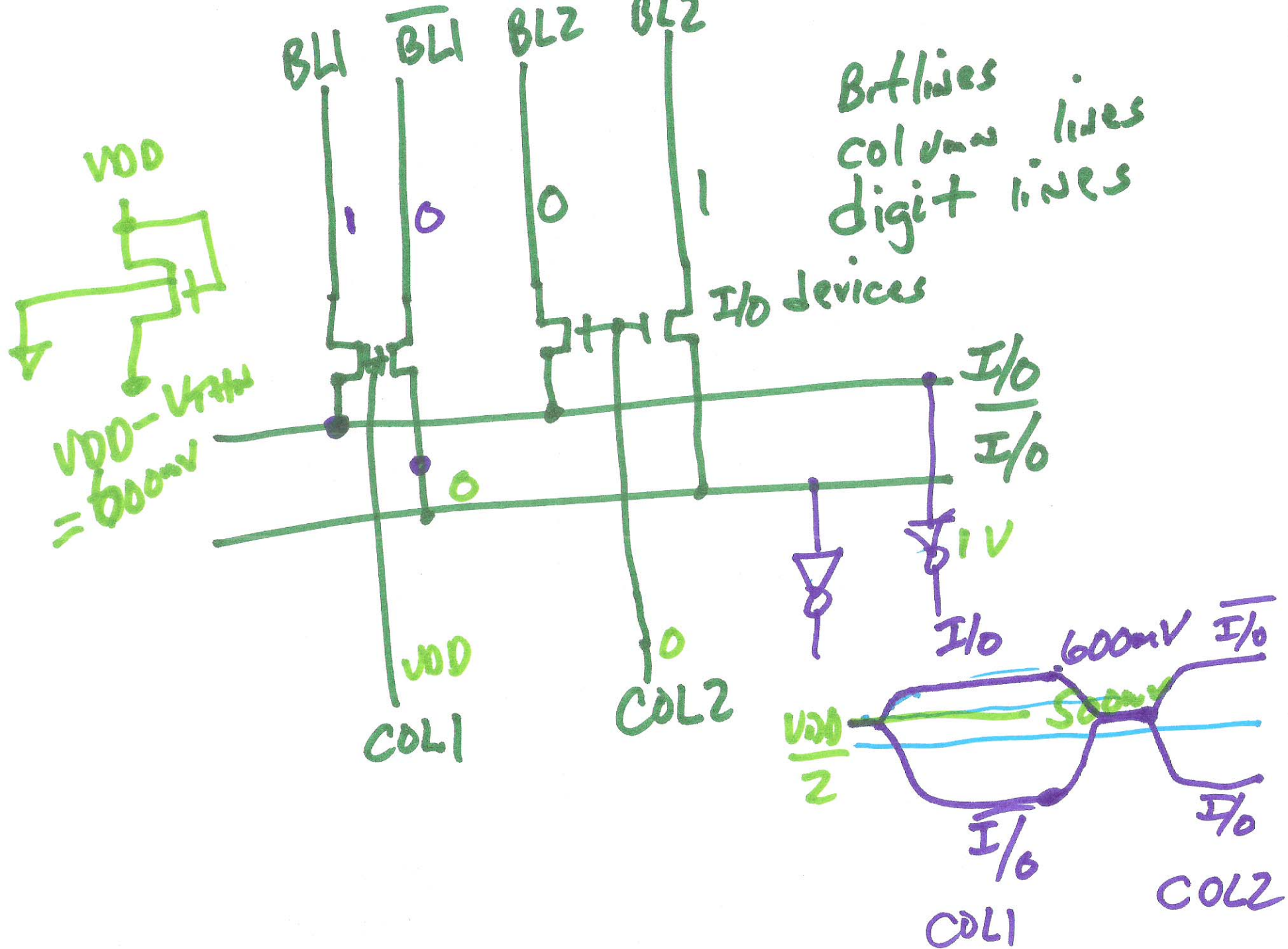
ECG 721

8/26/15

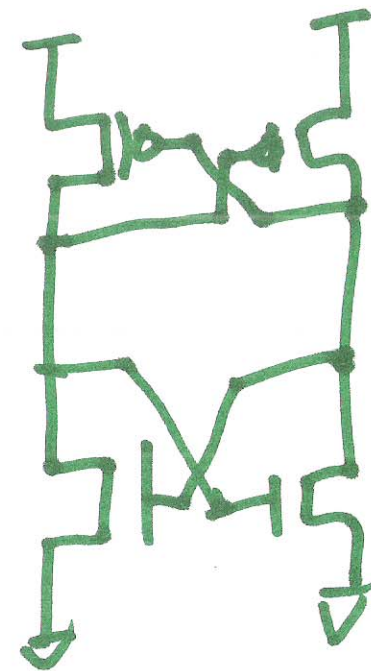
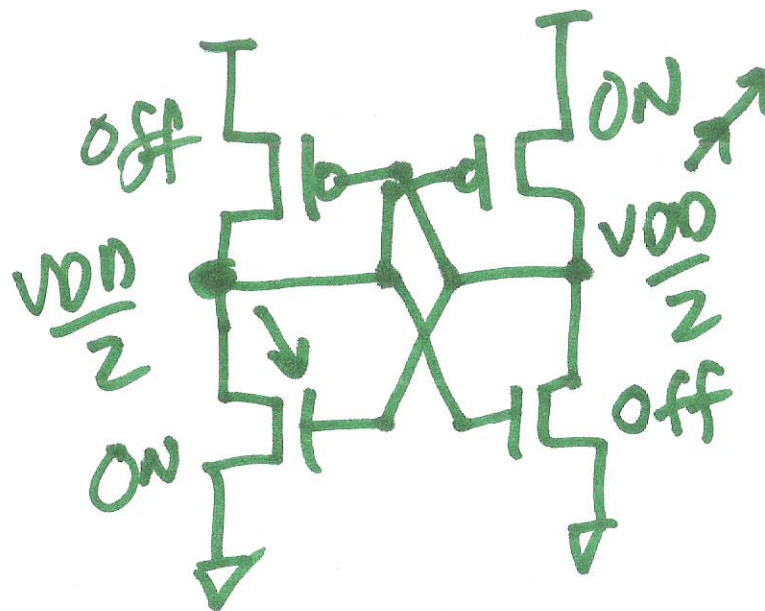
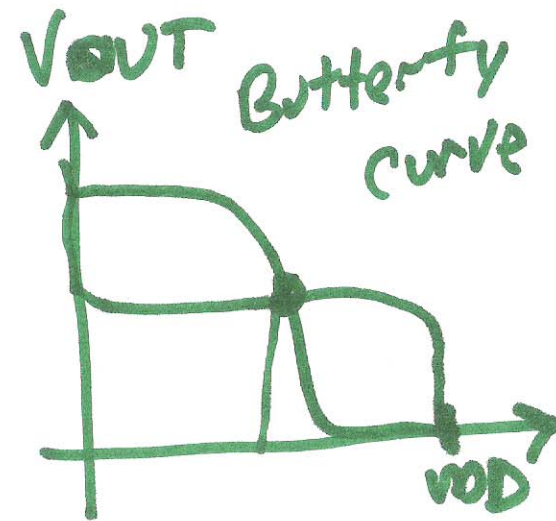
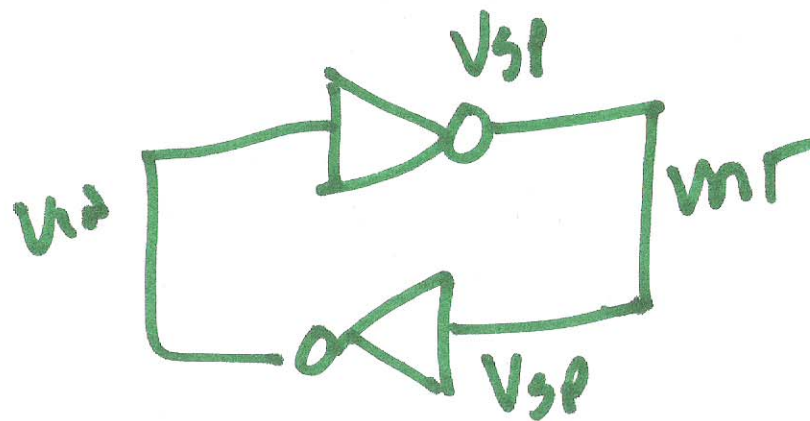
Lecture 2

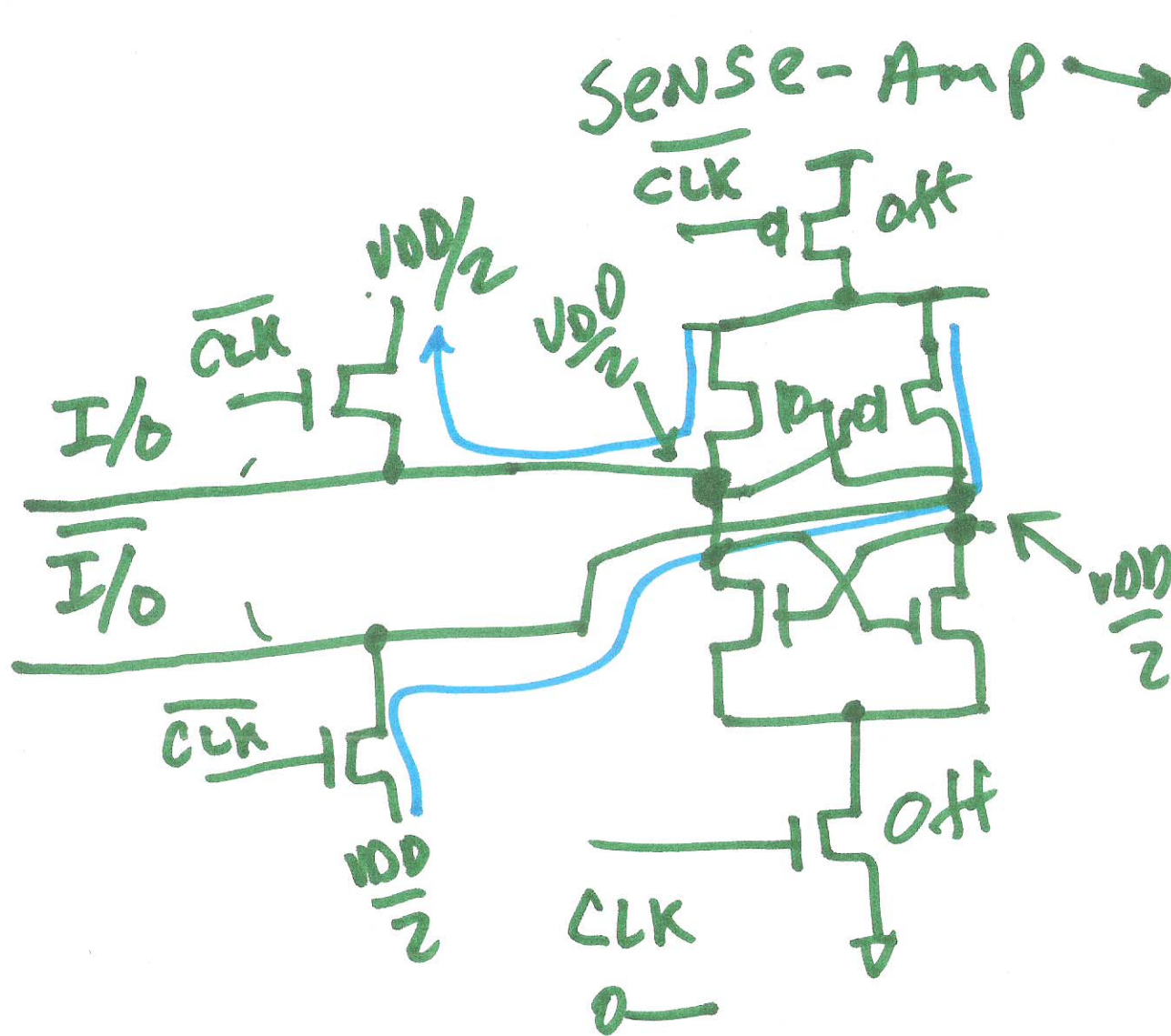


1)



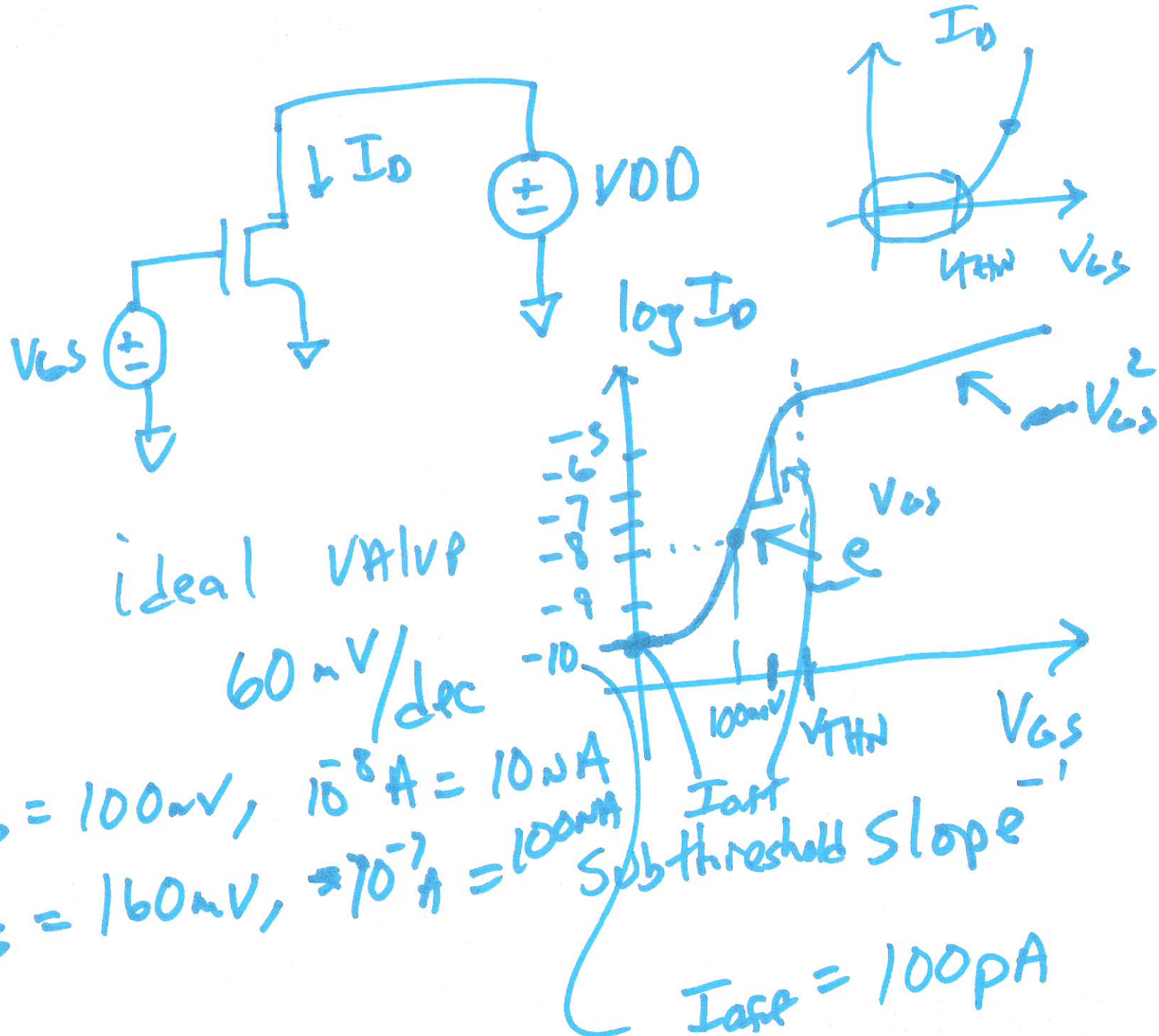
Latch





1. Sense data correctly & fast.
2. Regenerate full logic levels

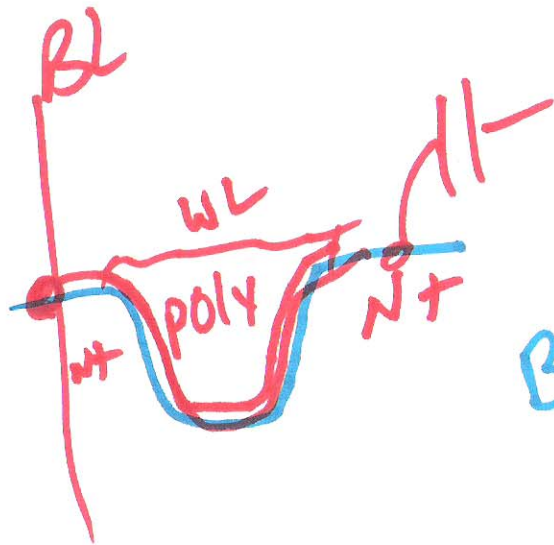
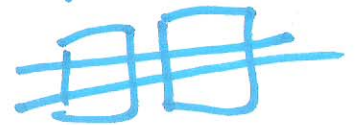
4)



5)

ITIC DRAM

one Transistor
one Capacitor
Dynamic Random
Access Memory



$$I = C \frac{dv}{dt}$$

$$100p = 20f \cdot \frac{dv}{dt}$$

$$\frac{dv}{dt} = \frac{5mV}{1s}$$

b)