

EE 220 Circuits 1

OCT. 23, 2019

Lecture 16

$$10^{11} \cdot \frac{1}{6} \cdot \left(\frac{1}{2}\right)^2 \cdot (10^{-6})^2$$

$$10^{11} \cdot 10^{-12} \cdot \frac{1}{6} \cdot \frac{1}{4}$$

$$\frac{1}{10} \cdot \frac{1}{6} \cdot \frac{1}{4}$$

$$= \frac{1}{240} = 4.16 \text{ mV}$$

$$i_c(t) = \frac{0.5 \mu\text{A}}{1.5 \mu\text{A}} \cdot t = \frac{1}{3} t$$

$$v_c(t) = \frac{1}{10 \text{ pF}} \int_0^t \frac{1}{3} t \, dt = 10^{11} \cdot \frac{1}{6} t^2 \Big|_0^t$$

$$t=0, v_c(t)=0 \quad v_c(t) = 10^{11} \cdot \frac{1}{6} \cdot t^2$$

$$t = \frac{1}{2} \mu\text{s}, v_c(1/2 \mu\text{s}) = 4.16 \text{ mV}$$

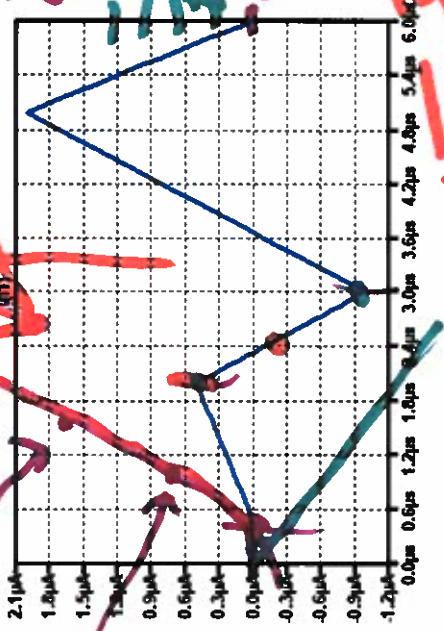
$$t = 1 \mu\text{s}, v_c(1 \mu\text{s}) = \frac{1}{60} = 16.6 \text{ mV}$$

1)

H.W. #14 EE 220 Fall 2019

Show your work for credit!

- Suppose a 0.4 uF capacitor is charged to 1 V. How much charge is stored on the capacitor? (1 point)
- The following current is used to charge a 10 pF capacitor. Sketch the voltage across the capacitor and the charge stored on the capacitor against time (show your hand calculations, no hand calculations, no credit). Verify your answer with LTSpice. Note that the current seen in this figure can be generated using the piece-wise linear (PWL) source, see example below (make sure you understand how to use a PWL source). (4 points)

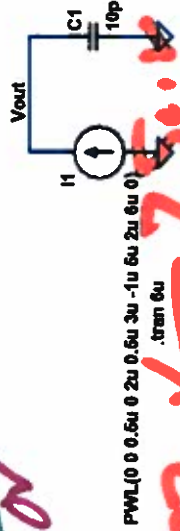


$Q = C \cdot V = 0.4 \mu\text{F} \cdot 1 \text{ V} = 0.4 \mu\text{C}$

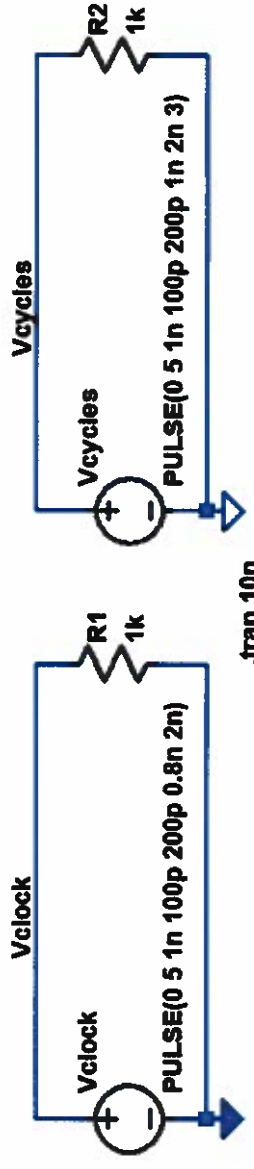
$I_{\text{avg}} = \frac{1.0 \mu\text{A} + 2.0 \mu\text{A}}{2} = 1.5 \mu\text{A}$

$t = \frac{Q}{I} = \frac{0.4 \mu\text{C}}{1.5 \mu\text{A}} = 0.267 \mu\text{s}$

$37 \sim V$



- Suppose that a constant current source, I , is used to *discharge* a capacitor, C . What will the voltage across the capacitor look like? Why? Provide an example using LTSpice to support your answer. (3 points)
- Repeat problem 3 if the constant current source is *charging* the capacitor. (2 points)
- In your own words explain what each of the parameters mean in the following pulse statements. (2 points)



- Suppose you are doing a sim where you need a clock signal operating at 1 MHz that oscillates between 0 V and 5 V. Assume the duty cycle of the clock is 50% (the clock signal is high [= 5 V] the same amount of time that the clock is low [= 0V]). Assume the rising and falling edges are 10 ns. Show how to implement this clock signal in LTSpice. (2 points)

(2)

$$t = 1.54 \mu\text{s}, v_c(t) = 10'' \cdot \frac{1}{6} \cdot \left(\frac{3}{2}\right)^2 \cdot 10^{-12}$$

$$= \frac{1}{10} \cdot \frac{1}{6} \cdot \frac{9}{4}$$

$$= \frac{9}{240} = 9 \cdot 4 \cdot 16 \approx 37 \mu\text{V}$$

$$v_c(t) = 10'' \cdot \frac{1}{6} t^2$$

$$v_c(t) = 10'' \cdot \frac{1}{6} \left(t - \frac{1}{2} \mu\text{s}\right)^2 + 0$$

$$\frac{1}{2} \mu\text{s} \leq t \leq 1.54 \mu\text{s}$$

$$i_c(t) = \frac{-1.54A}{145} \cdot t$$

$$v_c(t) = 10^{11} \int_0^t -\frac{3}{2} t \cdot dt$$

$$= 10^{11} \cdot \left(-\frac{3}{2}\right) \cdot \frac{1}{2} t^2 \Big|_0^t$$

$$t = 145$$

$$v_c(t) = 10^{11} \cdot \left(-\frac{3}{4}\right) \cdot 10^{-12}$$

$$= 10^{11} \left(-\frac{3}{4}\right) t^2$$

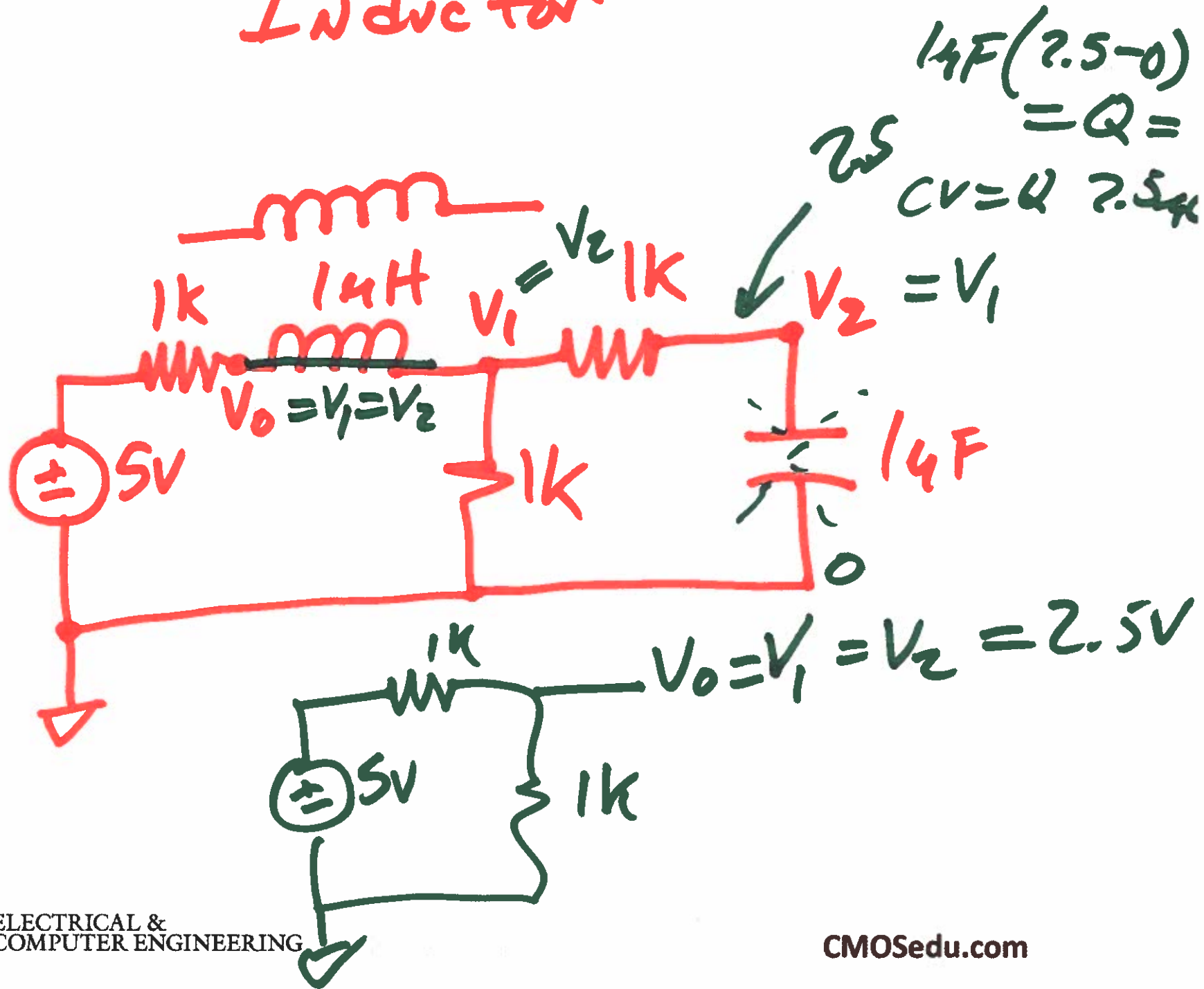
$$= -\frac{3}{40} = -75\mu W$$

$$t = \frac{1}{2} 45$$

$$v_c = 10^{11} \cdot 10^{-12} \left(-\frac{3}{4}\right) \left(\frac{1}{2}\right)^2$$

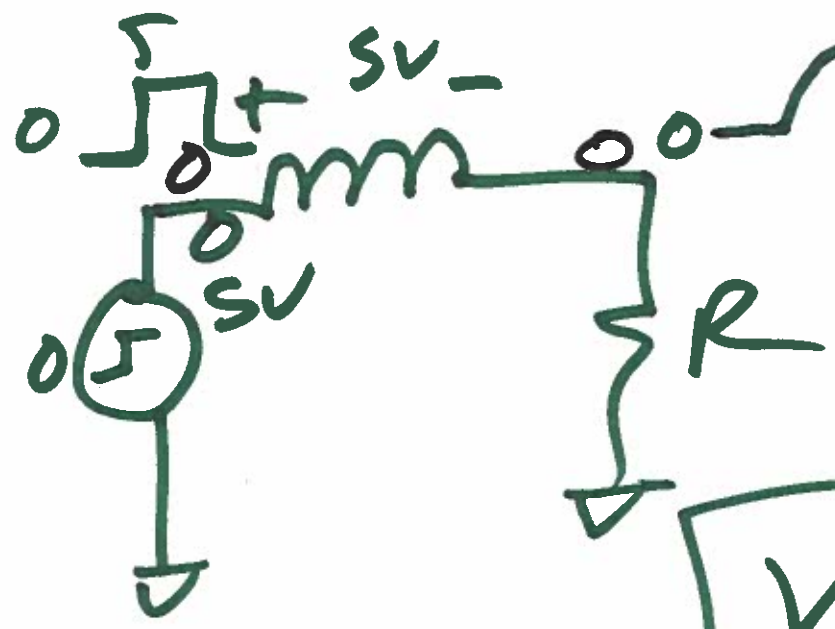
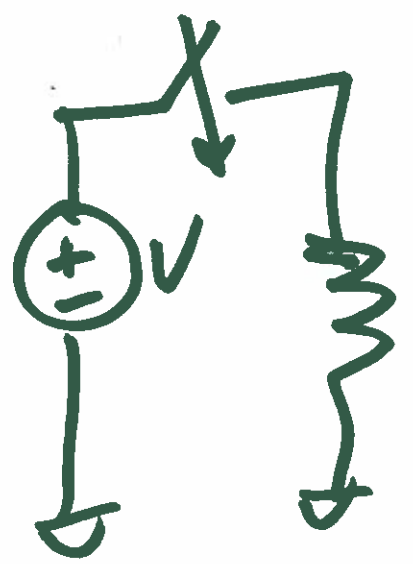
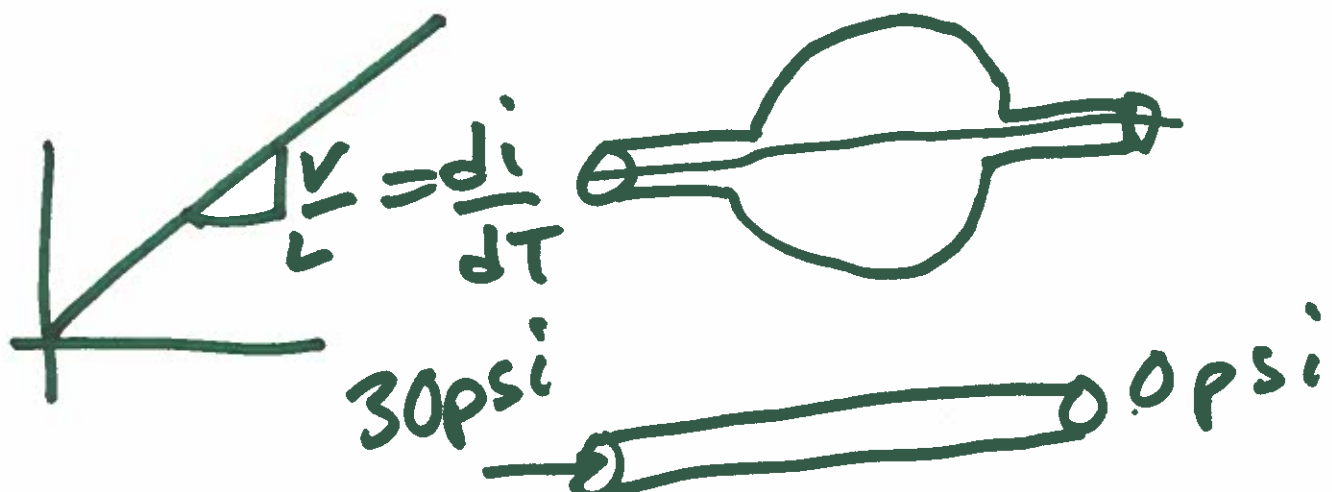
$$= \frac{-1}{10} \cdot \frac{3}{16} = -18.75\mu W$$

Inductor



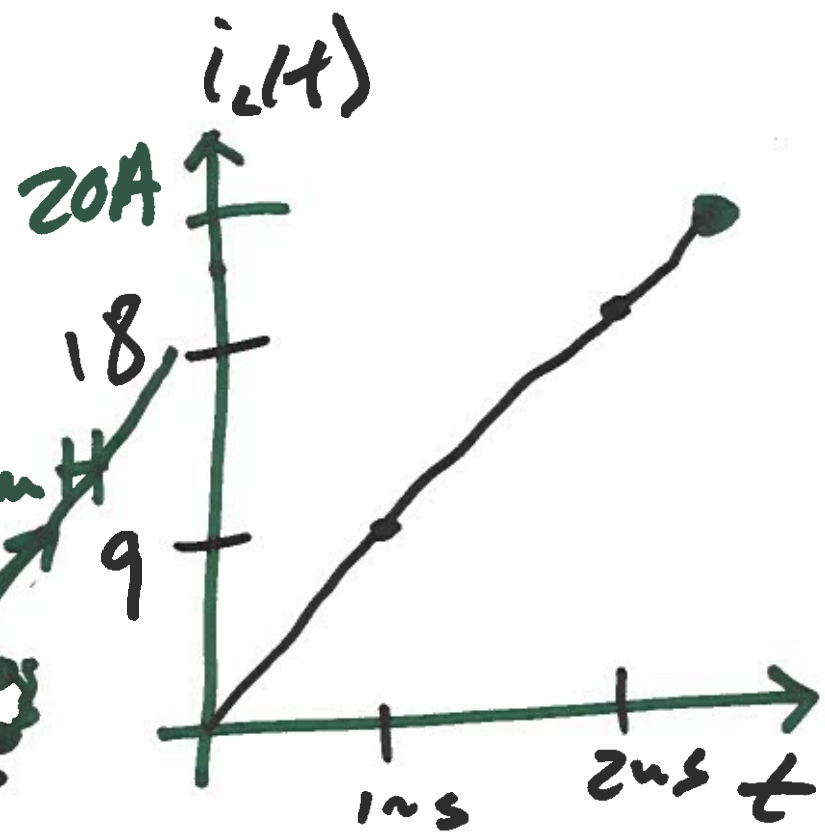
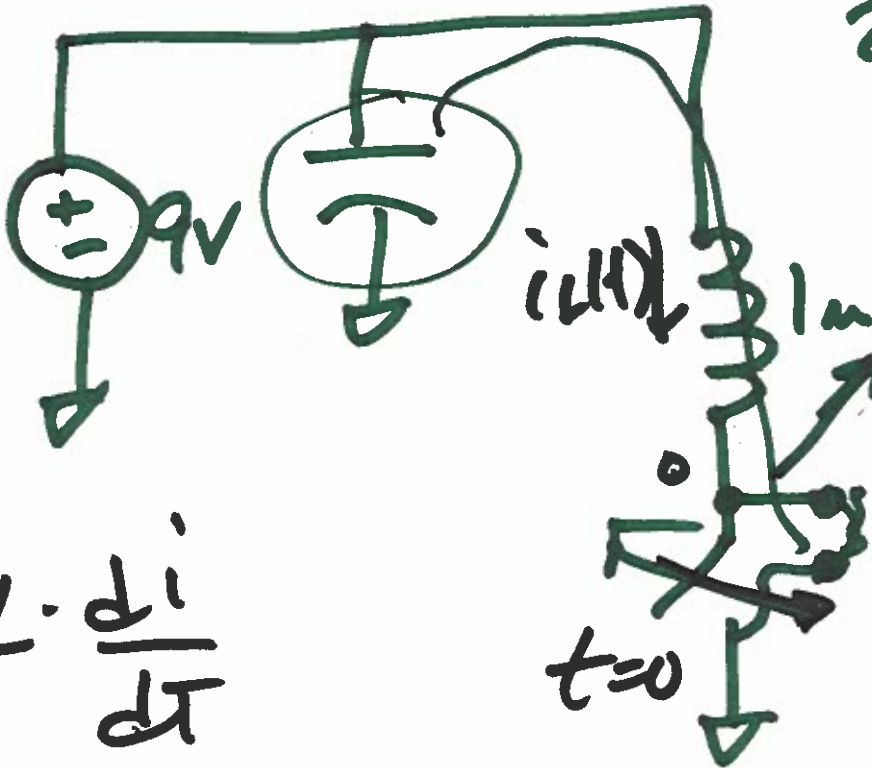
5)

fast signals
 inductor is
 AN OPEN
 CAN'T change
 current through
 inductor instantly



$$V = L \cdot \frac{di}{dt}$$

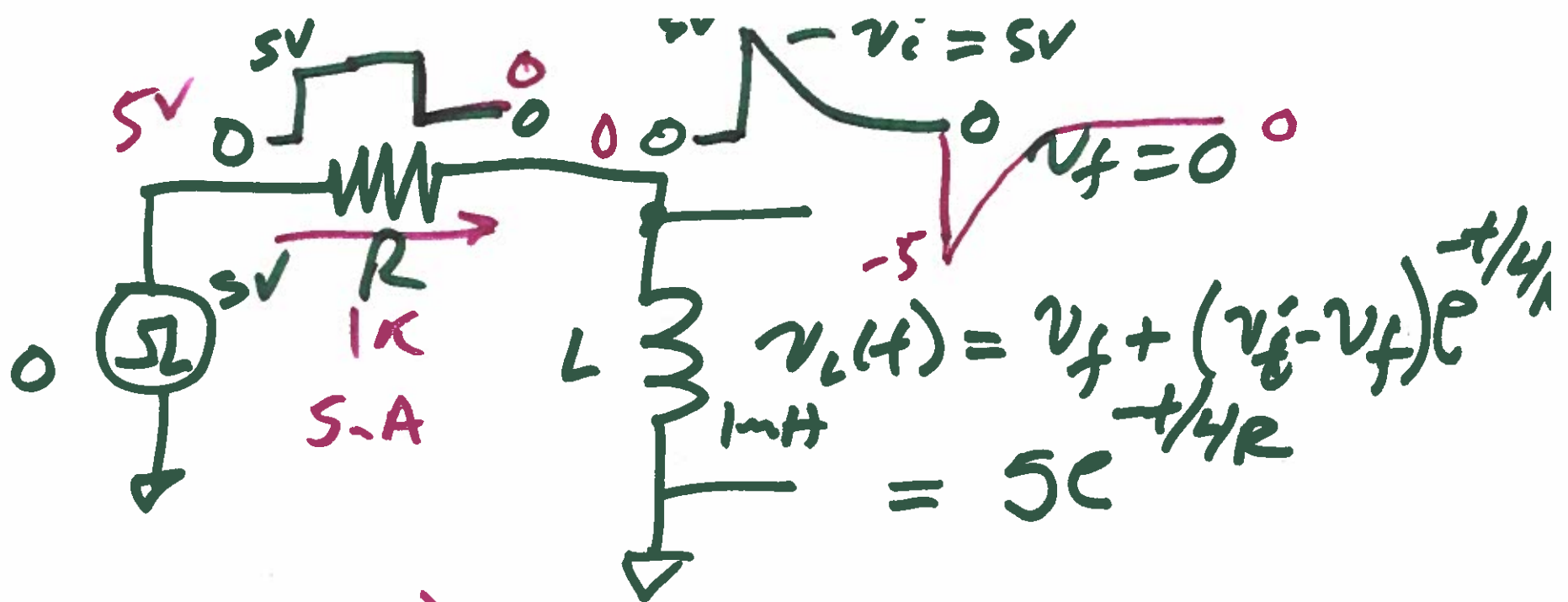
6)



$$V = L \cdot \frac{di}{dt}$$

$$\frac{9V}{1mH} = \frac{9mA}{\mu s} = \frac{9A}{ms}$$

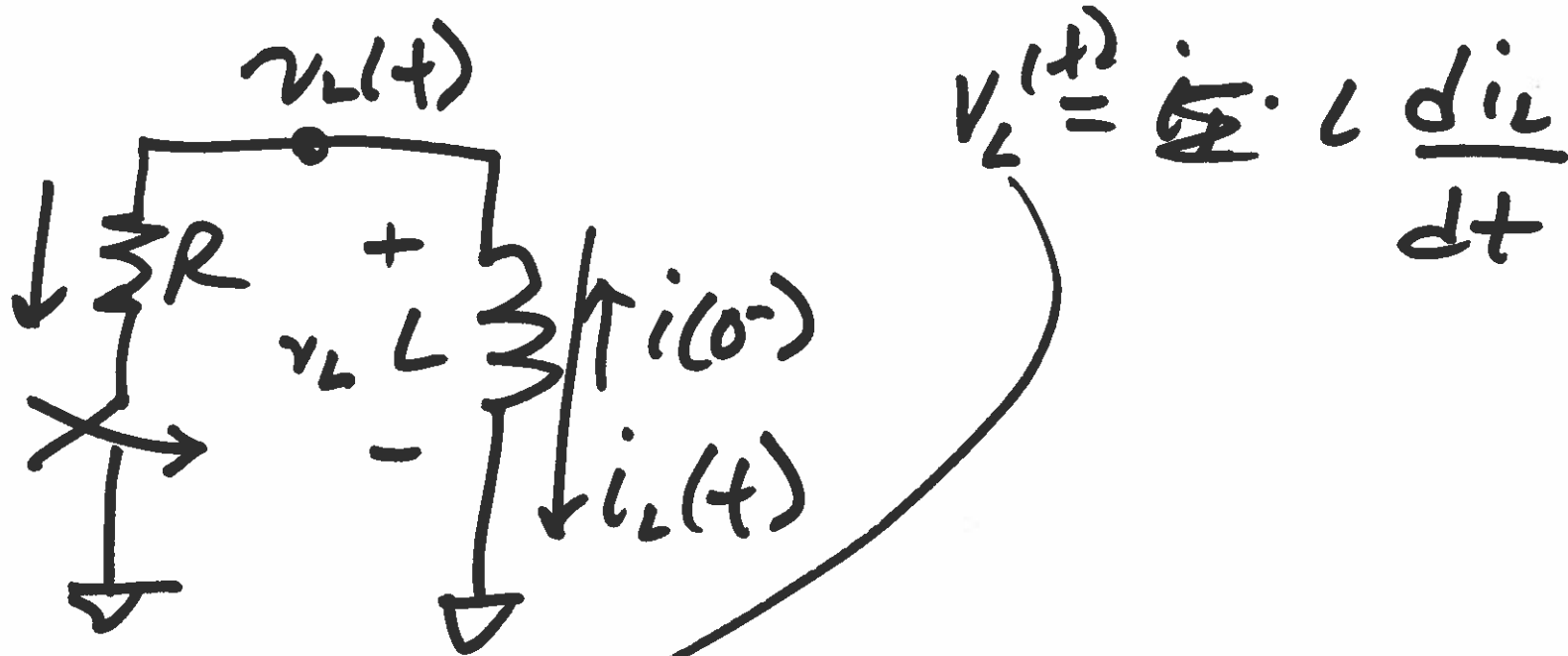
$$V = L \cdot \frac{di}{dt}$$



$$\frac{0 - (-5)}{1k} = 5mA$$

$$\tau = \frac{L}{R} = \frac{1mH}{1k} = 1\mu s$$

8)



$$\frac{v_L(t)}{R} + i_L(t) = 0$$

$$\frac{L}{R} \frac{di_L}{dt} + i_L(t) = 0$$

a)

$$-\frac{L}{R} \frac{di_L}{dt} = i_L$$

$$\int_{i(0)}^{i_L(t)} \frac{di_L}{i_L} = \int_0^t (-1/R) dt$$

$$\ln \frac{i_L}{i(0)} = -t/(LR)$$

$$i_L(t) = i(0) e^{-t/(LR)}$$

$$v_L(t) = R \cdot i_L(t)$$

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