

H.W. # 6 - Spring 2015 EE 320 Engineering Electronics I

Show your work for credit.

1. Suppose an op-amp is used to implement a gain = +10 amplifier. If the op-amp has a unity gain frequency of 10 MHz sketch the magnitude response of the amplifier.
2. Suppose the op-amp in problem 1 has a dc offset of 10 mV show how to calculate the DC output voltage of the amplifier.
3. What are the input resistances of inverting and non-inverting amplifiers? Use simulations to verify your answers.