

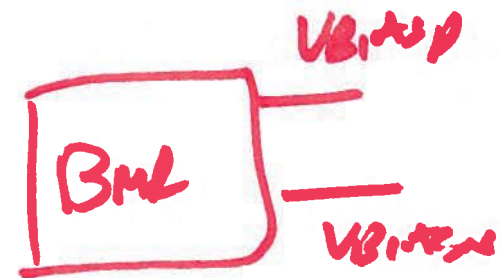
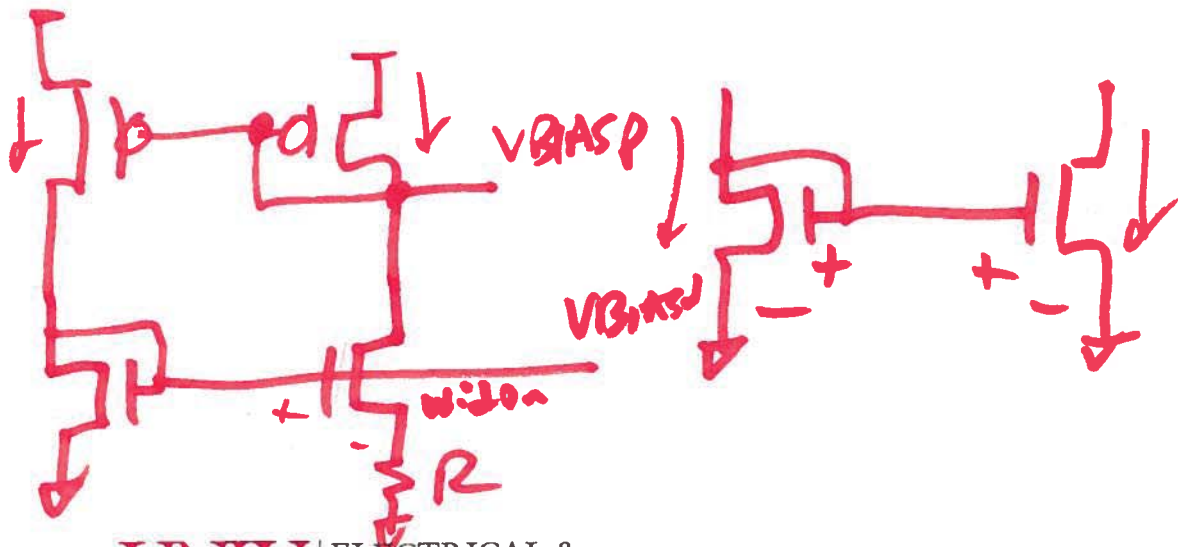
EE 420 / ECE 620

Analog IC Design

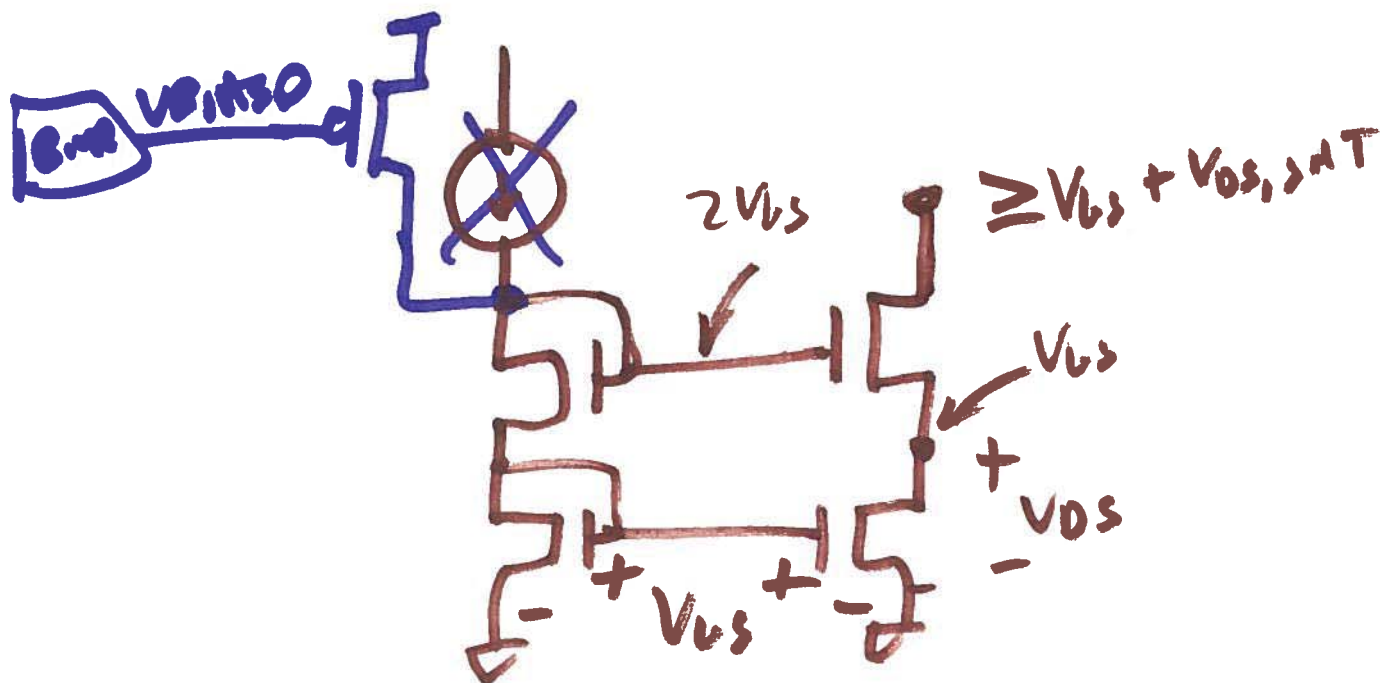
Lecture 11

Feb. 27, 2017

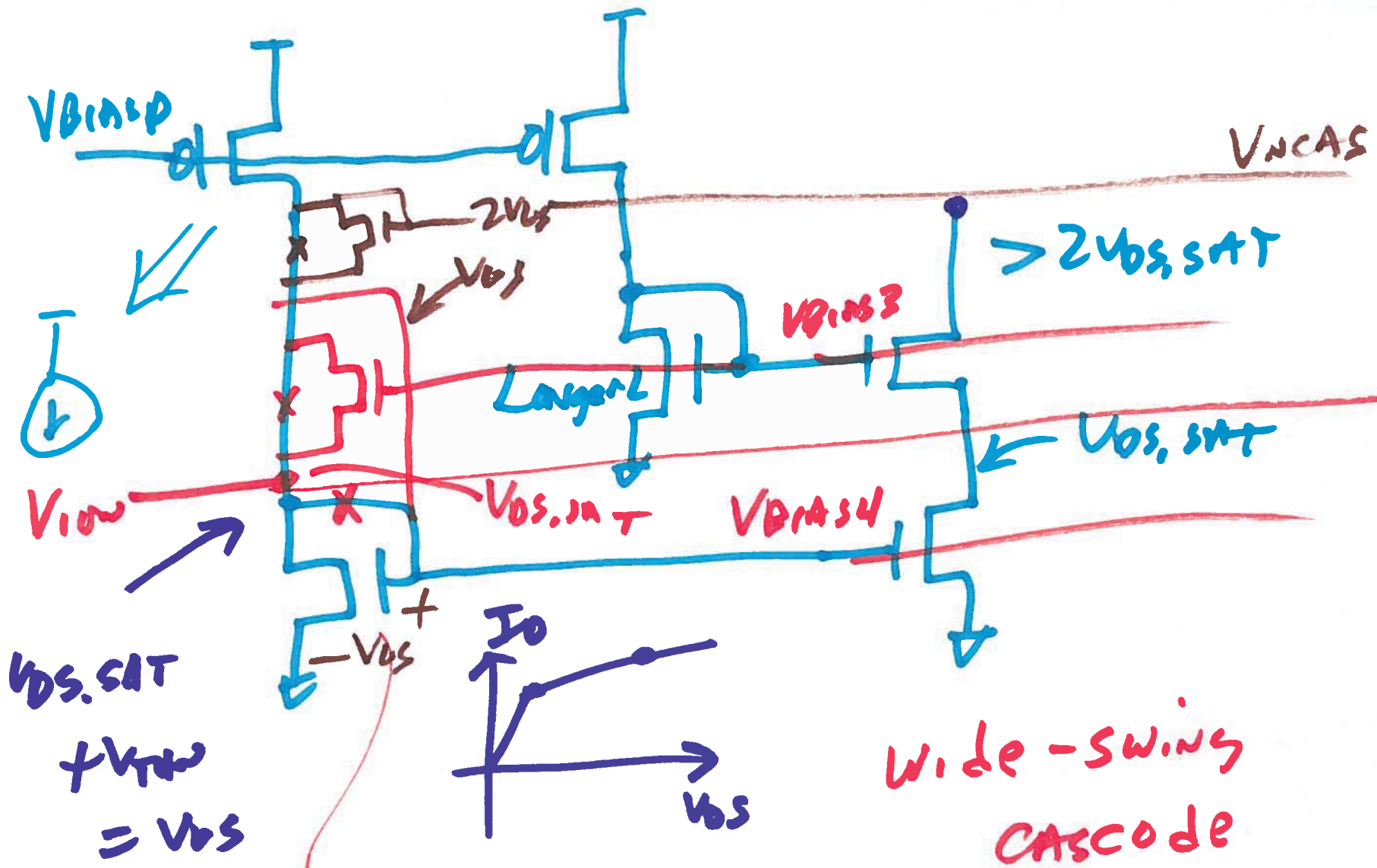
Basic current mirror



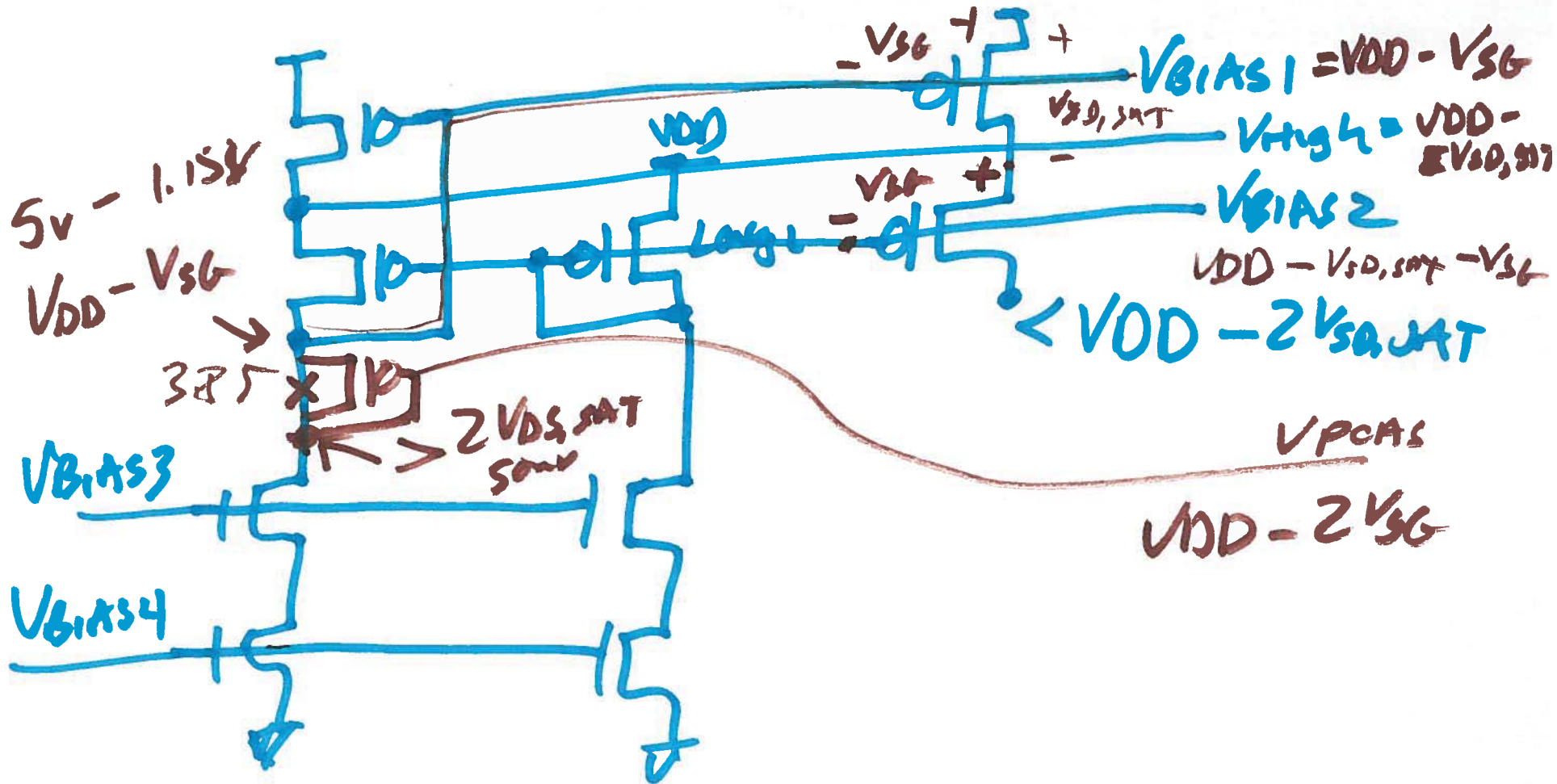
# CASCODE CURRENT MIRROR



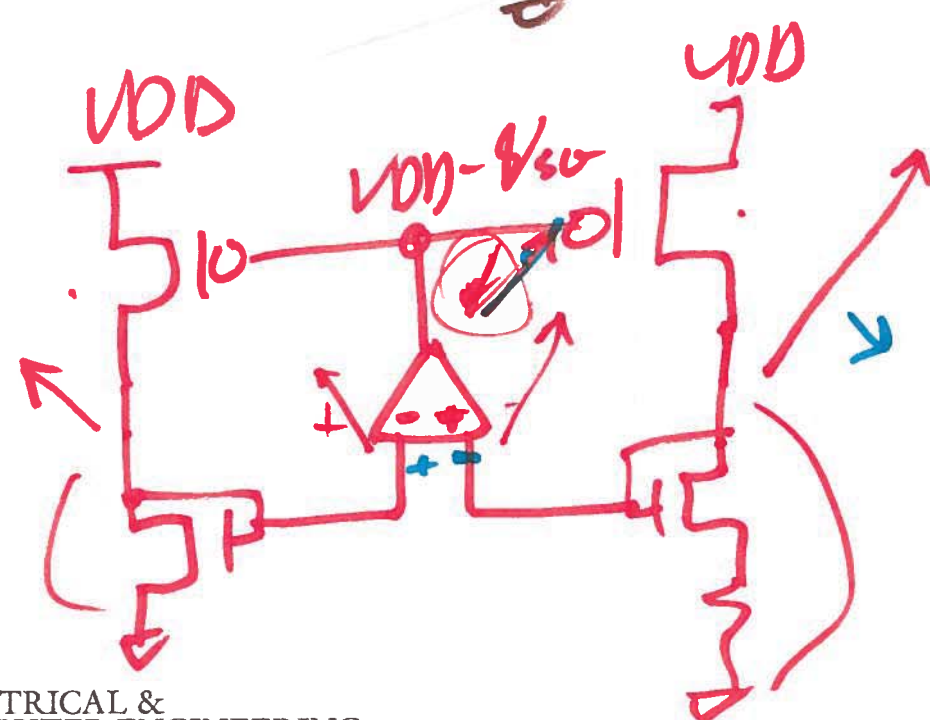
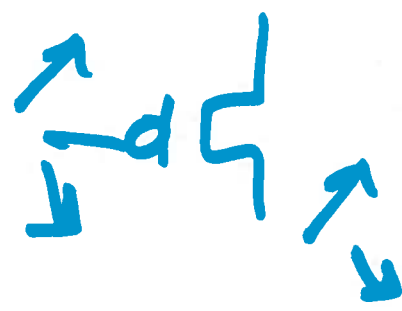
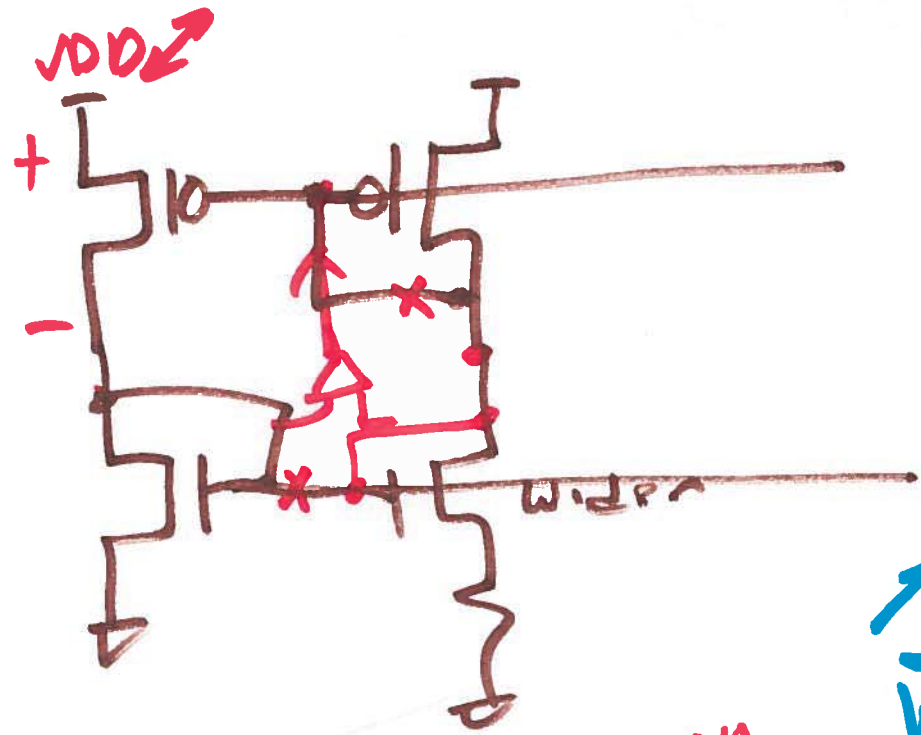
2)



3)



$$V_{SG} = V_{SD,SAT} + V_{THP}$$



5)