

EE 421L

LAB PROJECTS

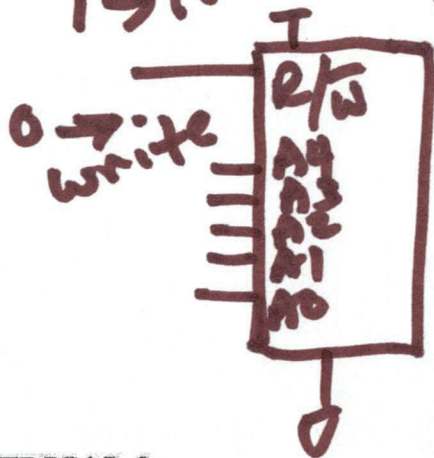
October 6, 2021

R-wi

32 words \rightarrow word \rightarrow 8bits

5-bit address

1 \rightarrow read A4, A3, A2, A1, A0

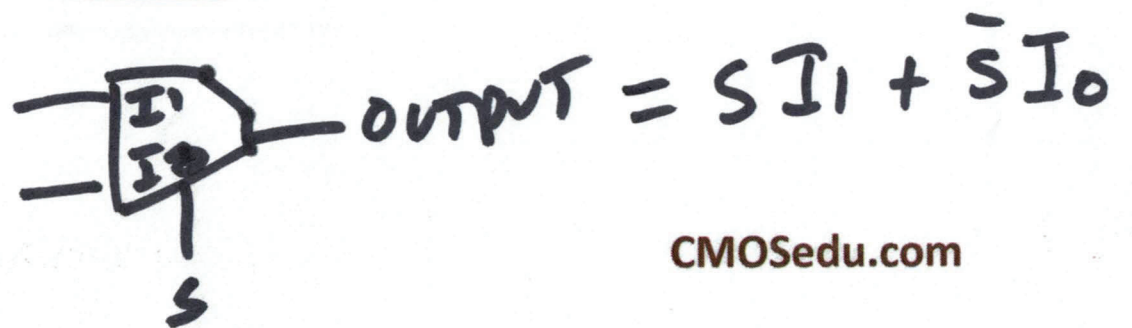
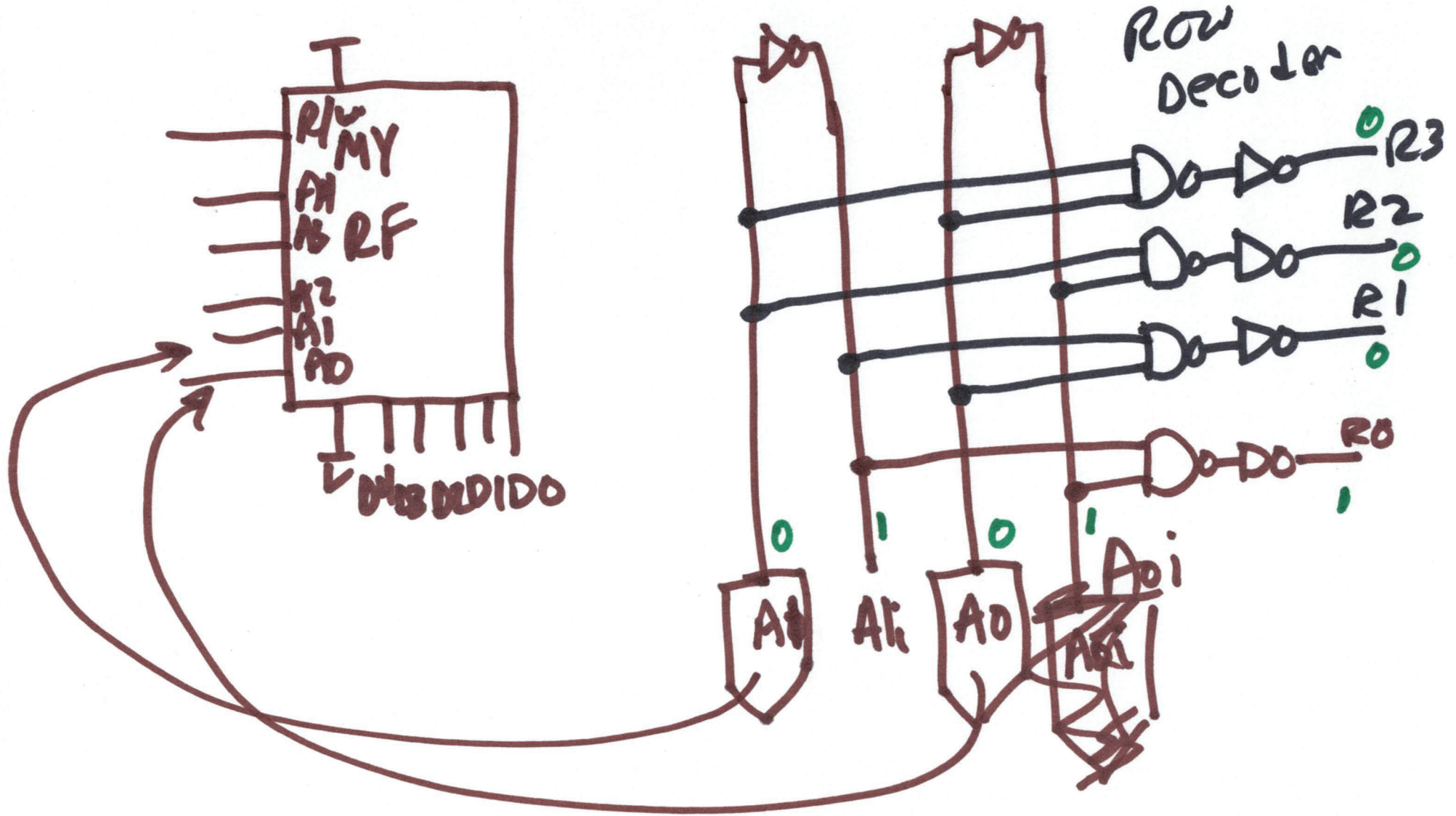


word 31

word 1

word 0

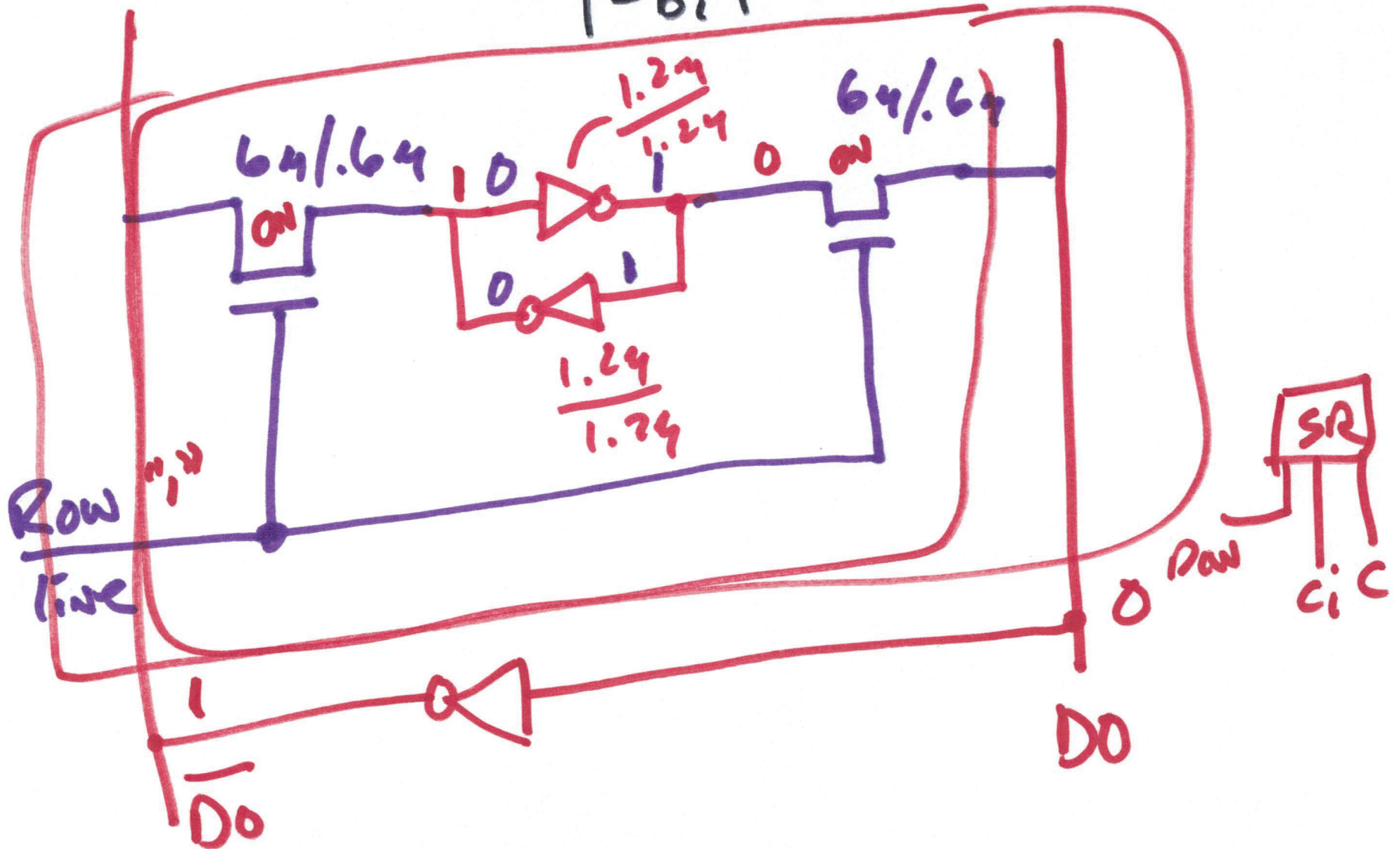
1)



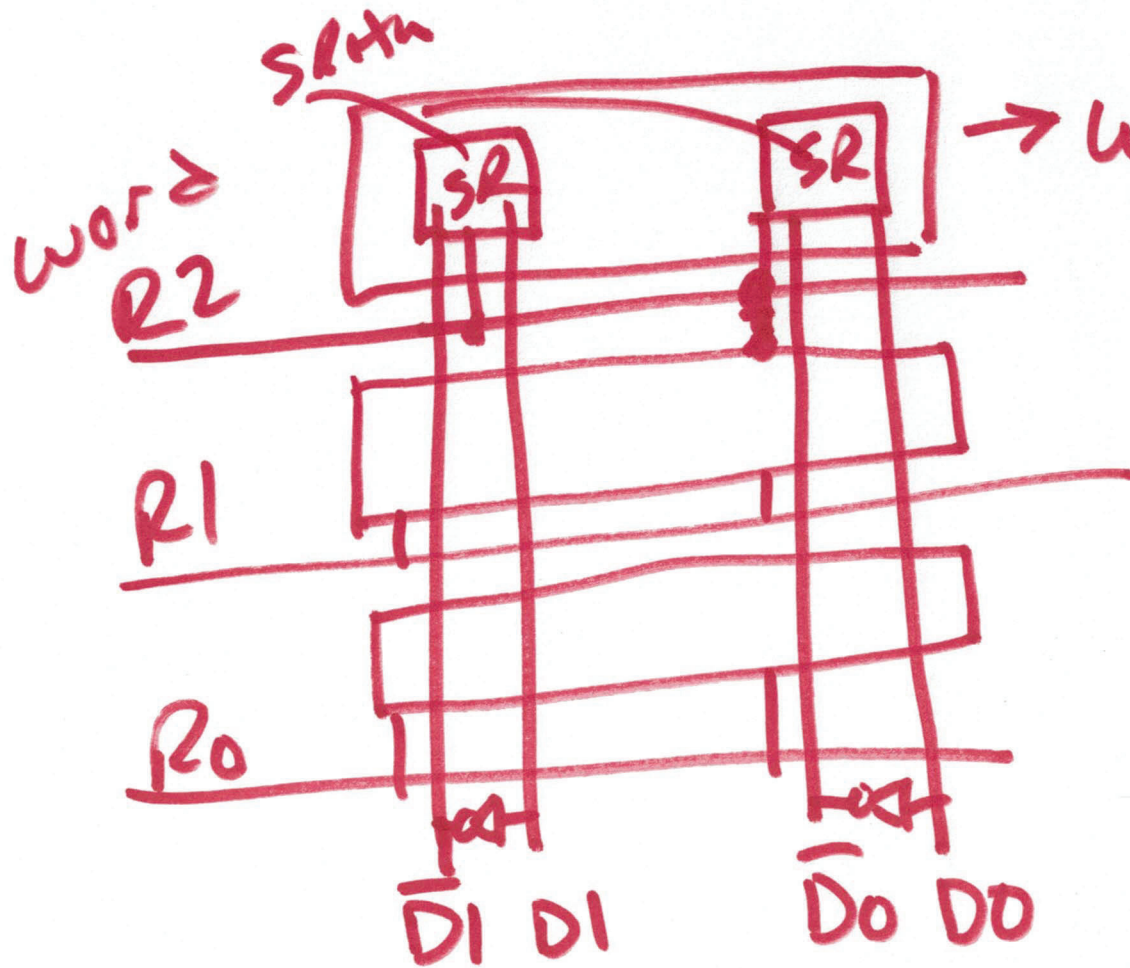
2)

Static RAM 6T (SRAM)

1-bit



3)



→ word has 8 SRs
in it
8-bit-word-sram

ARRAY → 32 words

ARRAY-32-words
-sram

4)

