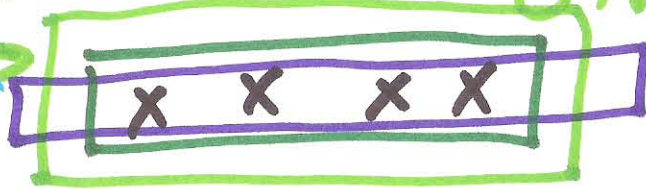


EE 421 / EGG 621

Digital IC Design

Review Layers

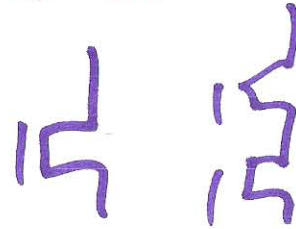
N-select GIVE EXAMPLES



ϕ_n ϕ_n

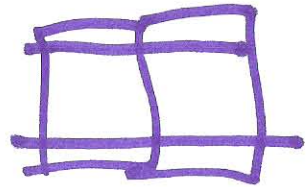
ACTIVE

N-well



p-select

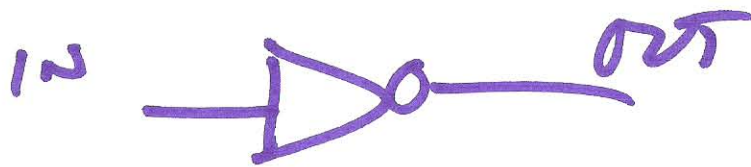
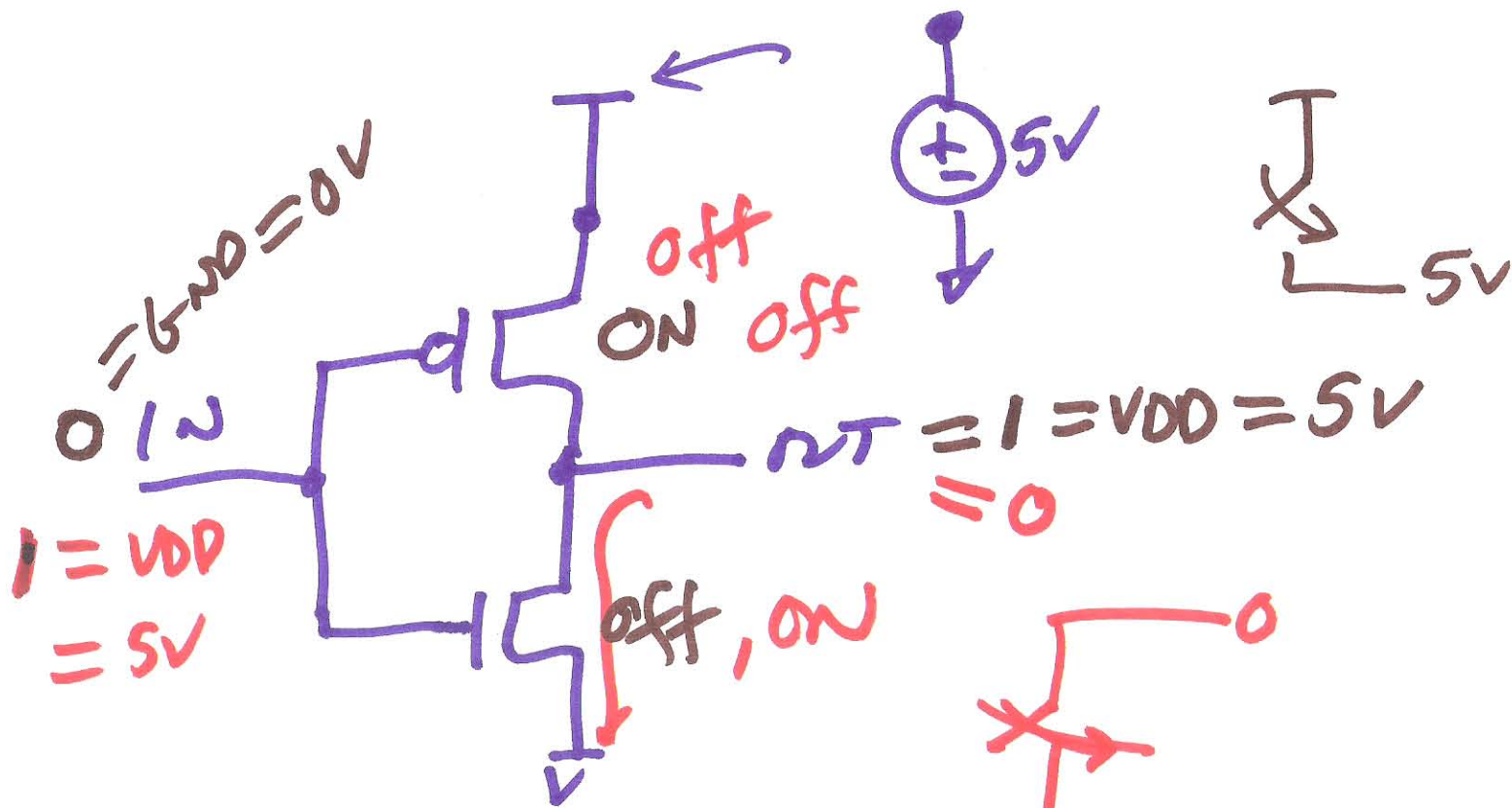
ACTIVE



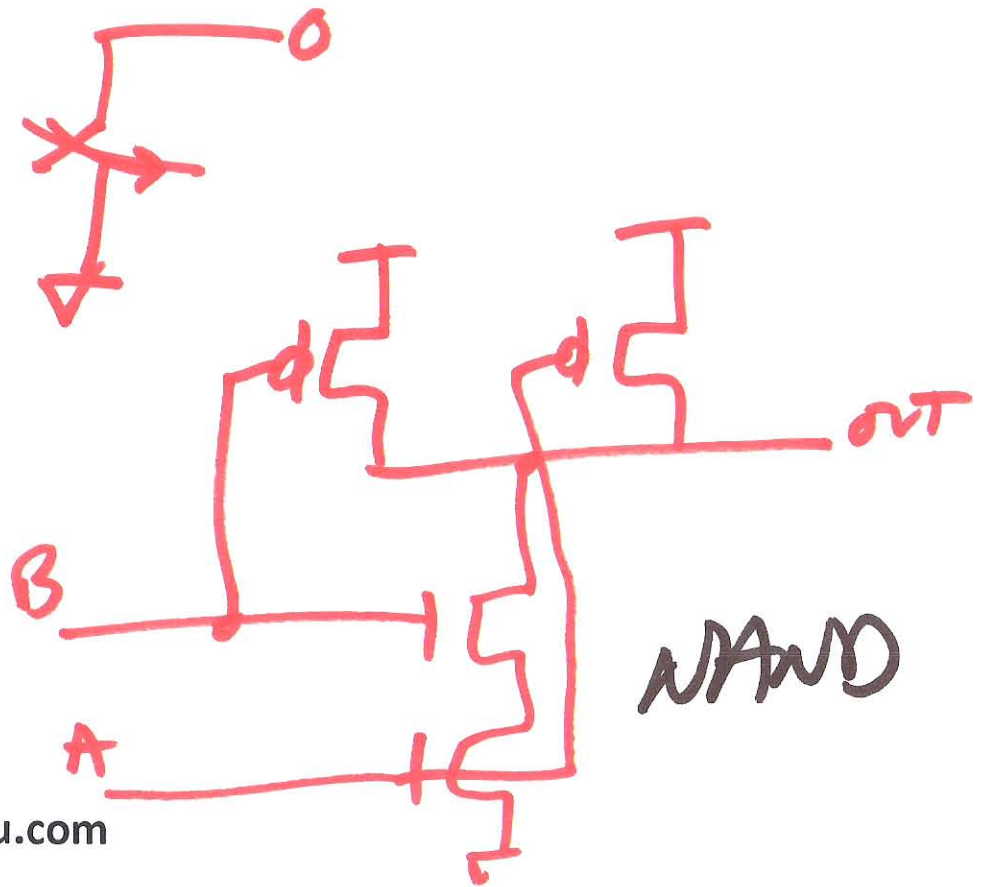
CMOSedu.com

DO

1)



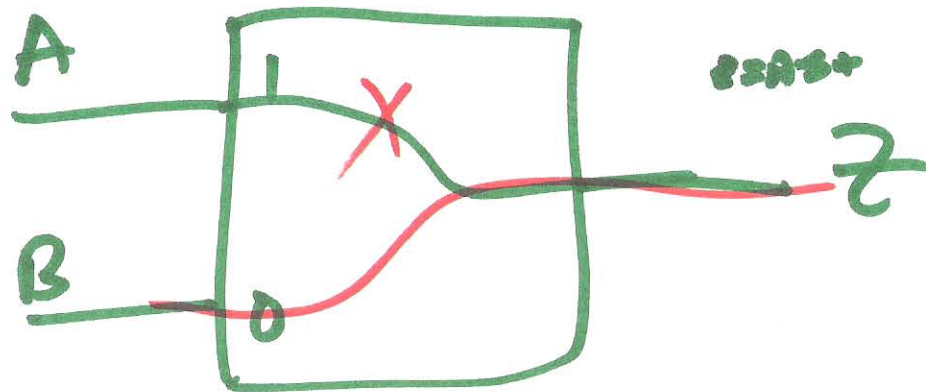
A	B	OUT
0	0	1
0	1	1
1	0	1
1	1	0



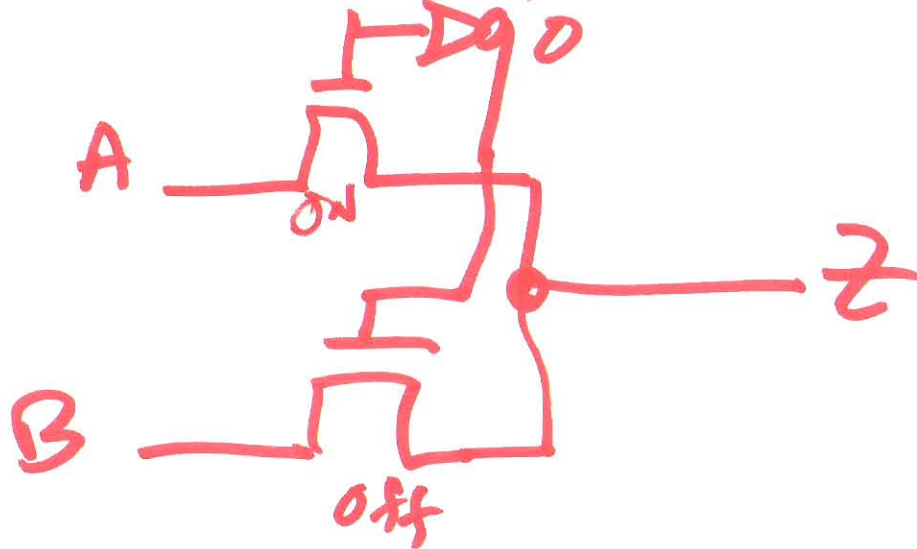
2)

$$z = S \cdot A + \bar{S} \cdot B$$

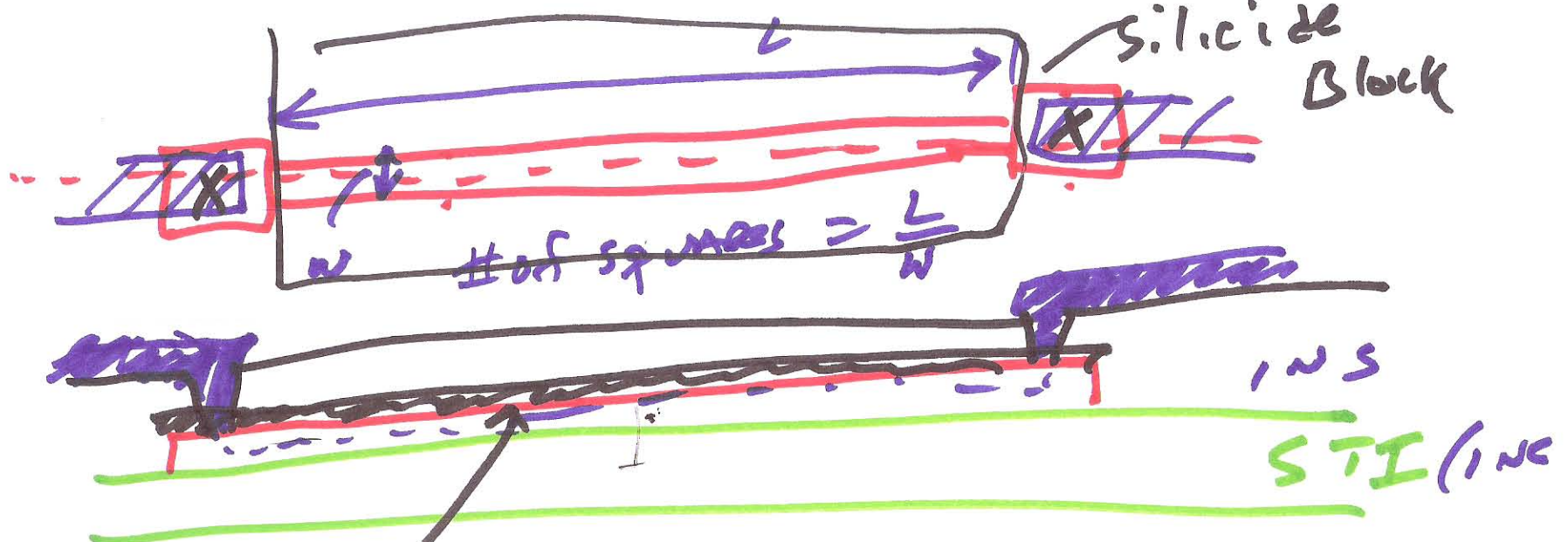
$s = 1$



$s = 1$



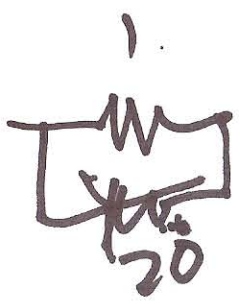
3)



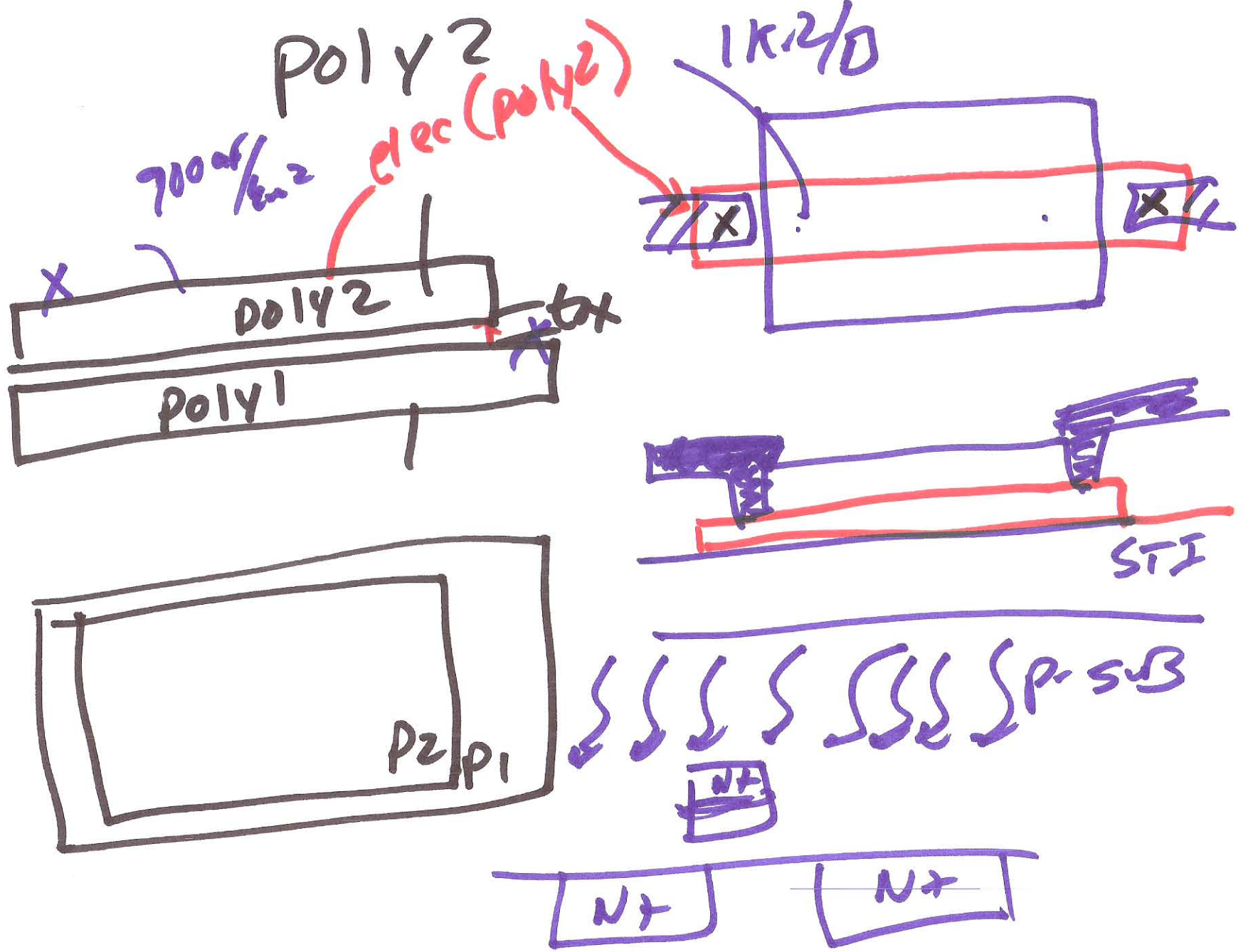
$$R_D = \frac{20 \Omega}{\square} \quad \rho_{SUB}$$

Silicide
(Si - refractory Tungsten)

$$R_D = \frac{1 \Omega}{\square}$$



4)



5)