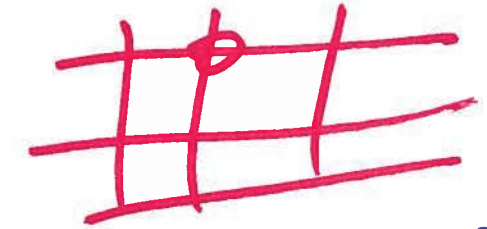
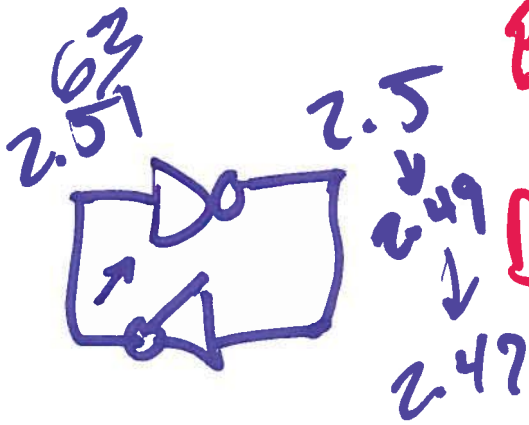


EE 421 / ECG 621

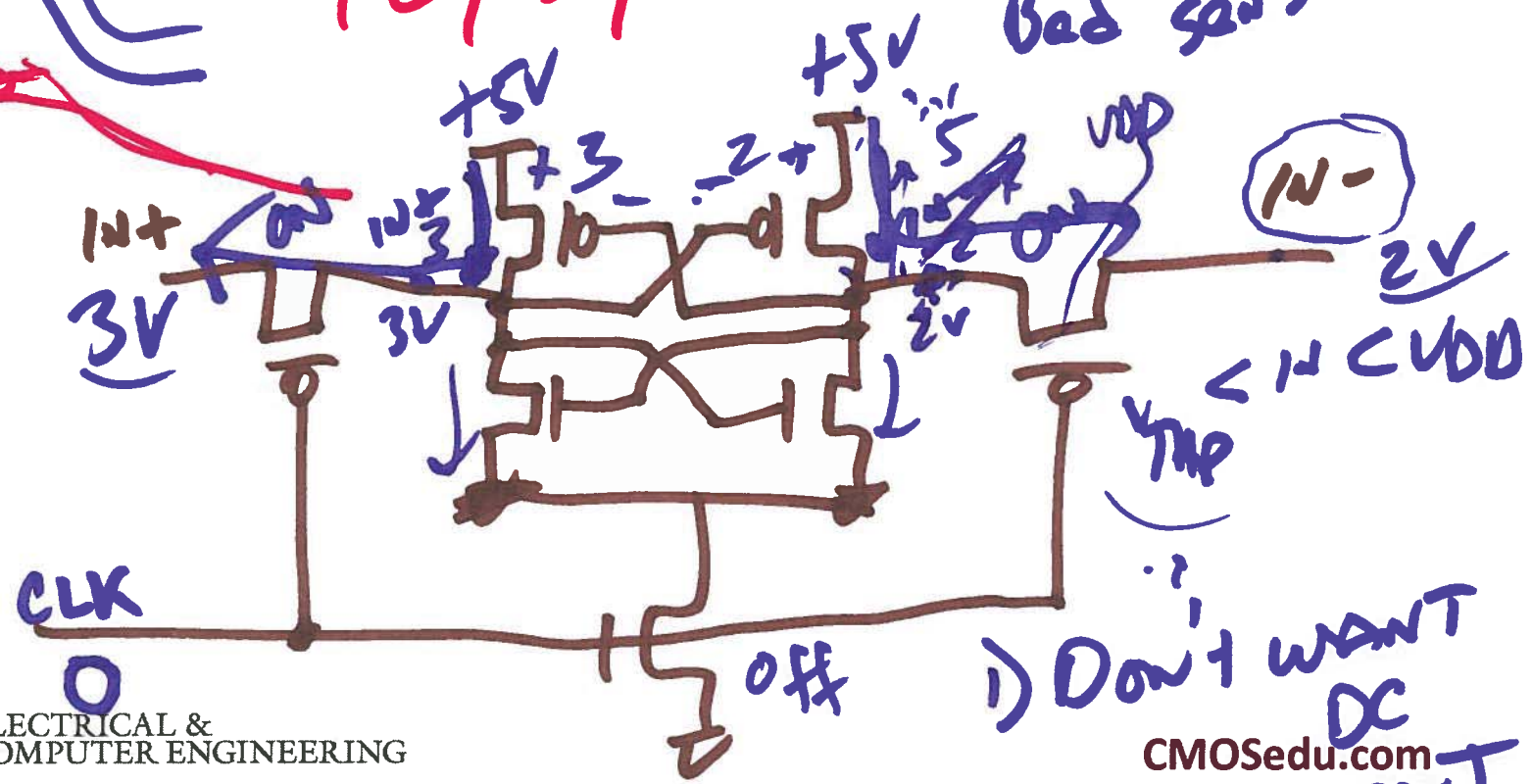
Digital IC Design

Lecture 25

12/5/2016



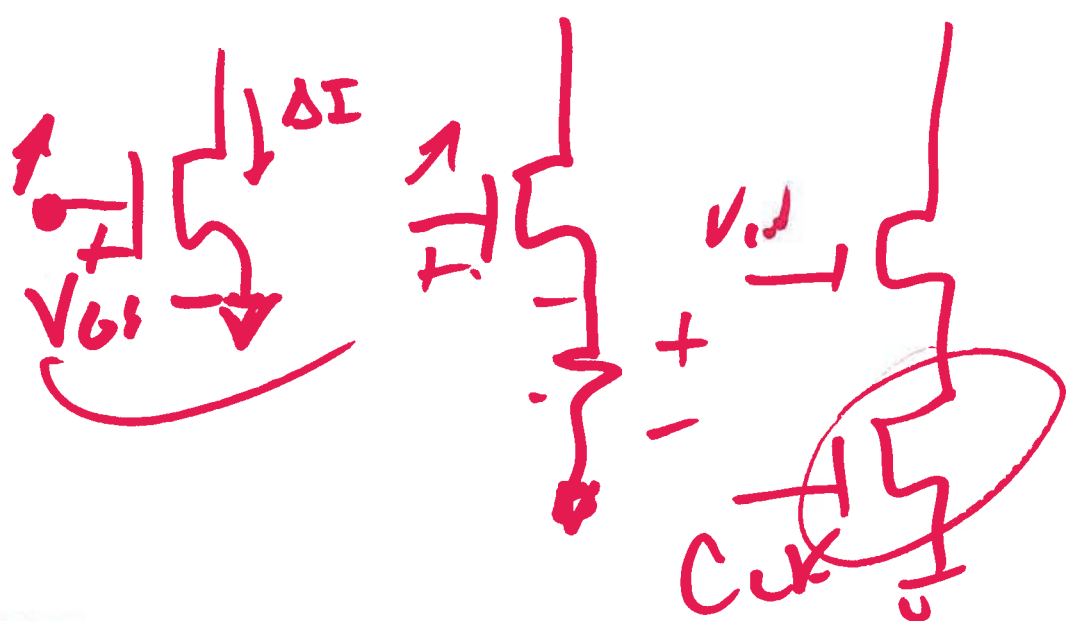
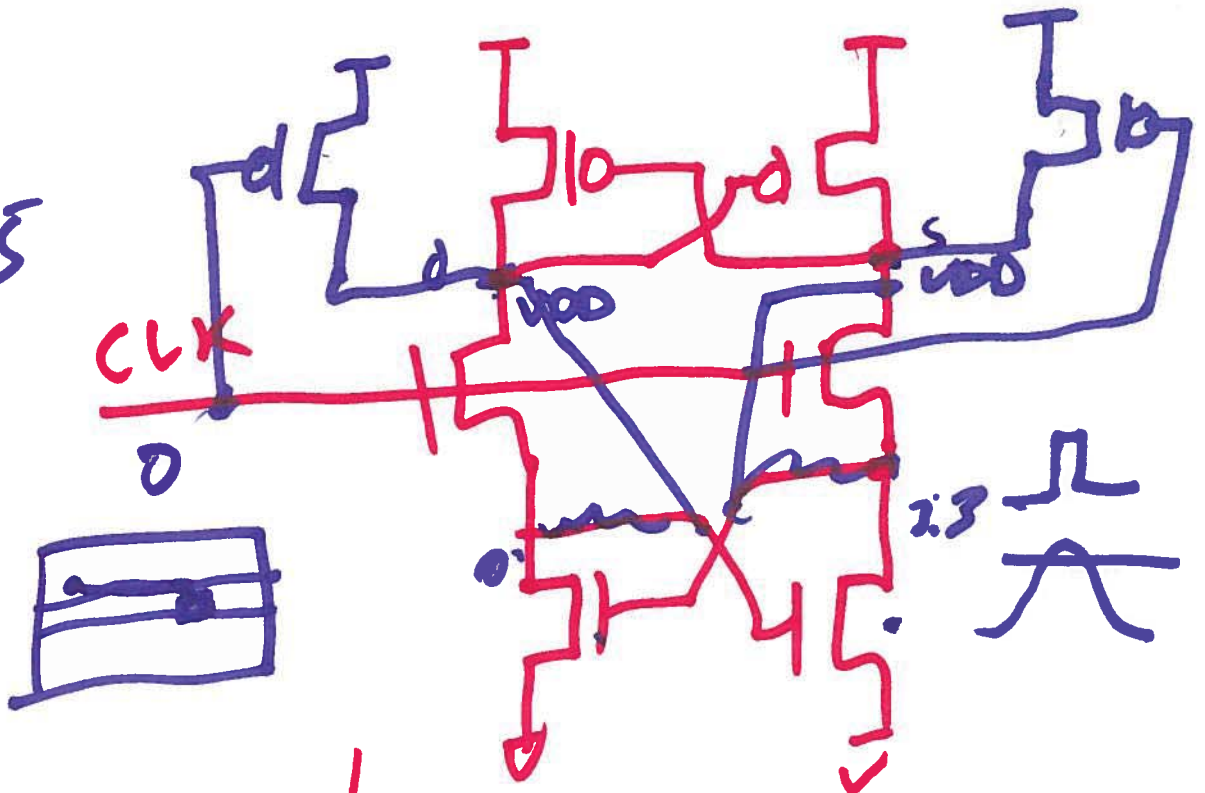
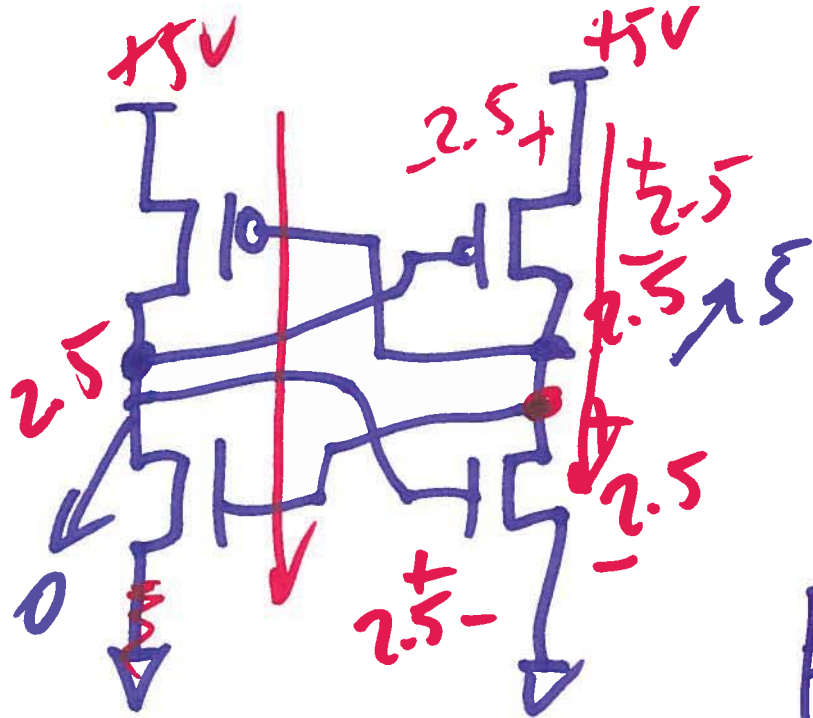
Bad sense amp



$V_{TAP} < 1/2 V_{DD}$

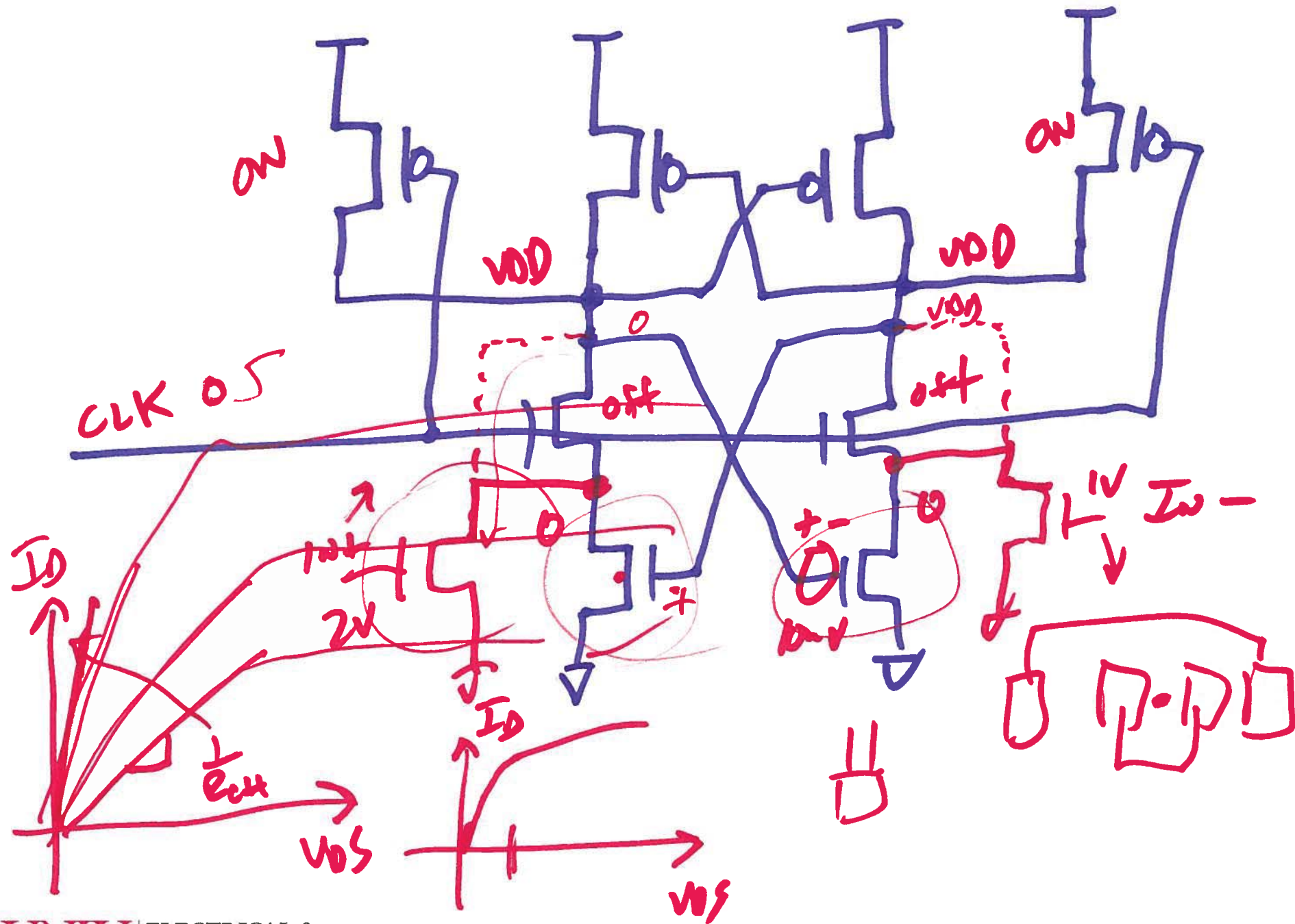
1) Don't want DC current

1)



Remove sense
And memory





3)