

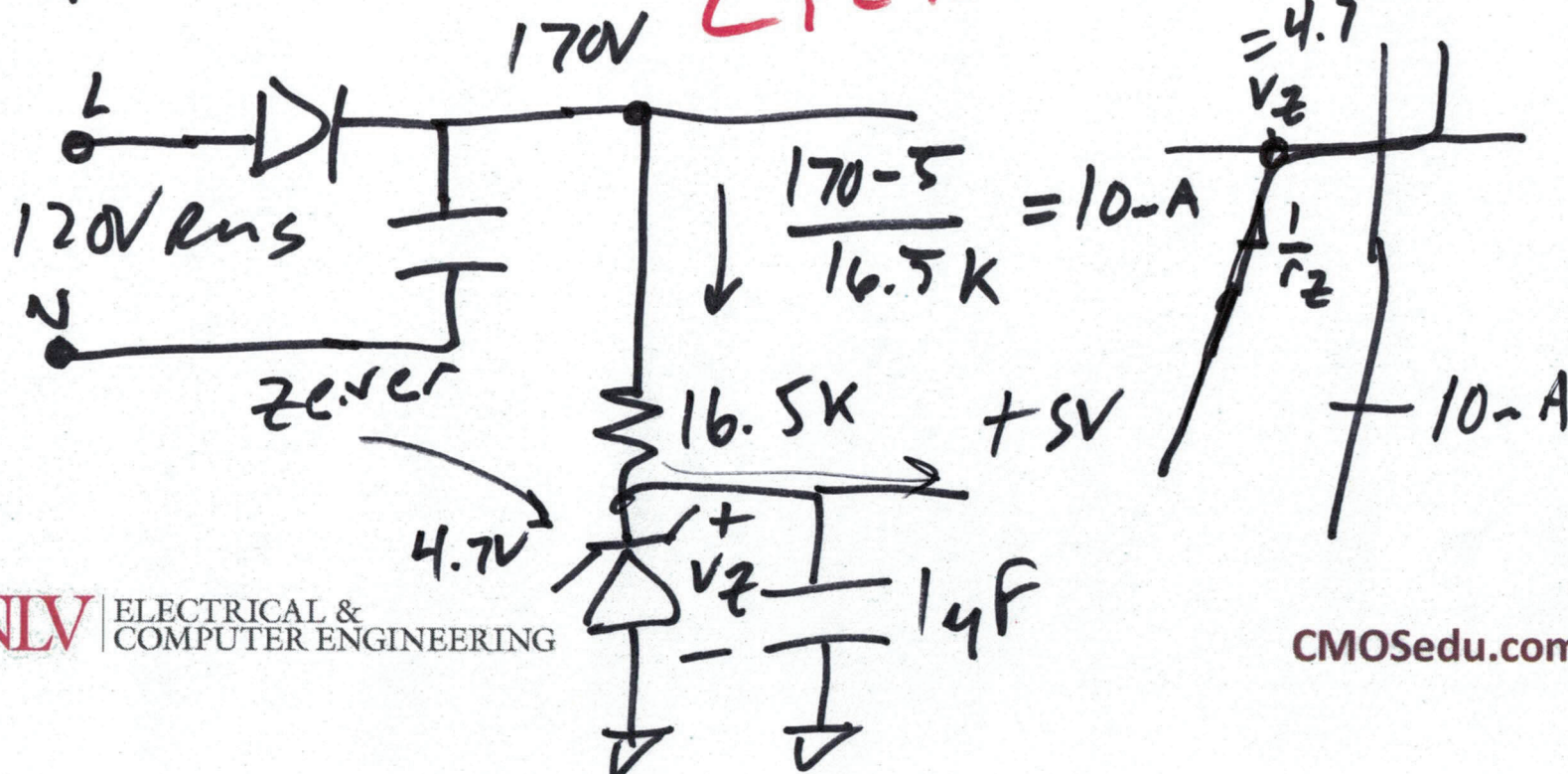
EE 421 IELG 621

# Digital IC Design

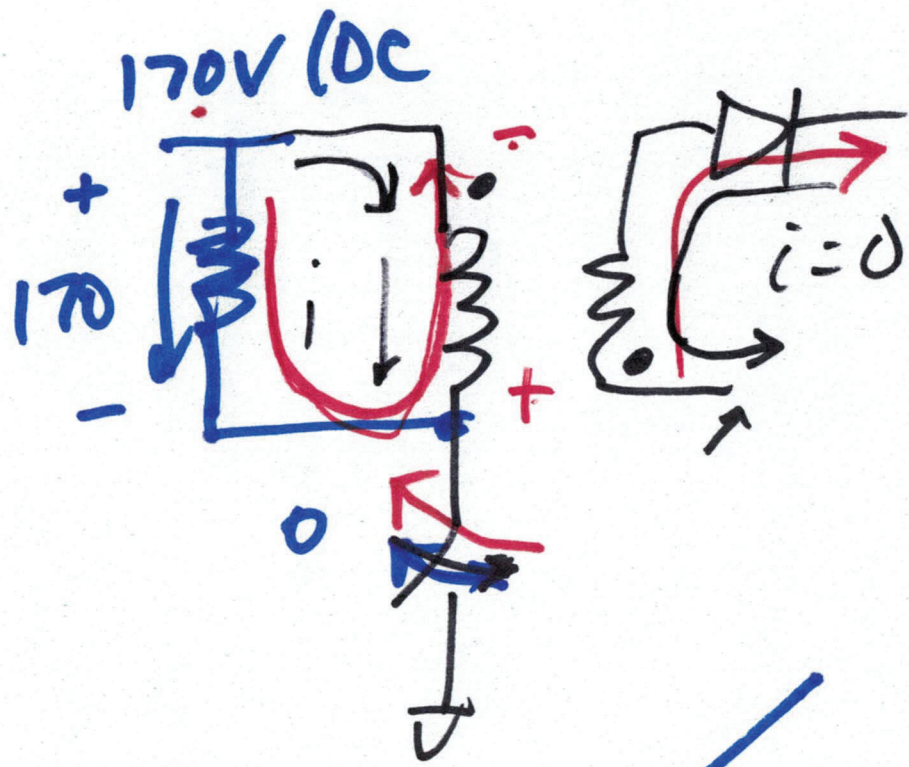
NOV. 30, 2020

$$V_p = V_{rms} \cdot \sqrt{2}$$
$$V_p = 170V$$

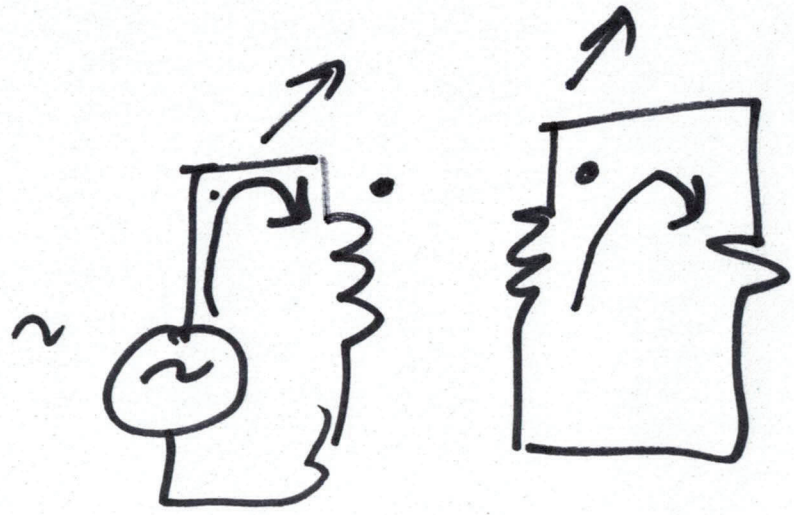
Lecture 26



1)



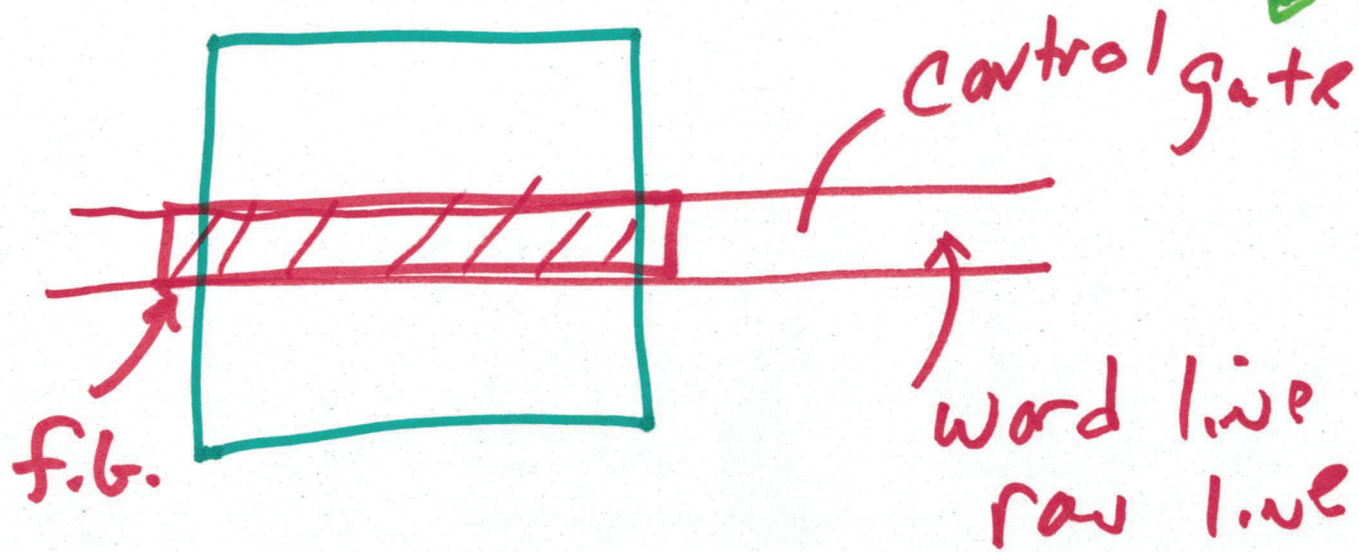
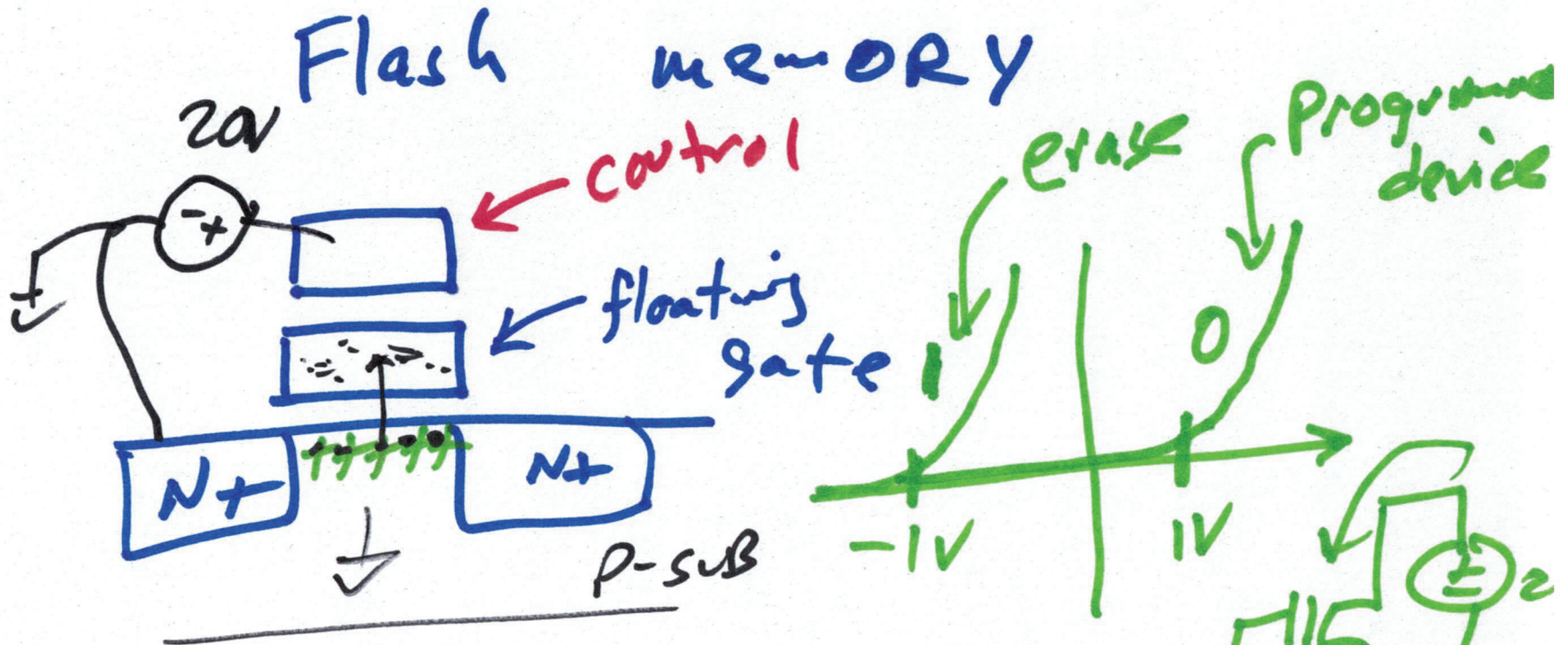
$$V = L \frac{di}{dt}$$



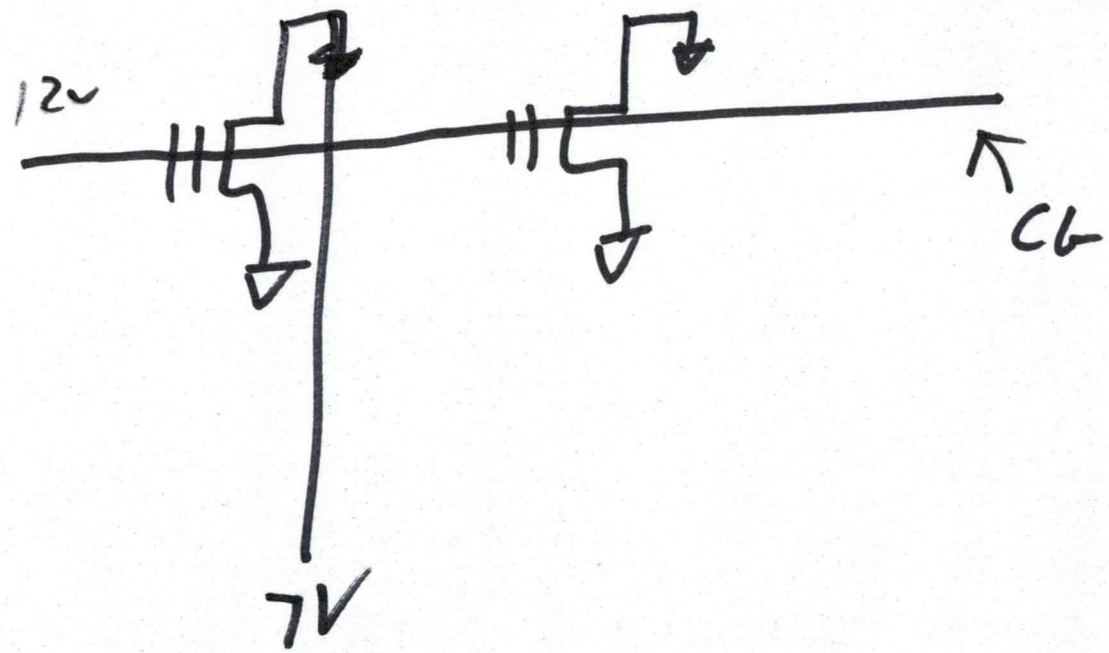
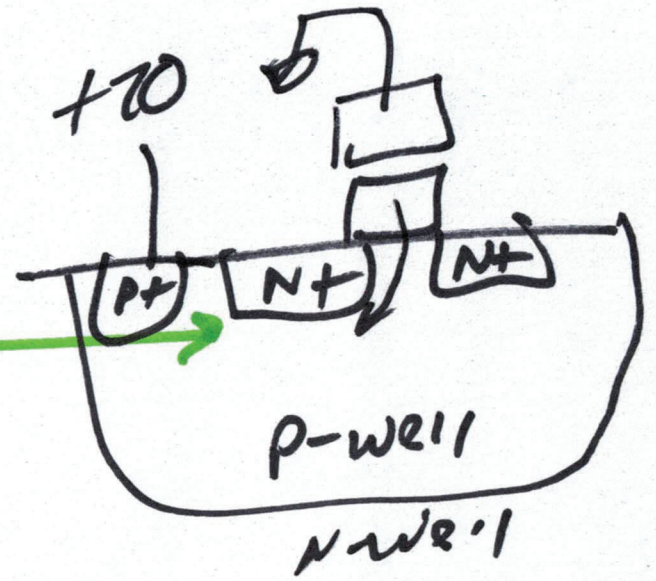
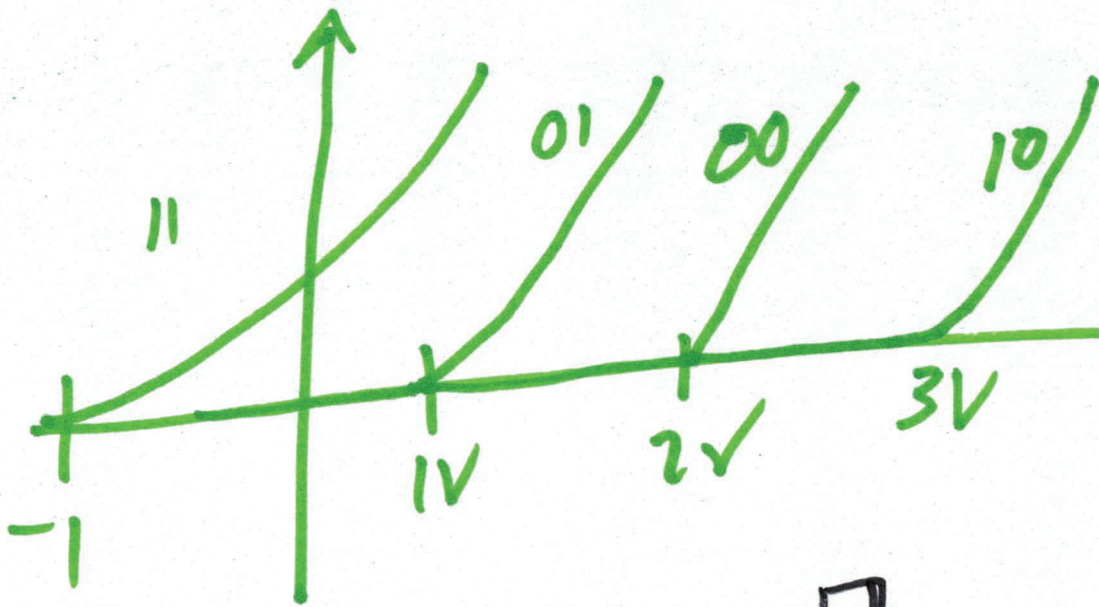
$$5 (I_{avg}(s)) \cdot 170 \cdot \text{AVG}(I(170V)) = P_{in}$$

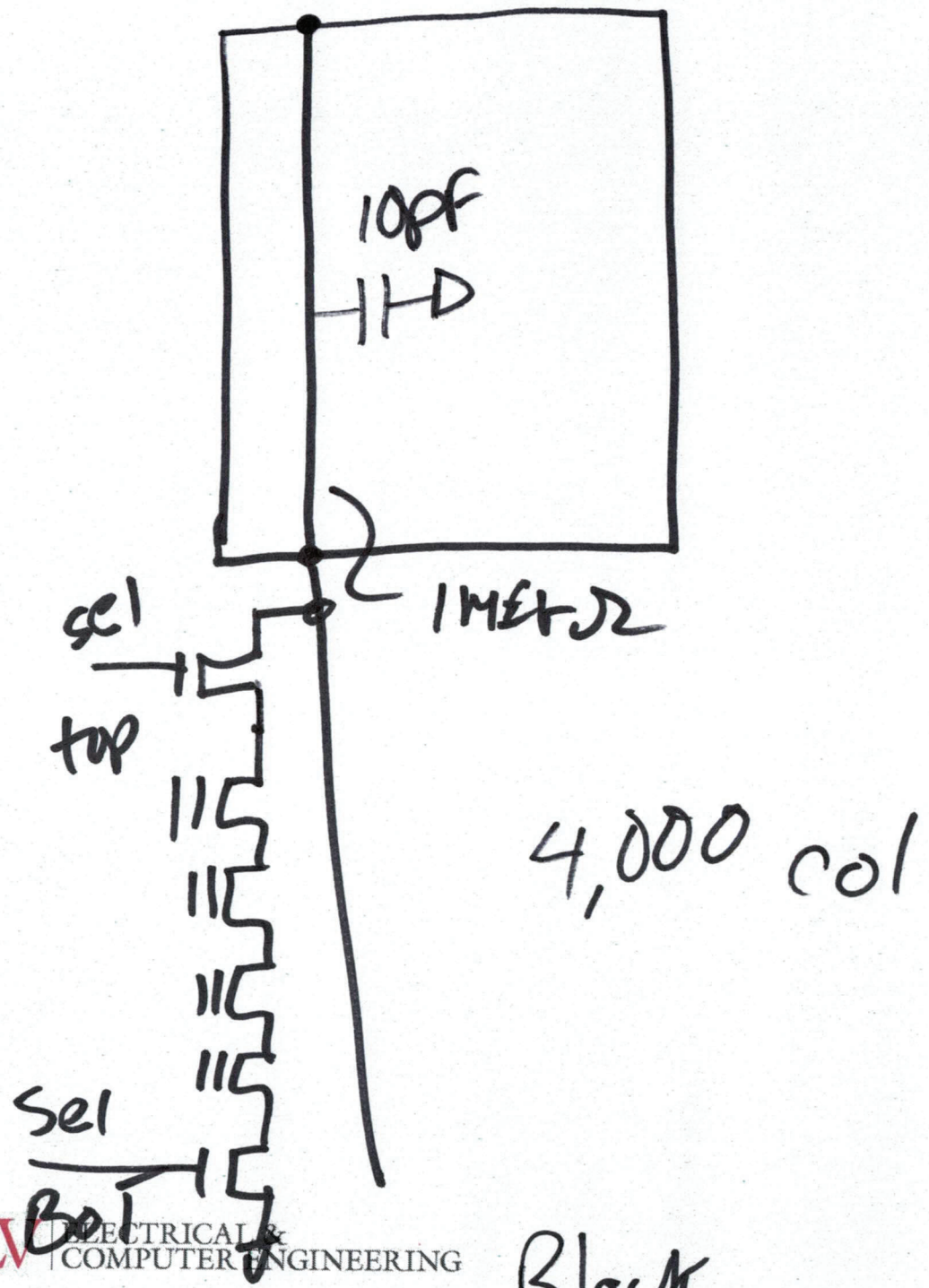
$$P_{out} = 12.5 I_{avg}(12.5)$$

2)



3)



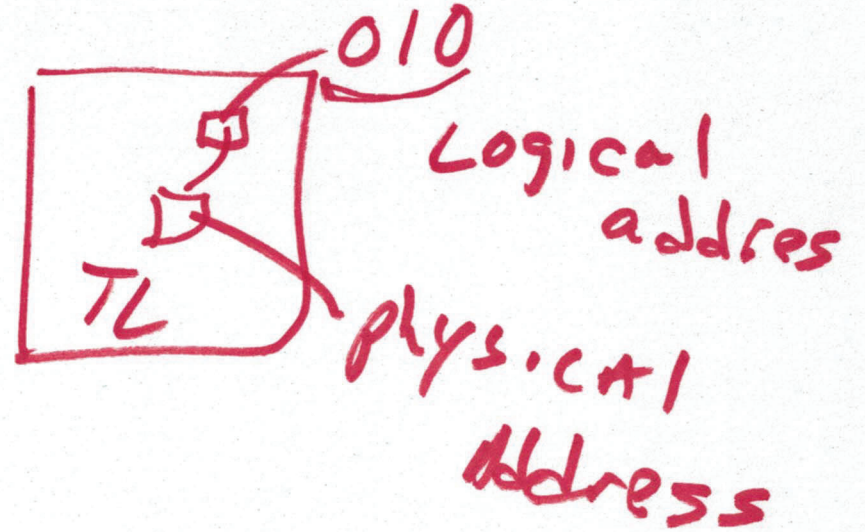
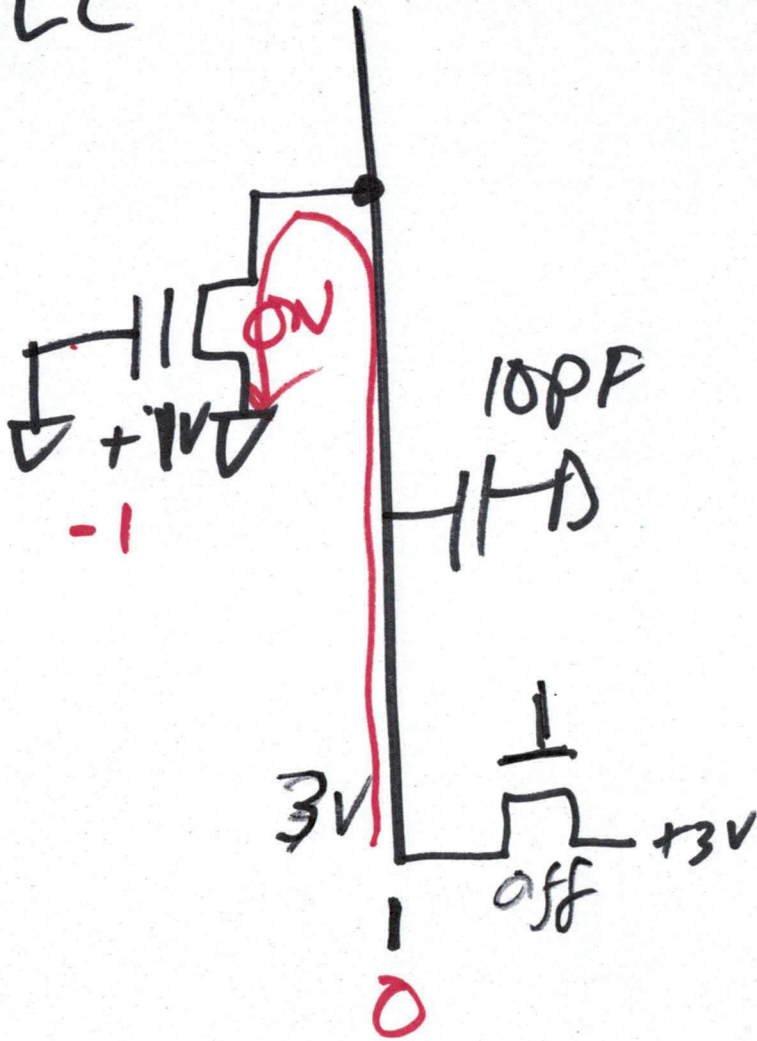


5)

Block

# Sensing in flash

SLC



6)