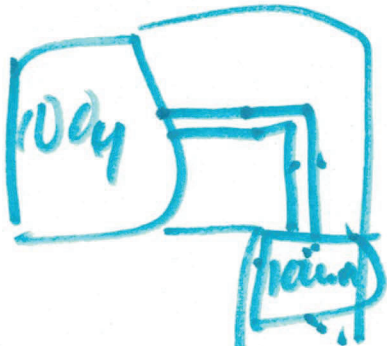


EE 421 / ECG 621

# Digital IC Design

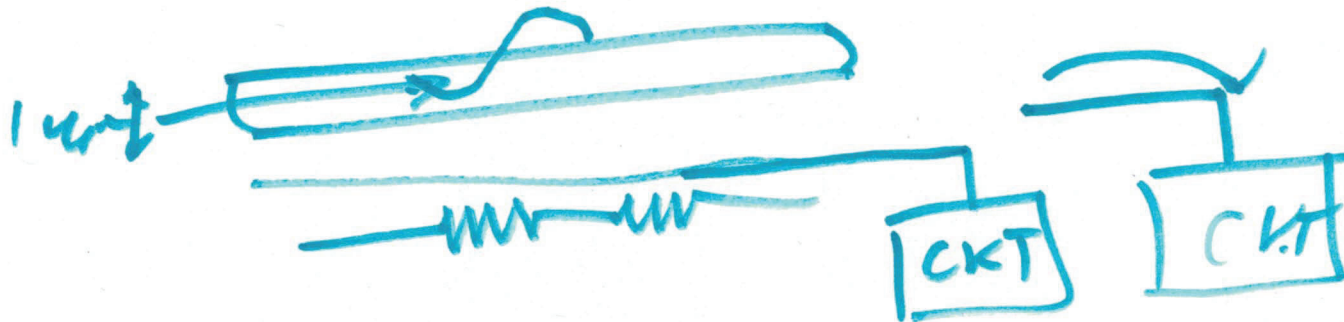
Sept. 16, 2020

## Lecture 7



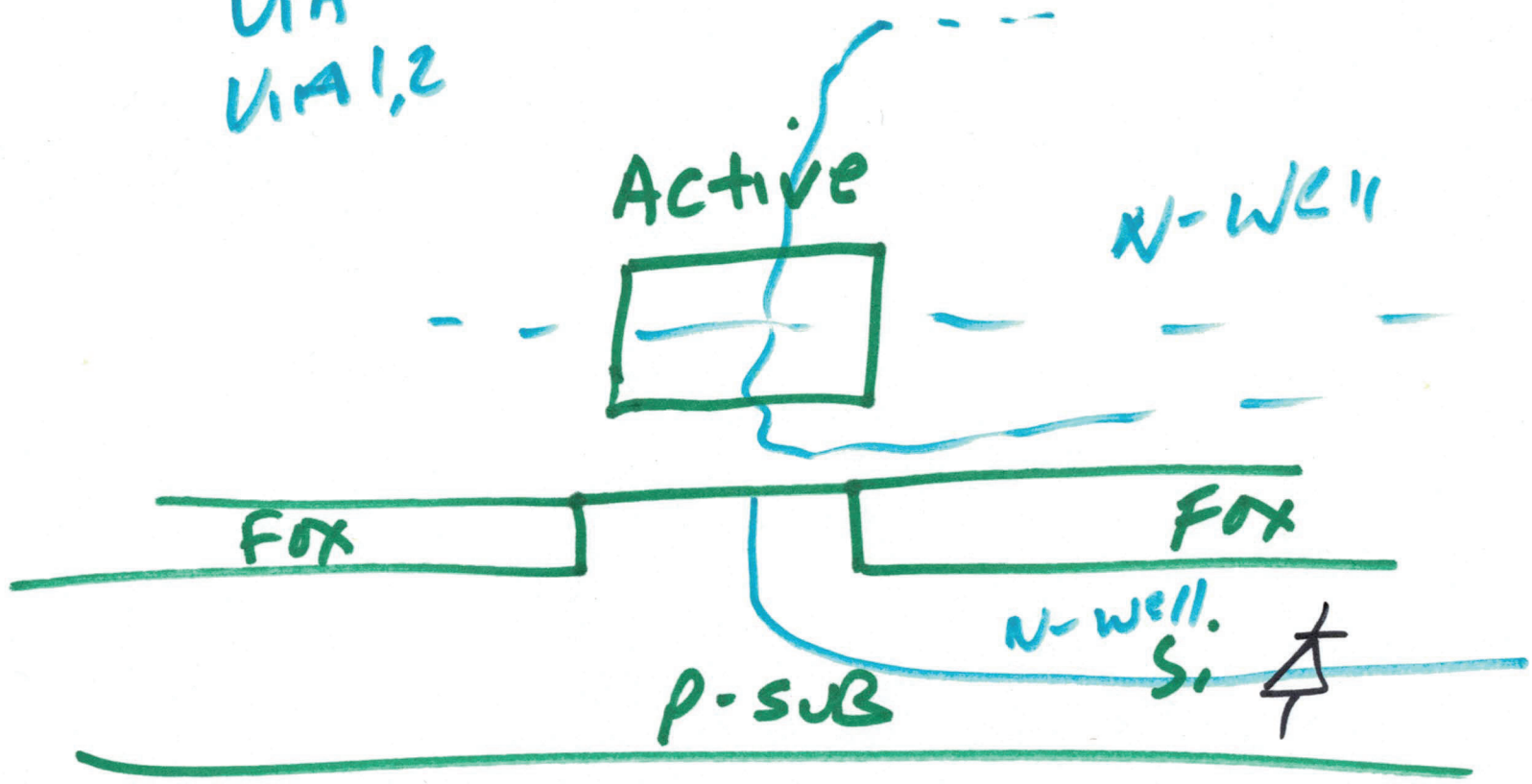
electromigration

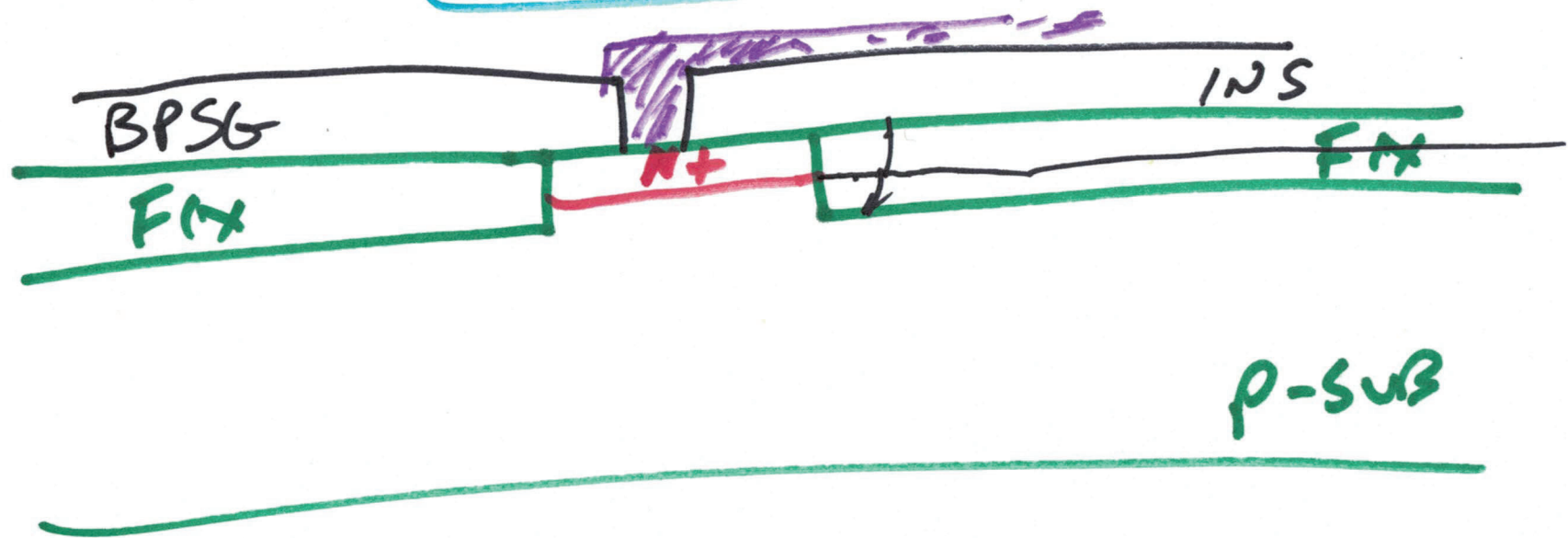
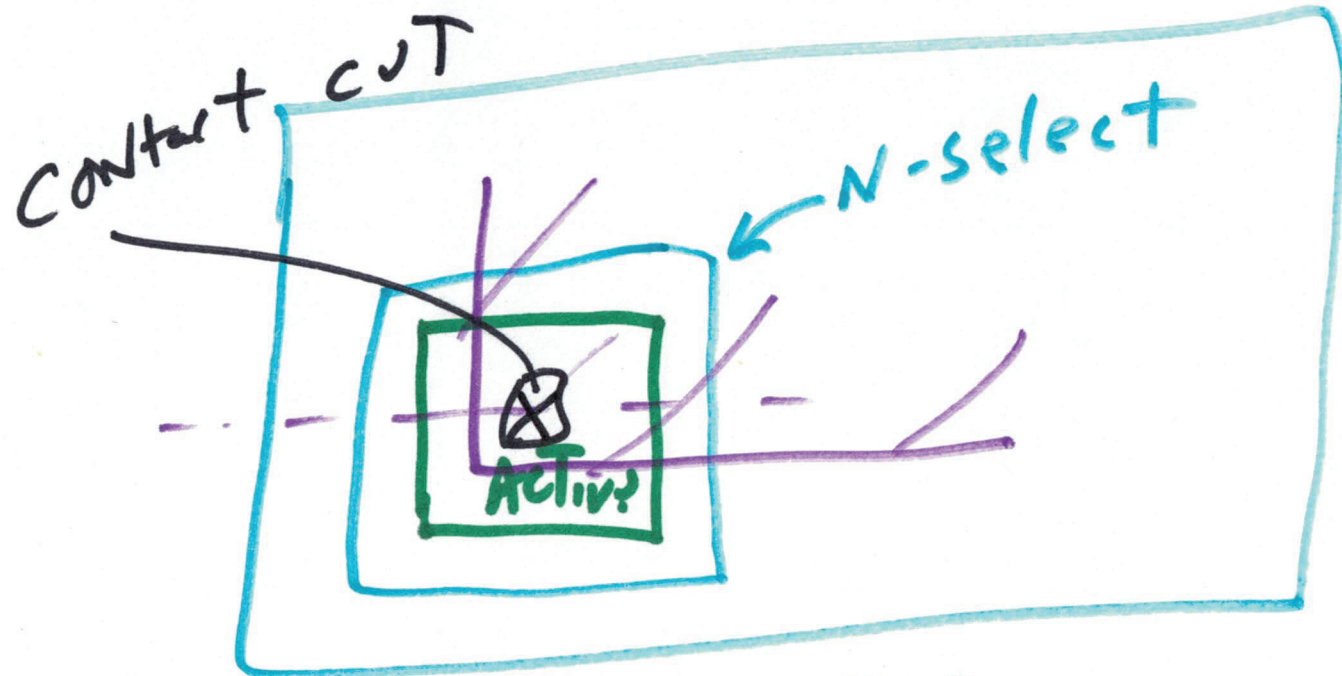
$$J_{a1} = I - A/4m$$



# Layers

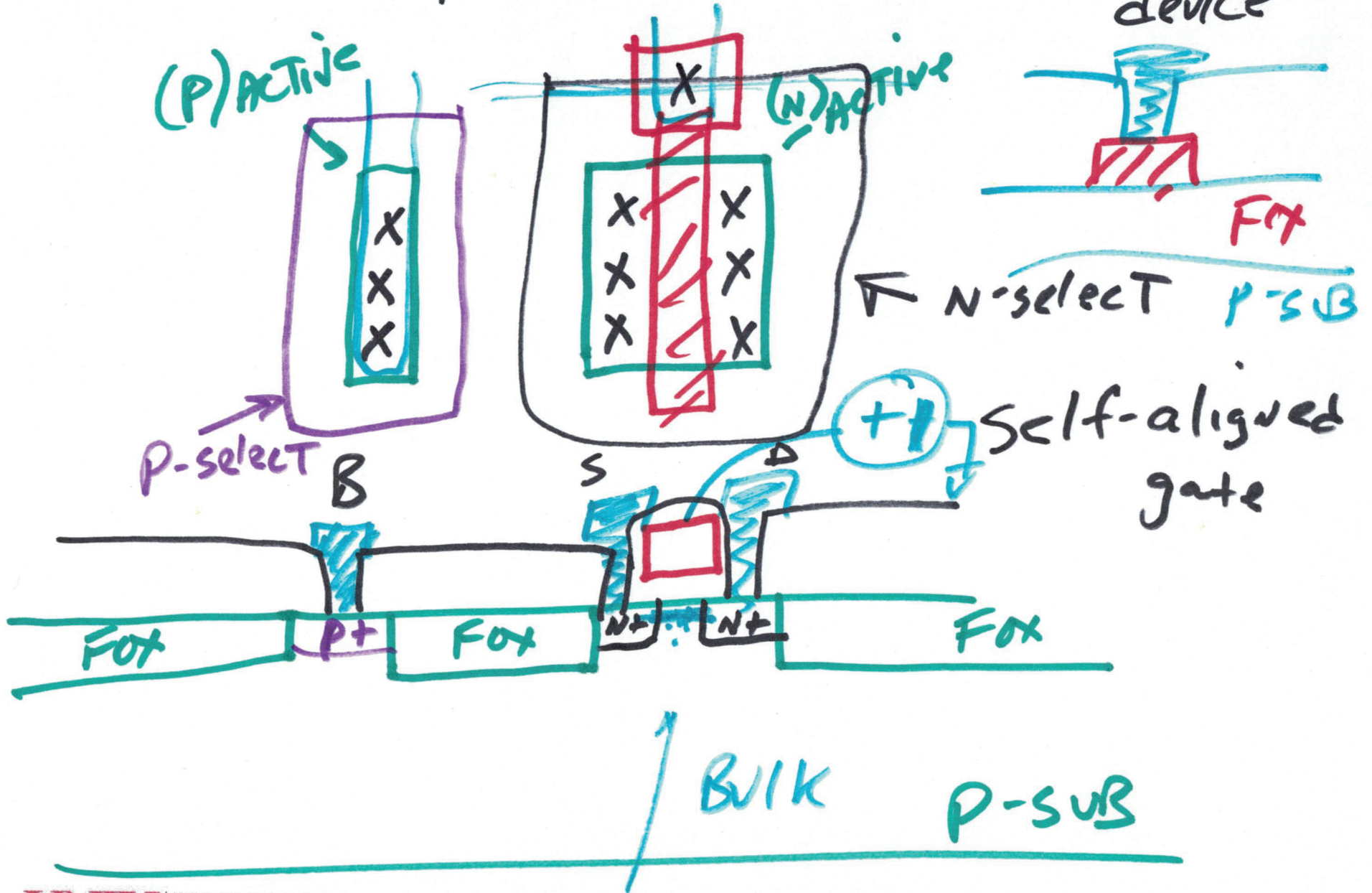
- N-Well
- Metal 1,2,3
- glass
- VIA
- VIA 1,2



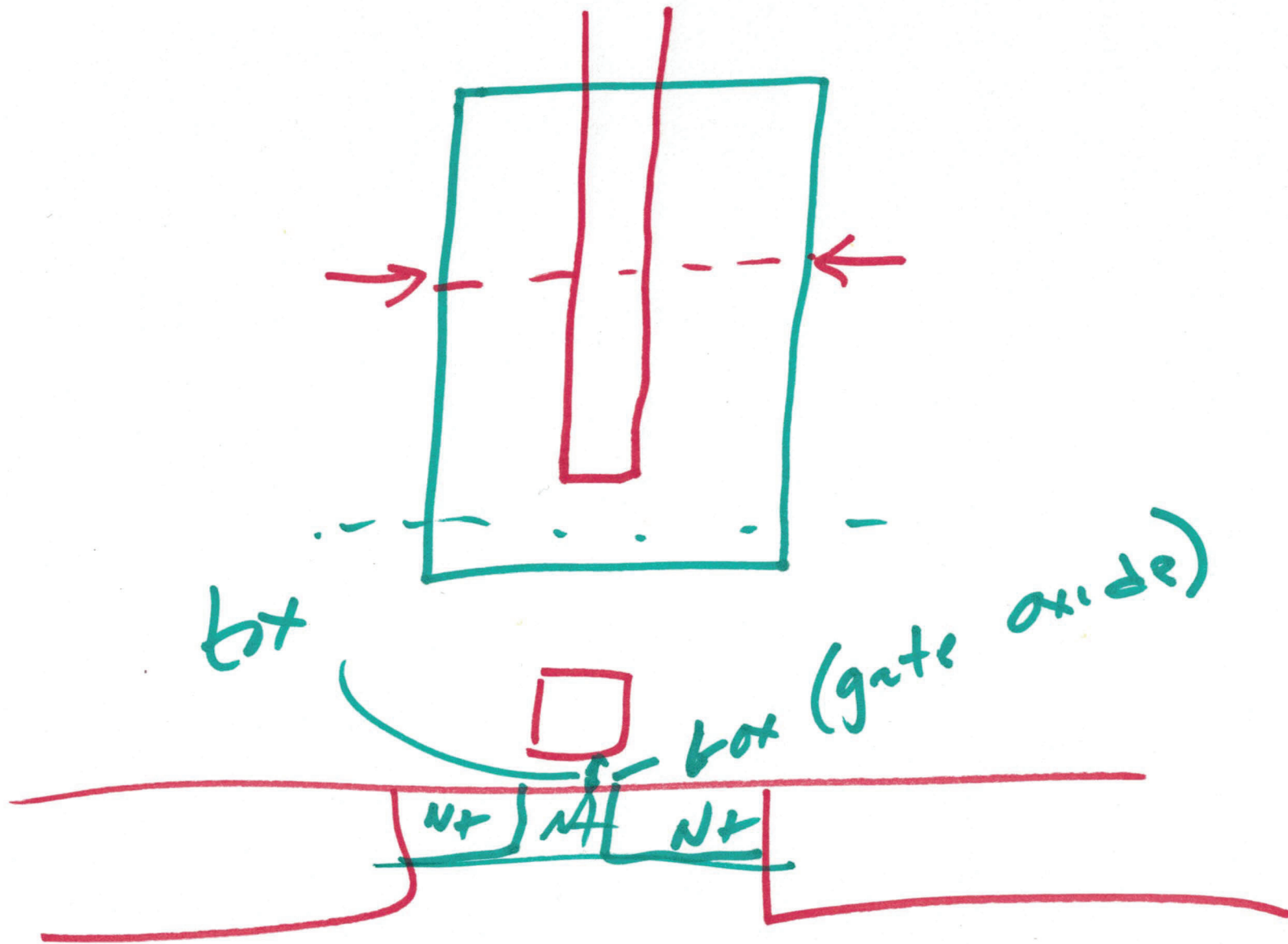


3)

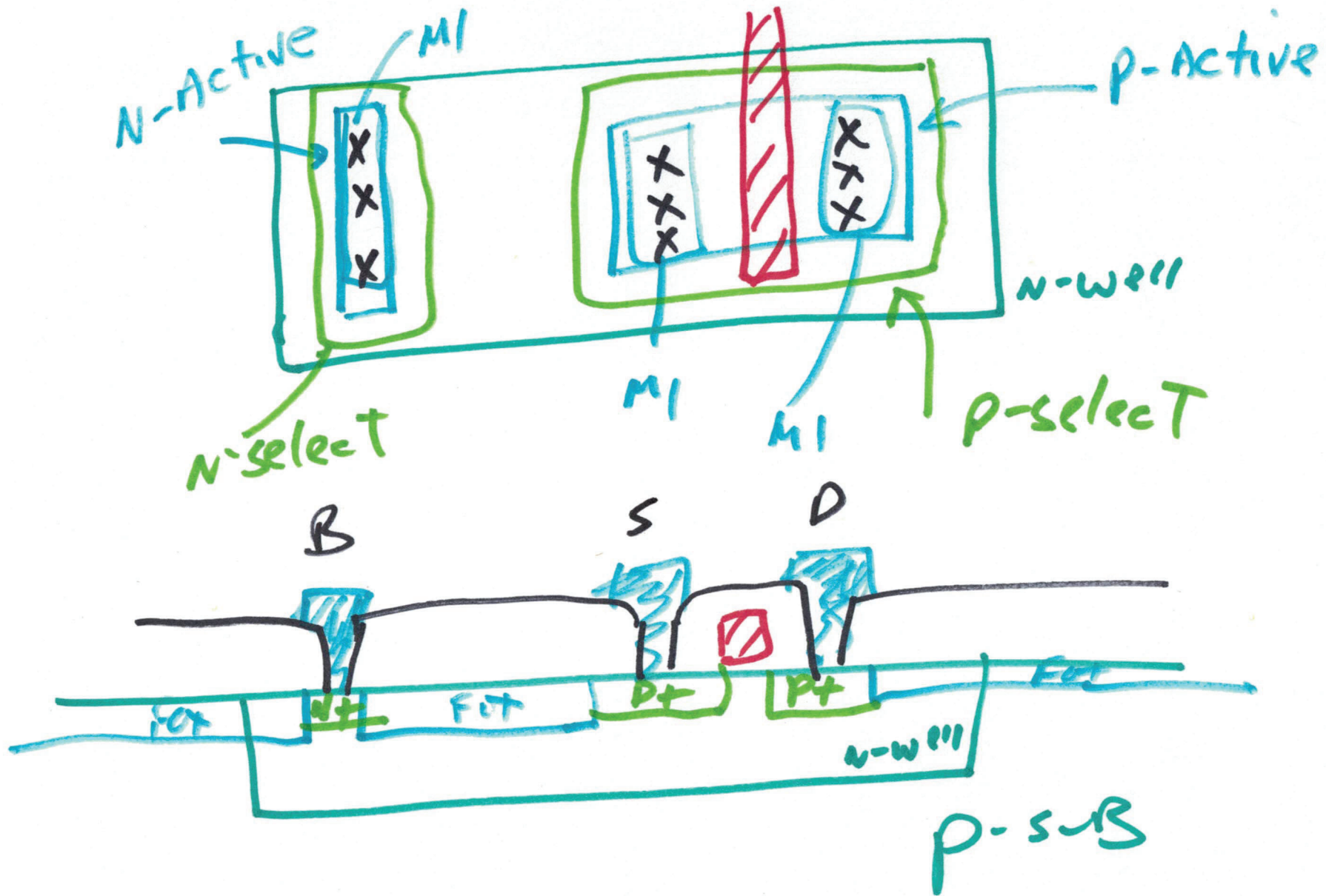
# Lay out of AN NMOS device



4)



5)



b)