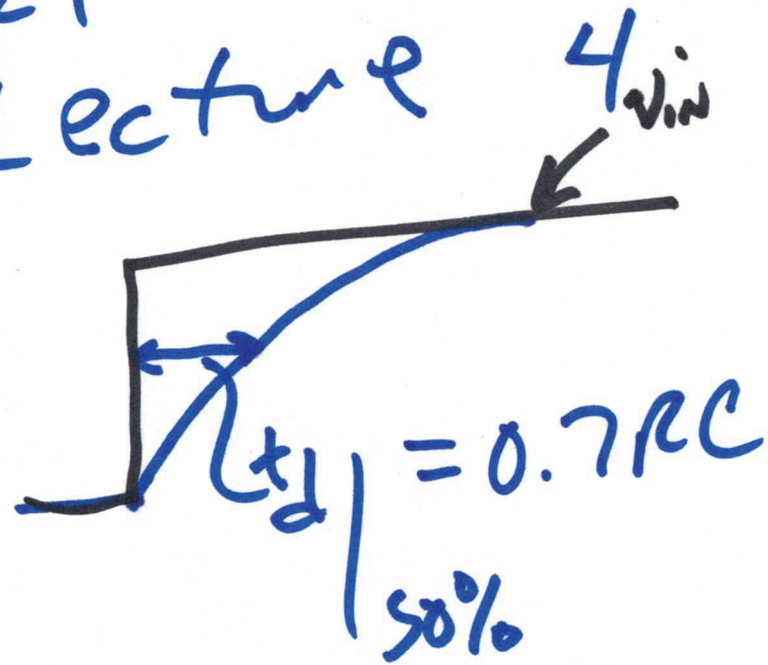
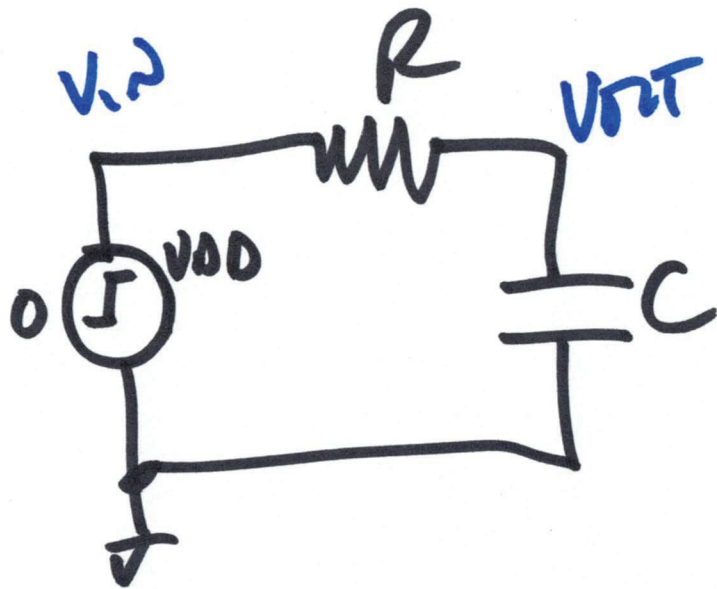


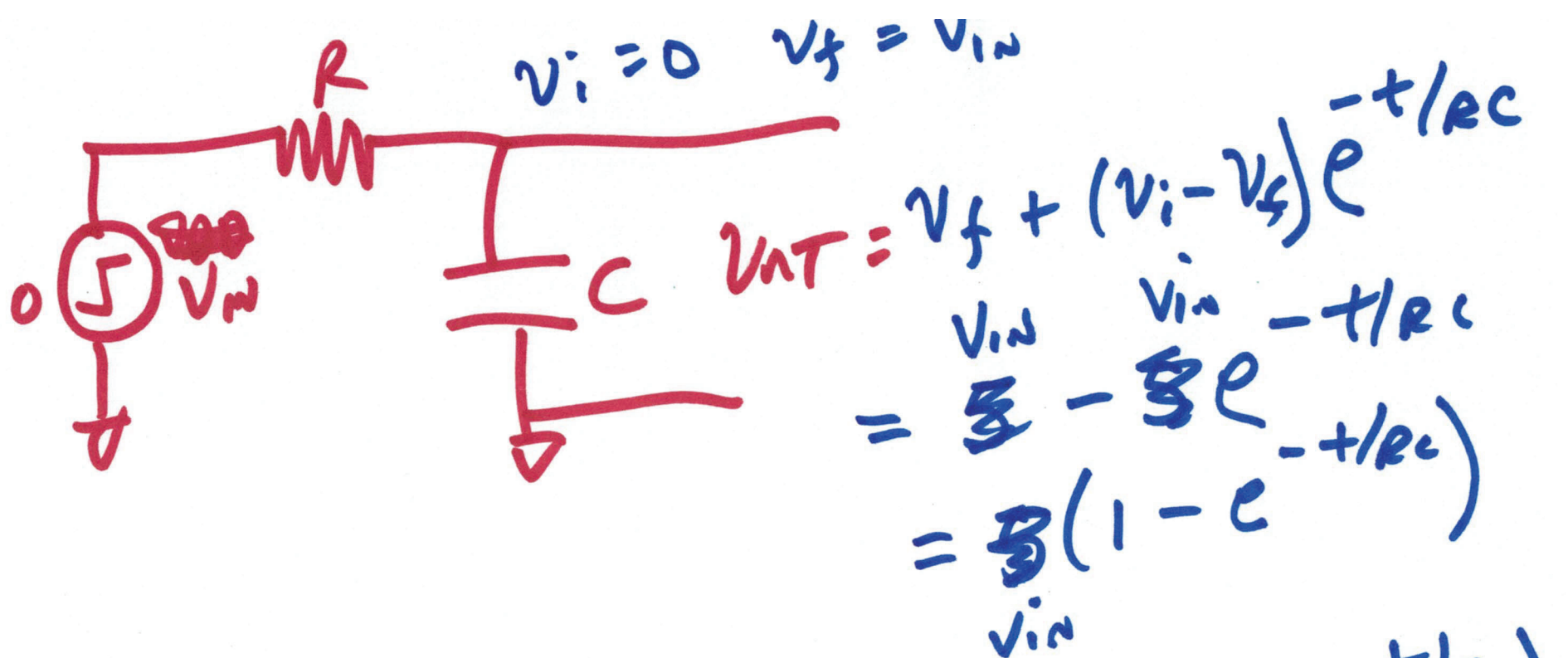
EE 421 / ECE 621

Digital IC Design

9/1/2021

Lecture 4





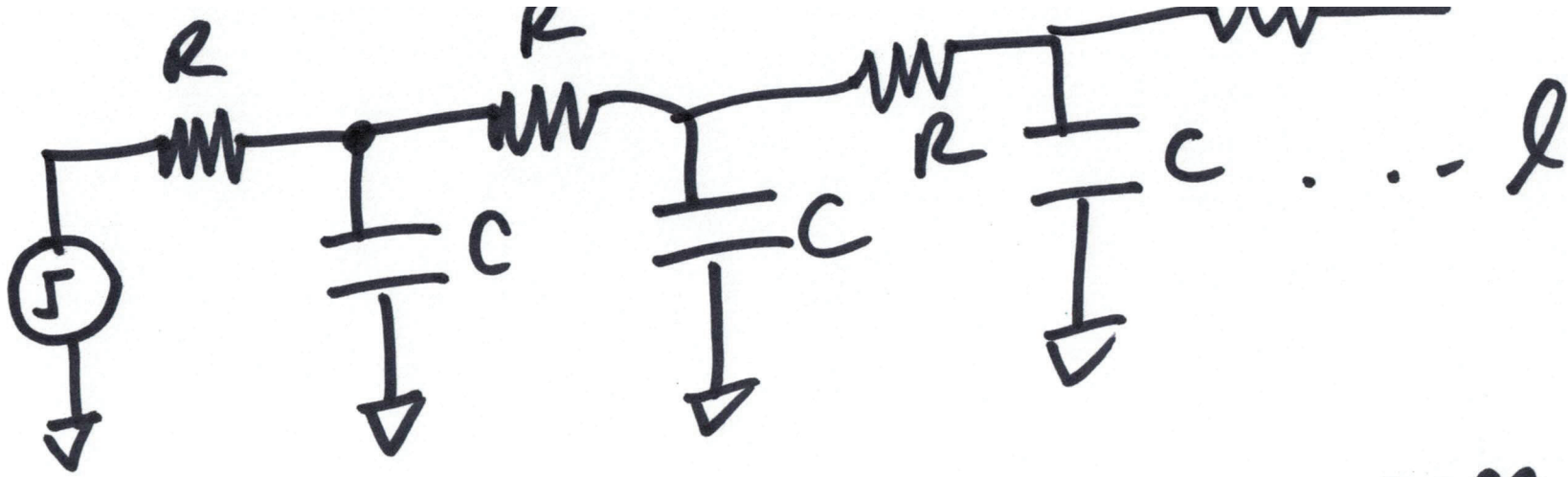
$$v_{OUT} = \frac{v_{in}}{2} = \frac{v_{in}}{2}(1 - e^{-t/RC})$$

$$t_d = 0.7RC$$

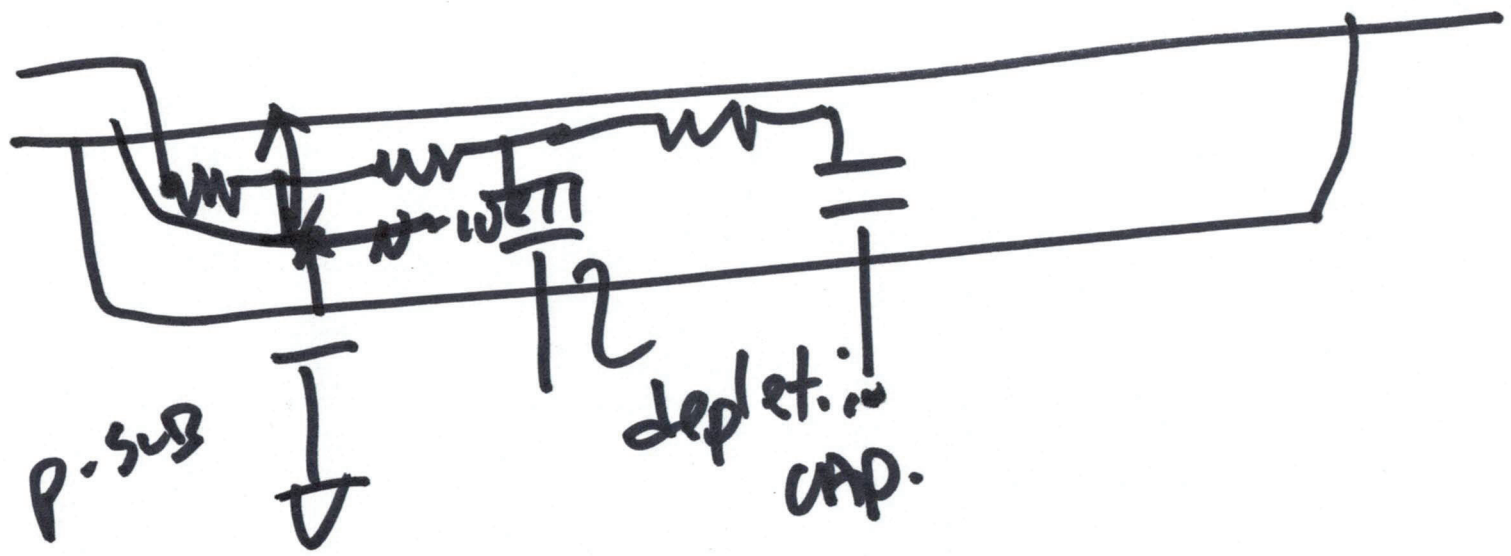
$$\ln(e^{-t_d/RC}) = \ln\left(\frac{1}{2}\right)$$

$$-\frac{t_d}{RC} = -0.7$$

2)



$\uparrow C \downarrow$
 $t_d = 0.7RC + 0.7 \cdot 2R \cdot C + 0.73RC$
 $0.7Q \cdot R \cdot C$



3)

$$t_d = 0.7RC(1 + 2 + 3 + \dots + l)$$

$$\frac{l(l+1)}{2}$$

$$t_d = 0.35 \cdot RC l(l+1)$$

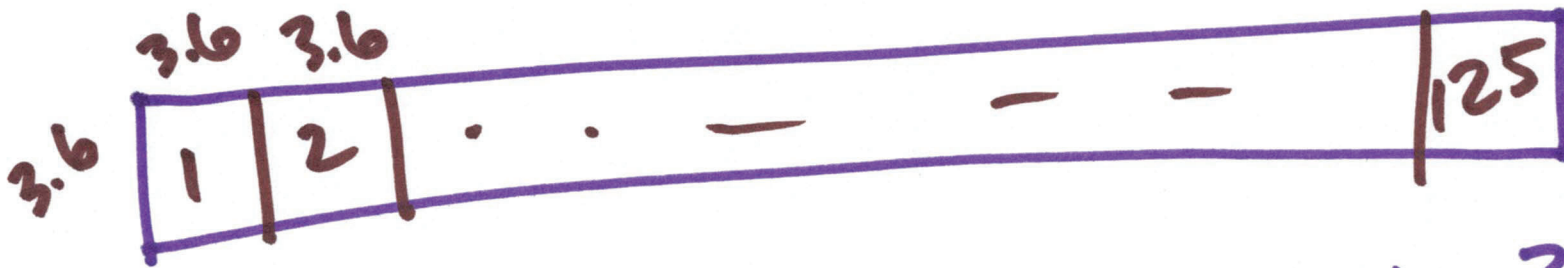
l is big

$$t_d \approx 0.35RC l^2$$

Delay through n-well

$100k = 800 \cdot \frac{L}{W}$
 $100k = 800 \cdot \frac{L}{3.6}$

$100k \text{ resistor}$
 $R_{sq} = 800 \frac{\Omega}{\square}$



of squares

$$\frac{450}{3.6} = 125 = L$$

$r = 800$

$$L = \frac{100k \cdot 3.6}{800}$$

$$L = 450$$

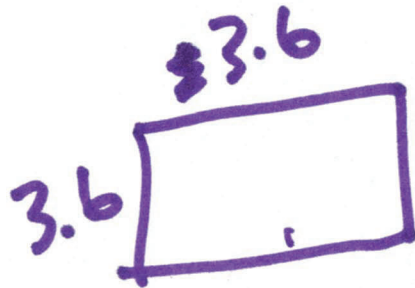
$$W = 3.6$$

5)

$$C_{sw} = 1 \text{ fF}/4\mu\text{m}$$

$$C_{\text{bottom}} = 1 \text{ fF}/4\mu\text{m}^2$$

C_{sw} perimeter



$$C_{sw}|_{\text{tot}} = \frac{1 \text{ fF}}{4\mu\text{m}} \cdot 14.4\mu\text{m} = 14.4 \text{ fF}$$



$$C_{sw}|_{\text{tot}} = \frac{1 \text{ fF}}{4\mu\text{m}^2} \cdot 3.6\mu\text{m} \times 3.6\mu\text{m} \approx 13 \text{ fF}$$

$$C = 14.4 \text{ fF} + 13$$

$$C = 27.4 \text{ fF}$$

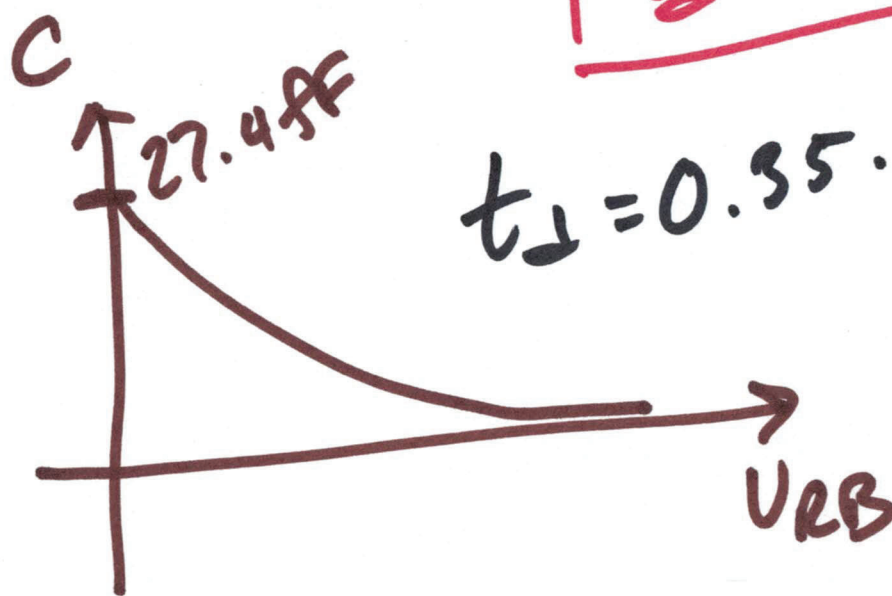
$$L = 125, \quad C = 27.4 \text{ fF}, \quad r = 800$$

$$t_d = 0.35 \cdot 27.4 \text{ fF} \cdot 800 \cdot 125^2$$

$$t_d \approx 120 \text{ ns}$$

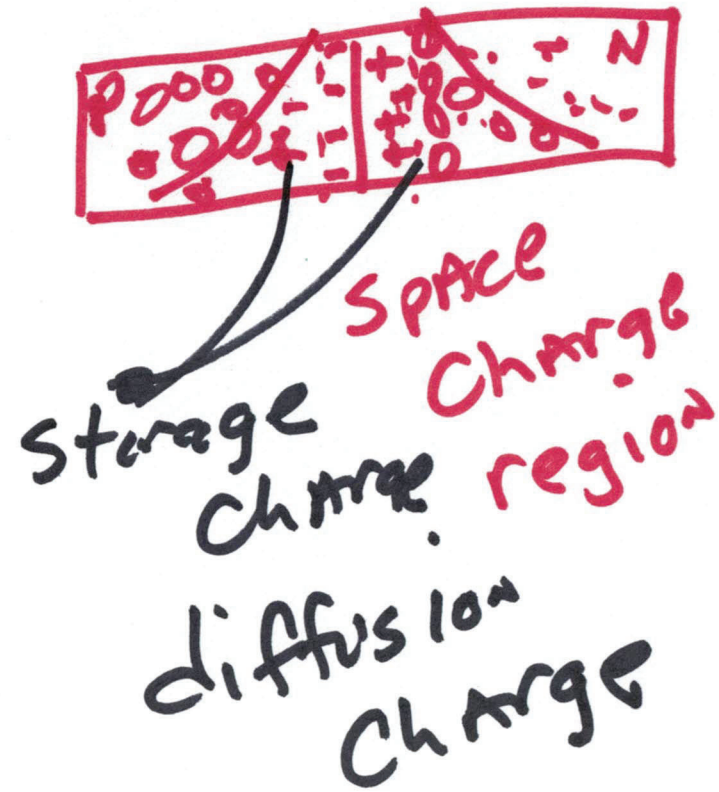
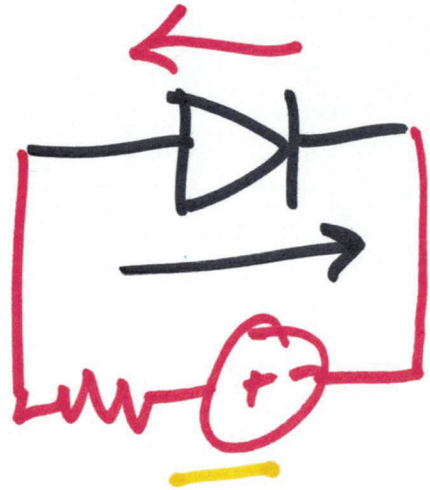
$$3425 \text{ fF}$$

$$3.425 \text{ pF}$$

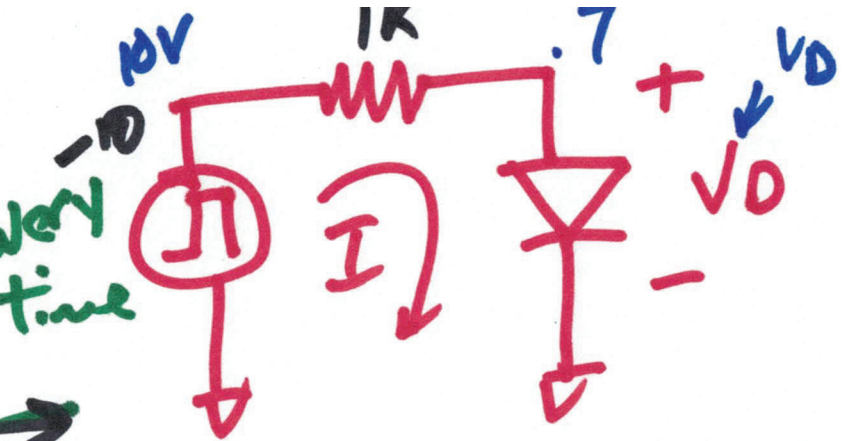
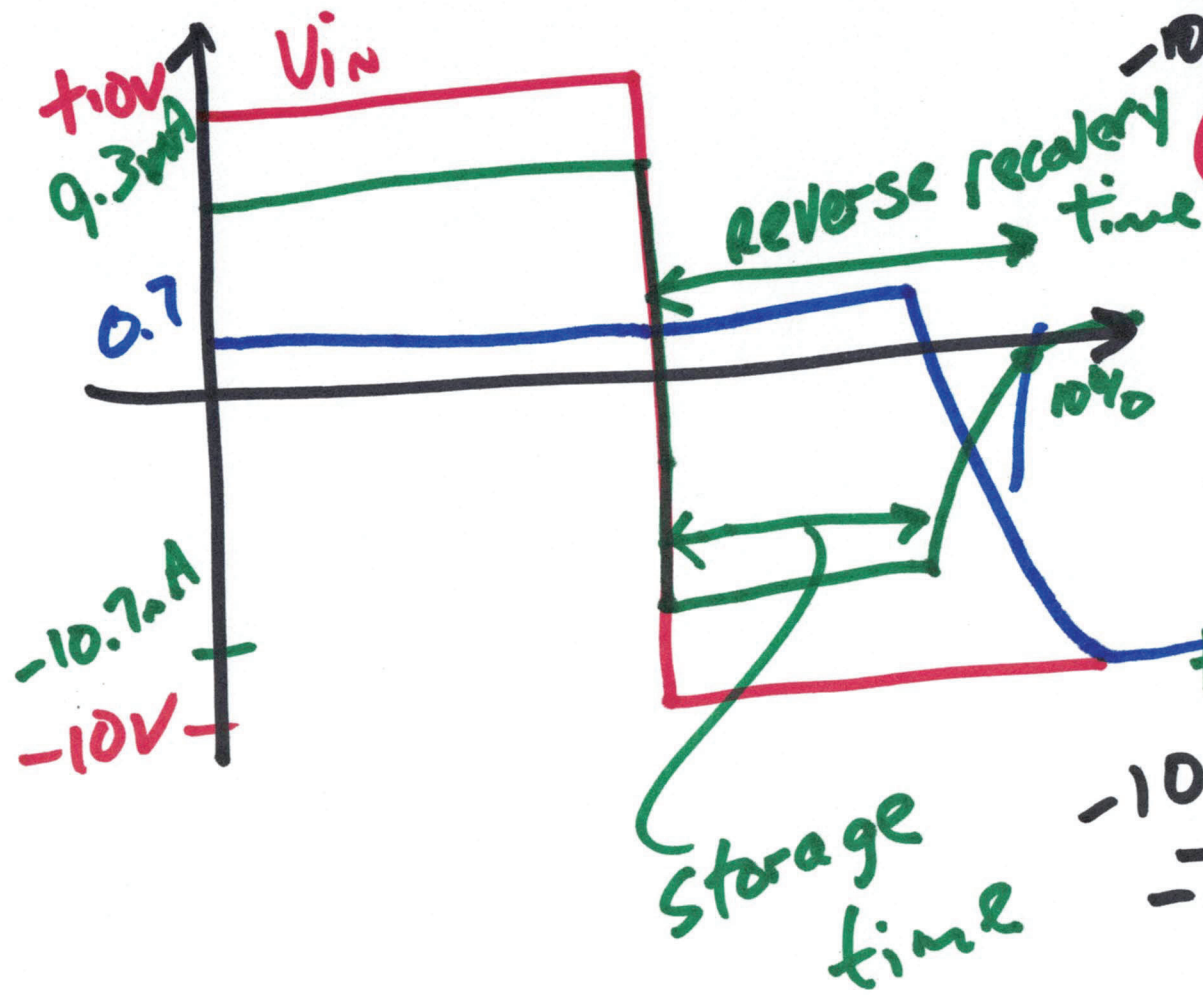


$$t_d = 0.35 \cdot 27.4 \text{ fF} \cdot \frac{800 \cdot 125}{100 \text{ K}}$$

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8)



$$\frac{-10 - .7}{1K} = -10.7mA$$

$V = IR$

$$\frac{.7 - (-10)}{1K} = 10.7mA$$