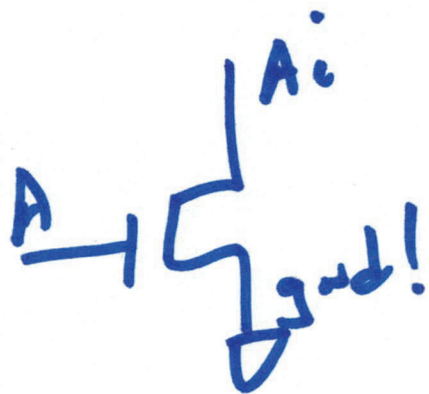


EE 421 / ELG 621

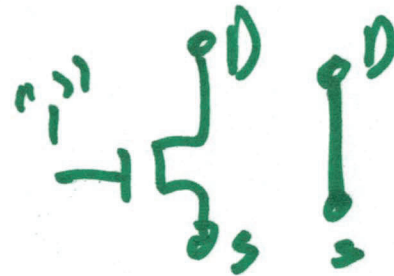
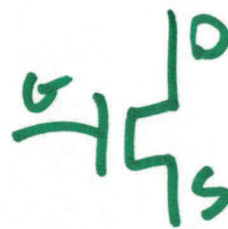
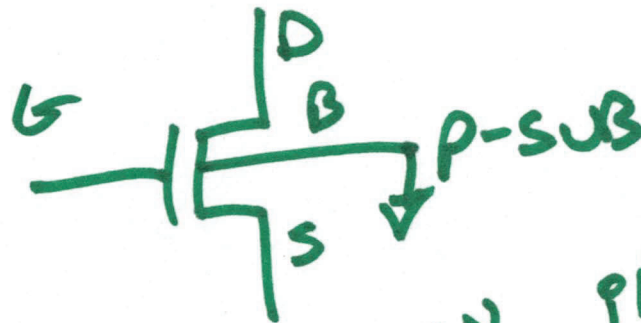
Digital IC Design

Sept. 15, 2021
Lecture 7



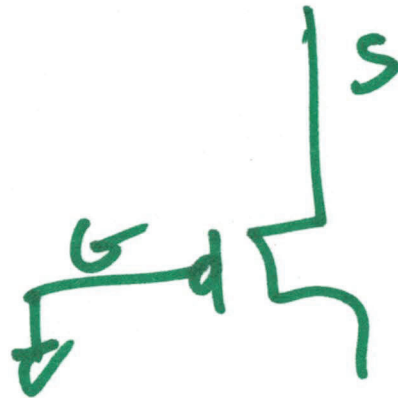
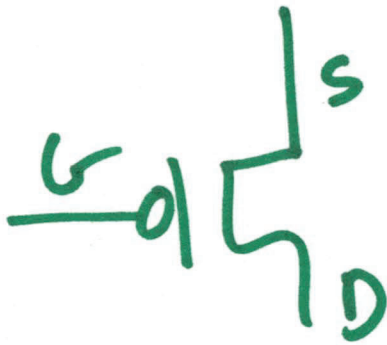
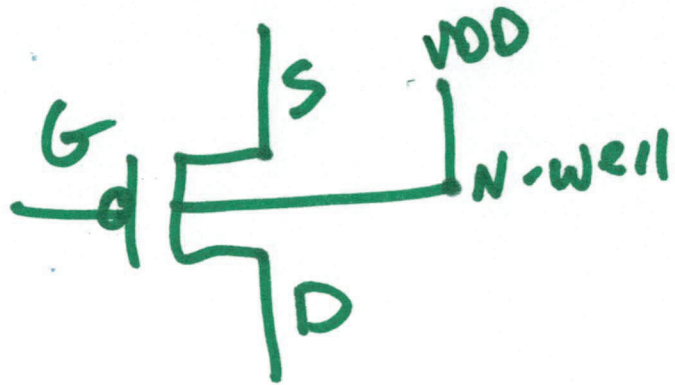
N-well
 Metal 1, 2, 3
 VIA 2, VIA, CONTACT
 GLASS
 res-id

NMOS

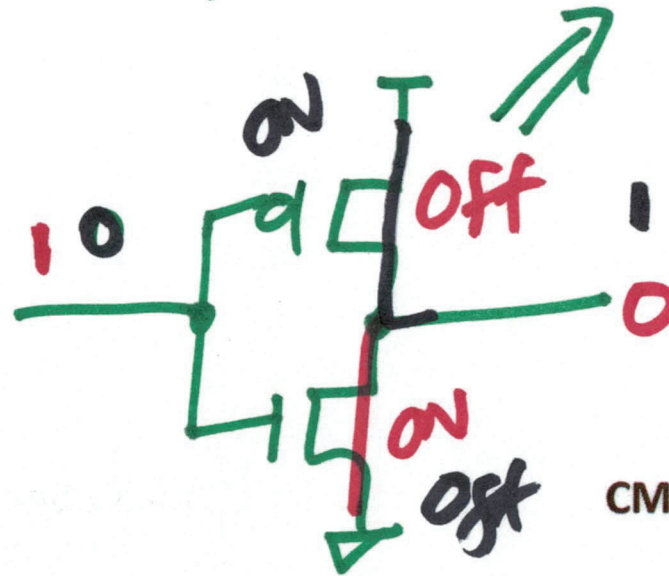


11

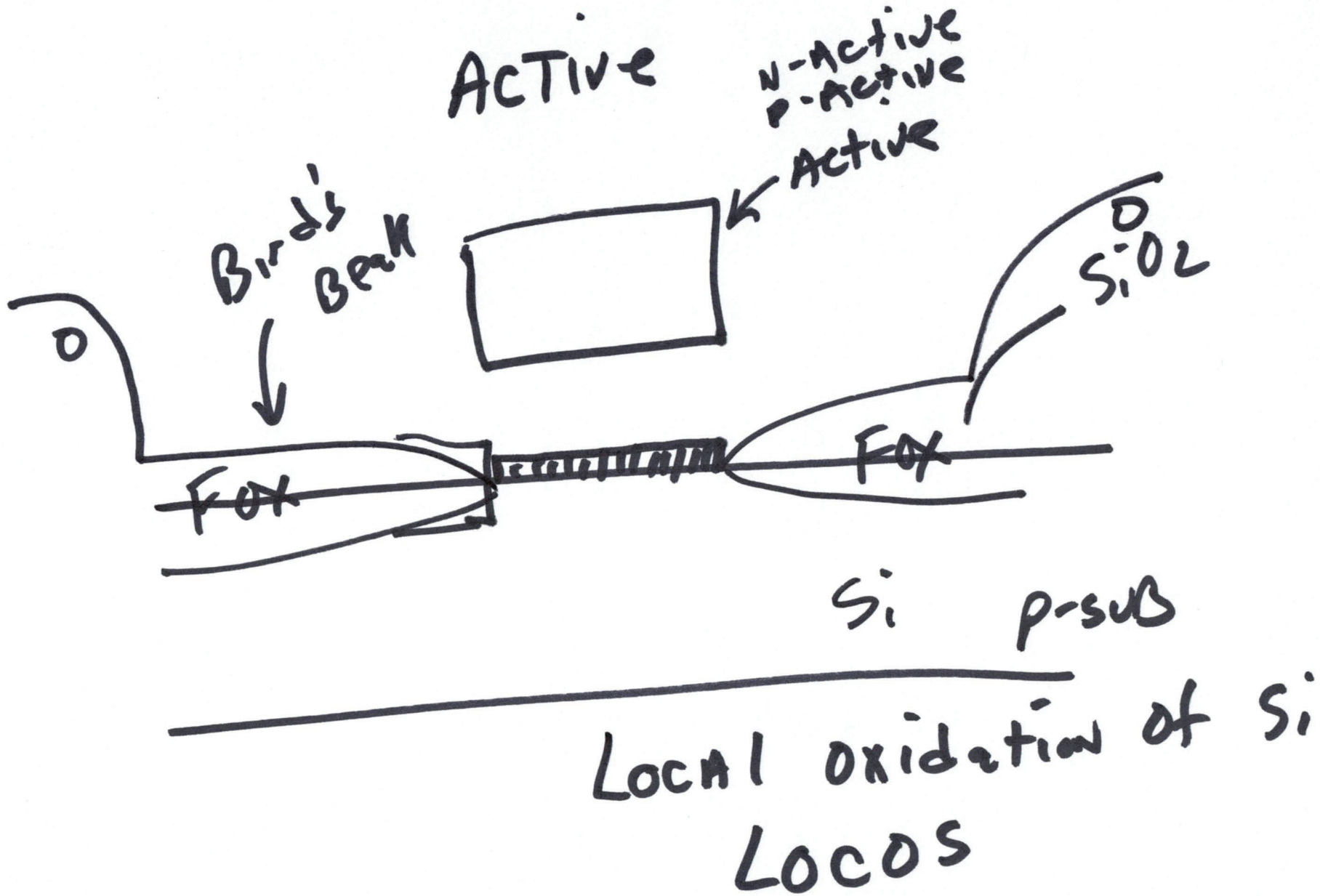
PMOS



inverter



21

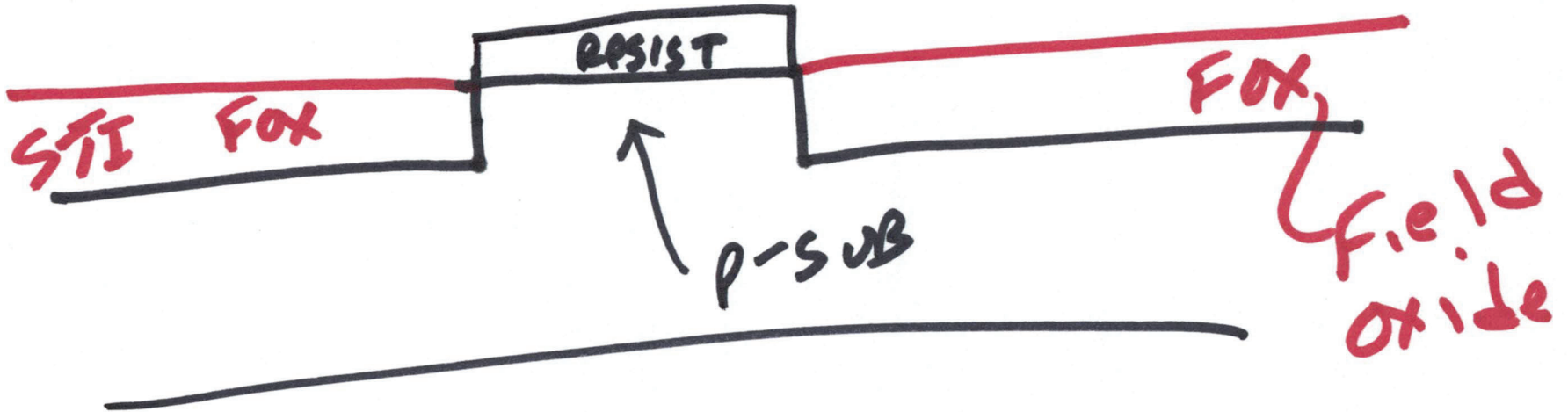
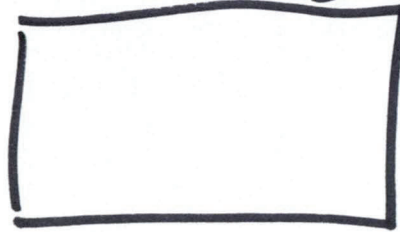


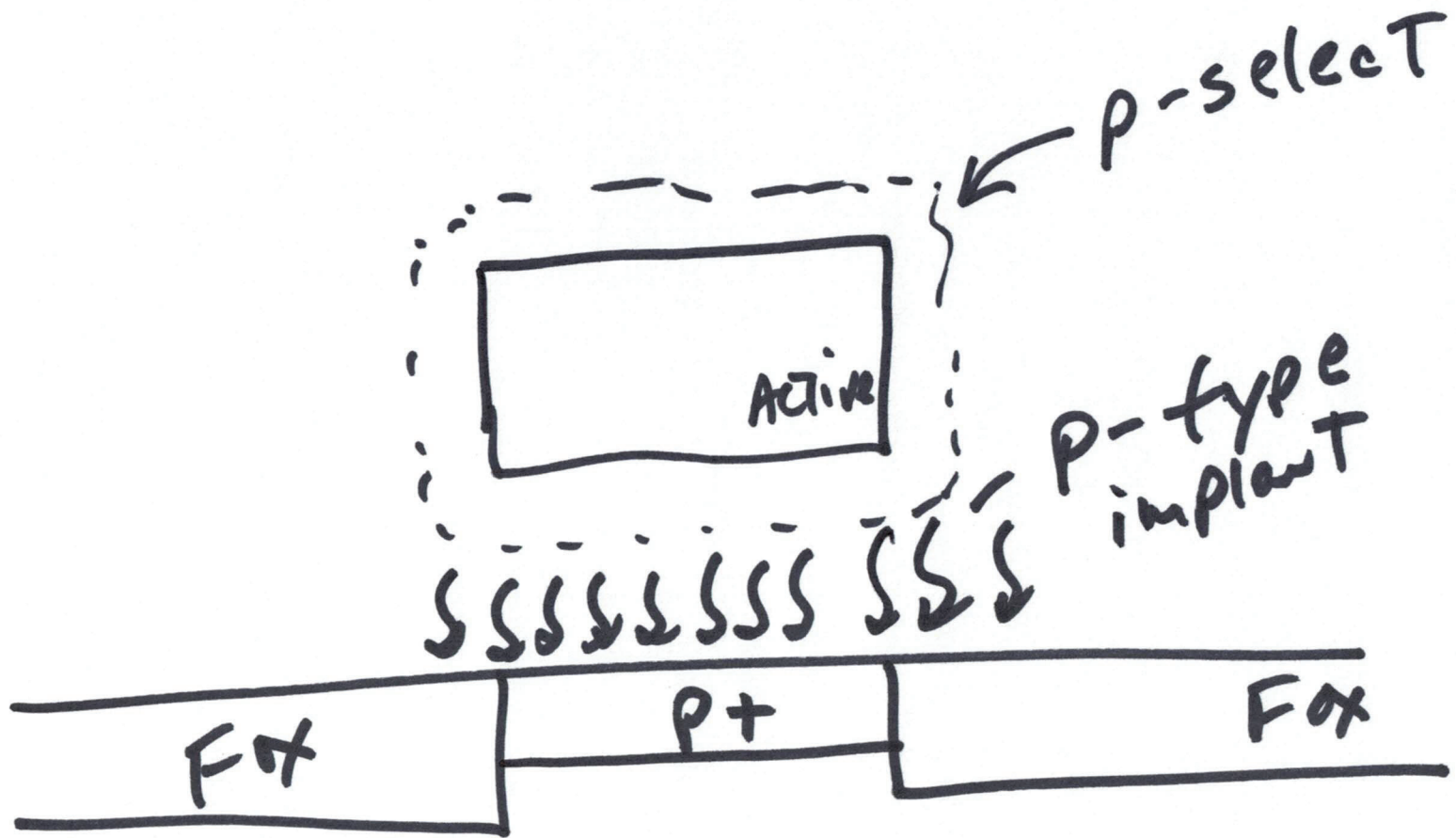
GOT
GATE oxide

Shallow Trench Isolation

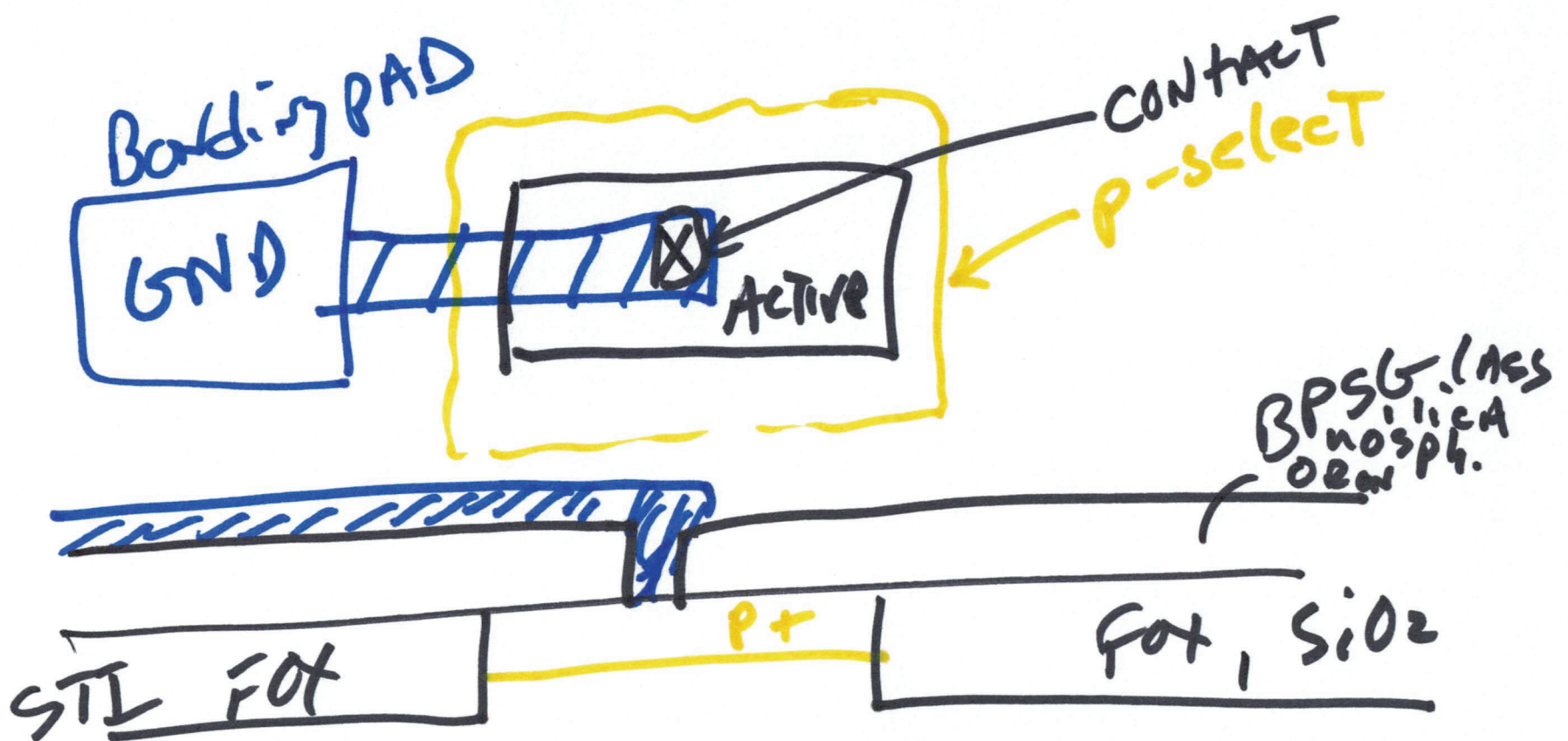
STI

ACTIVE
↓

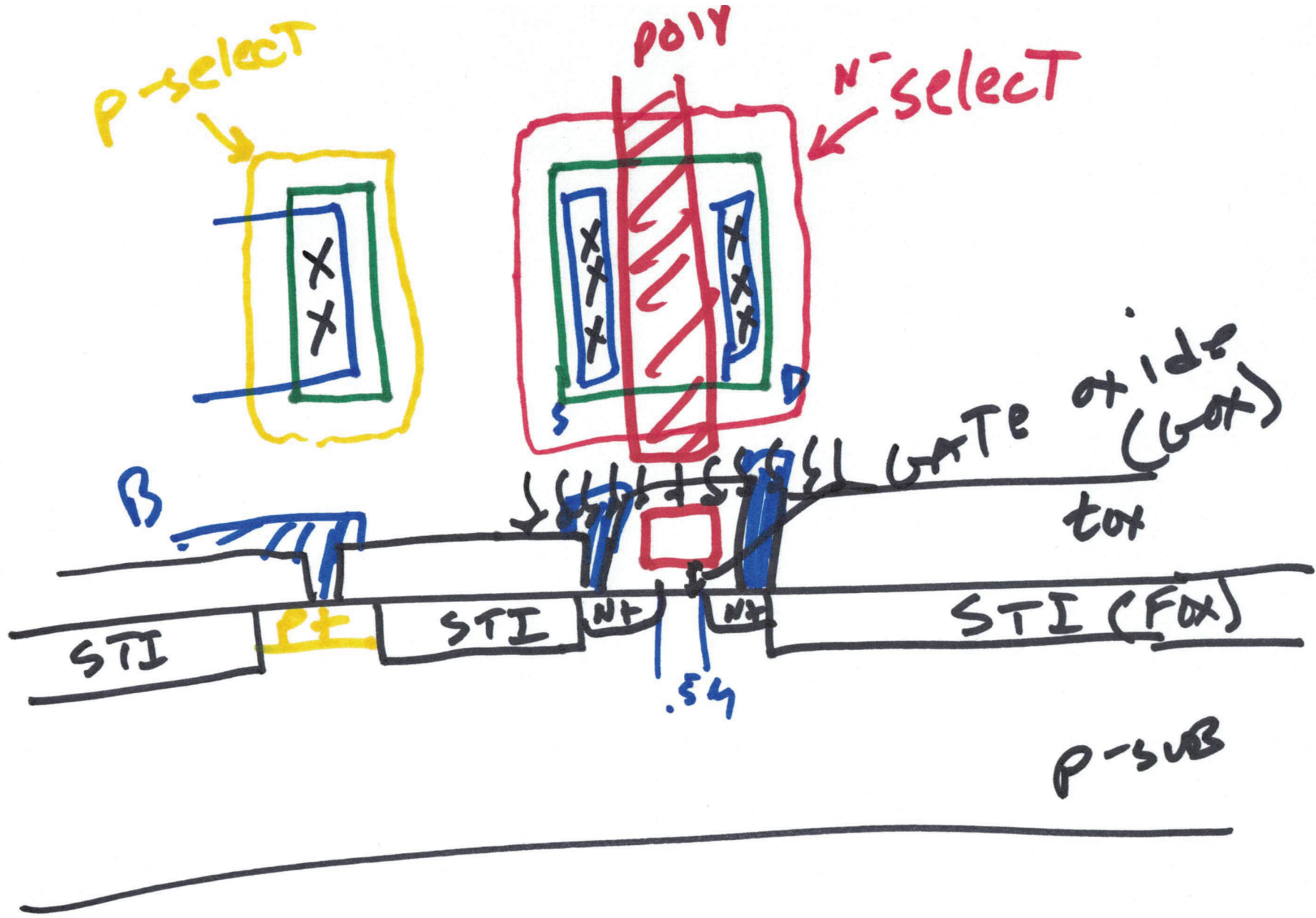




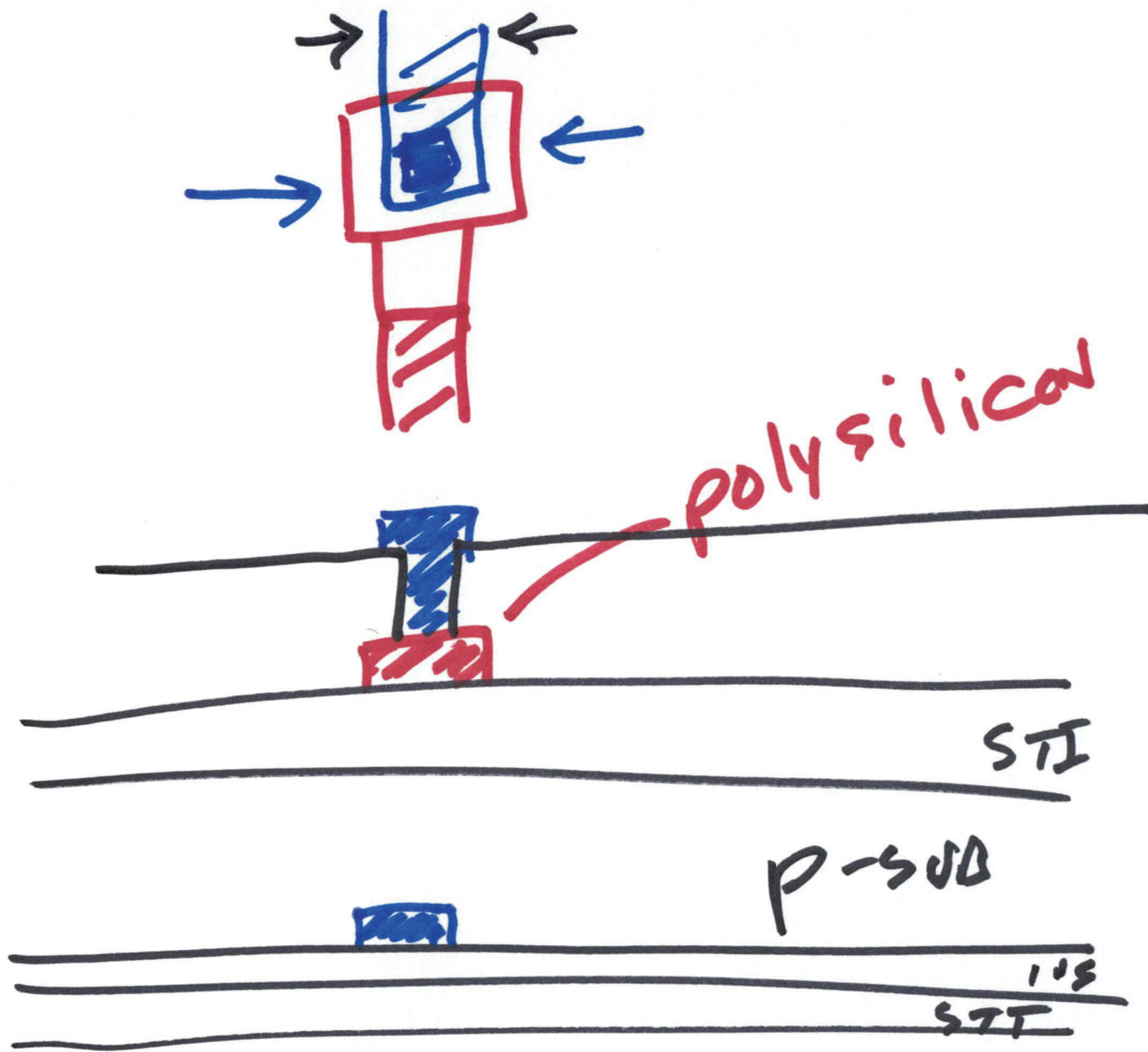
5)



6)



1)



polysilicon

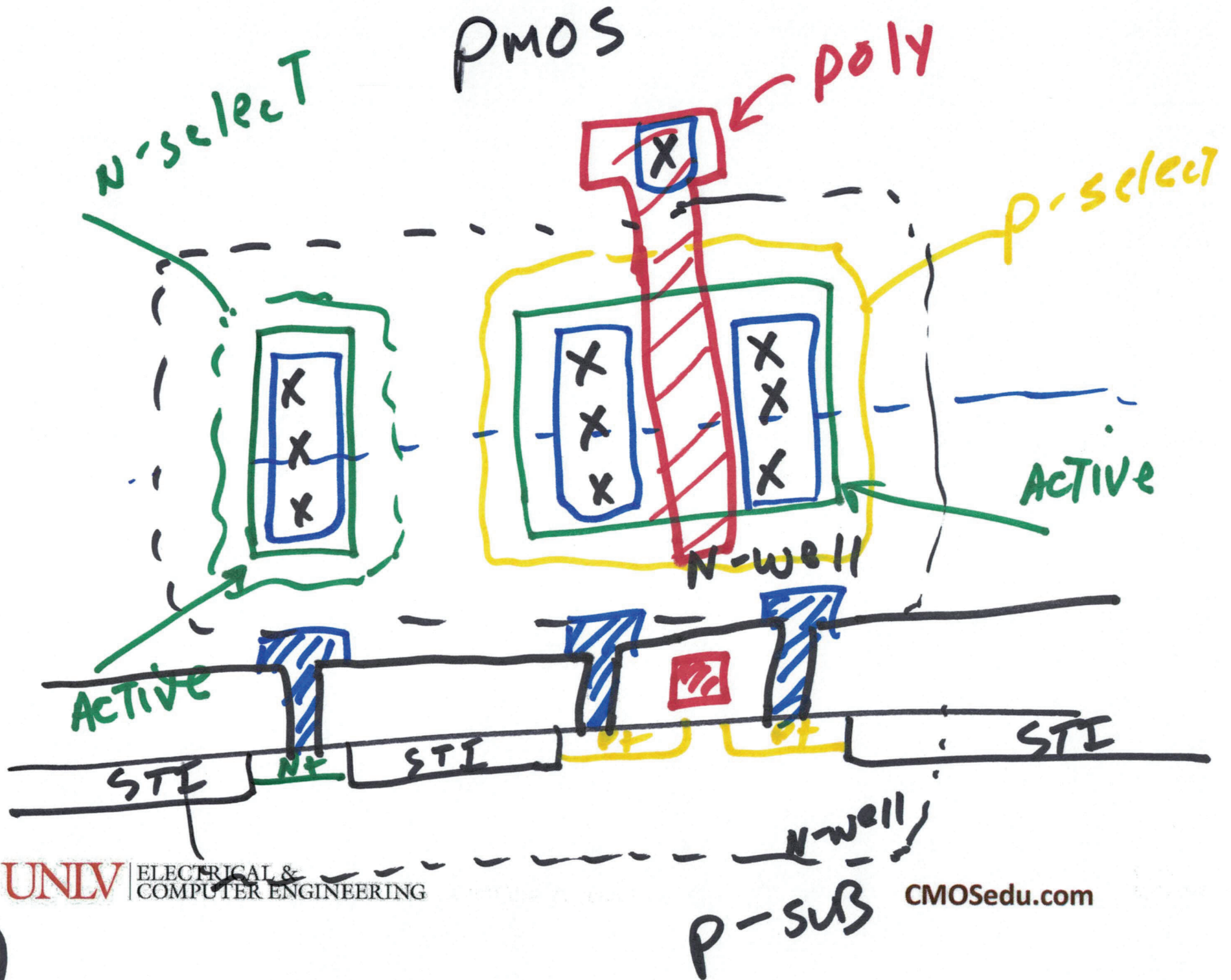
STI

p-sub

100

STT

3)



9)