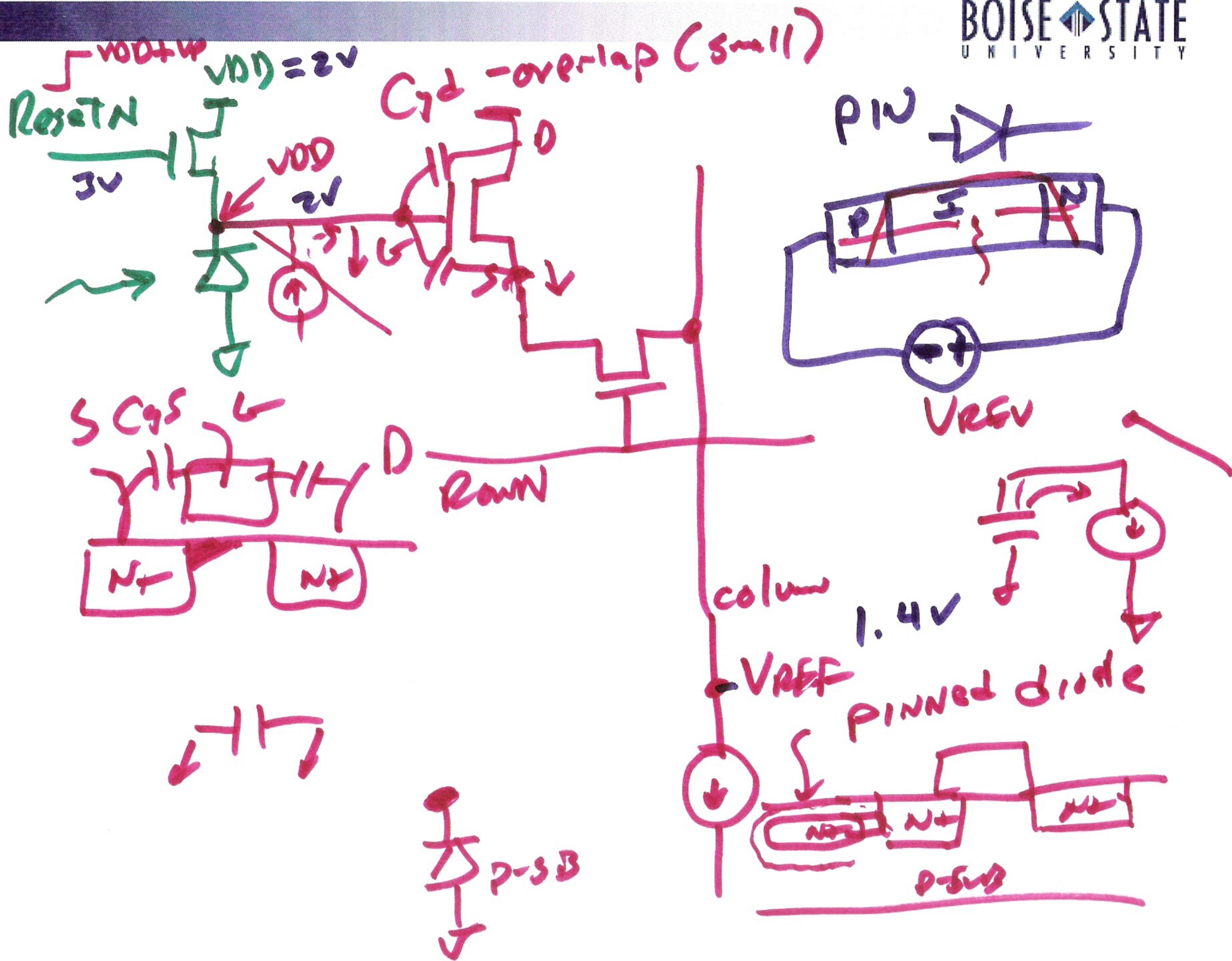
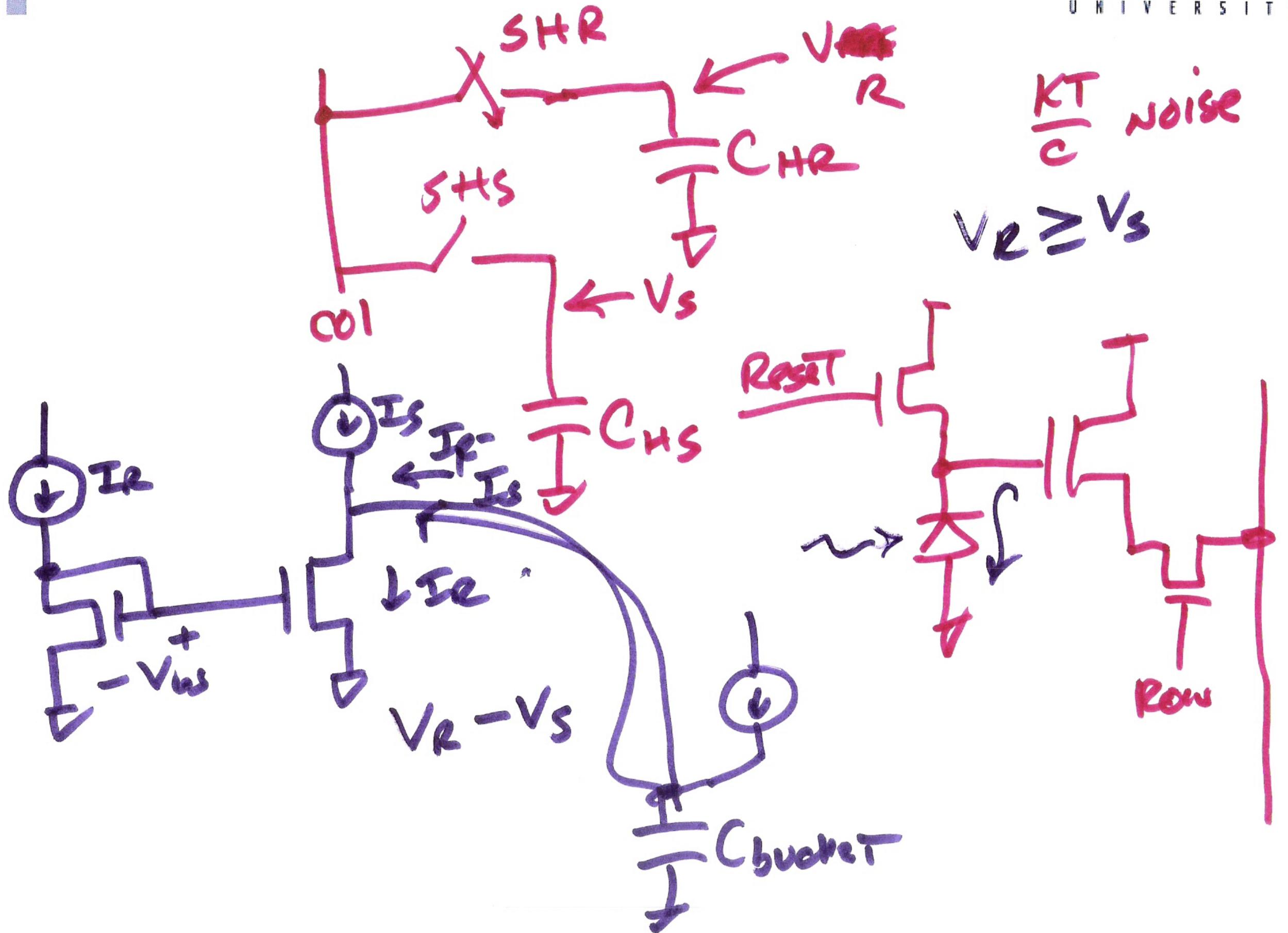
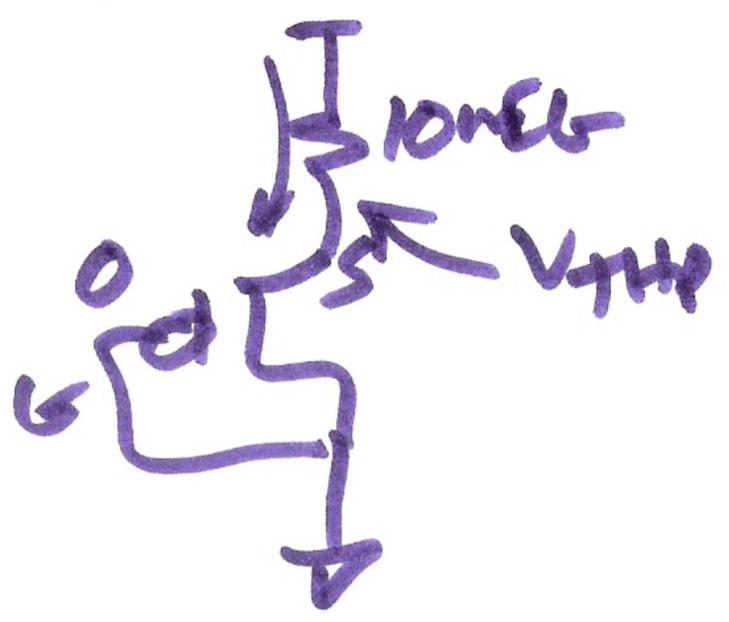
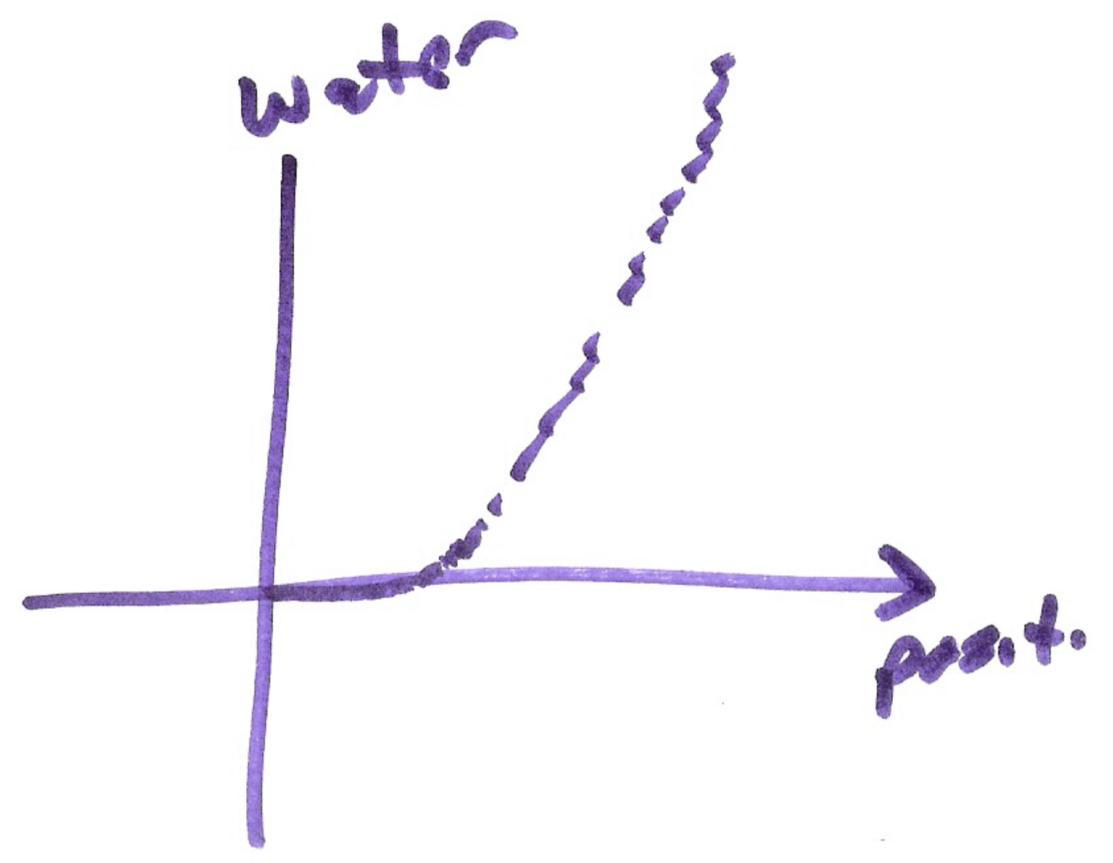
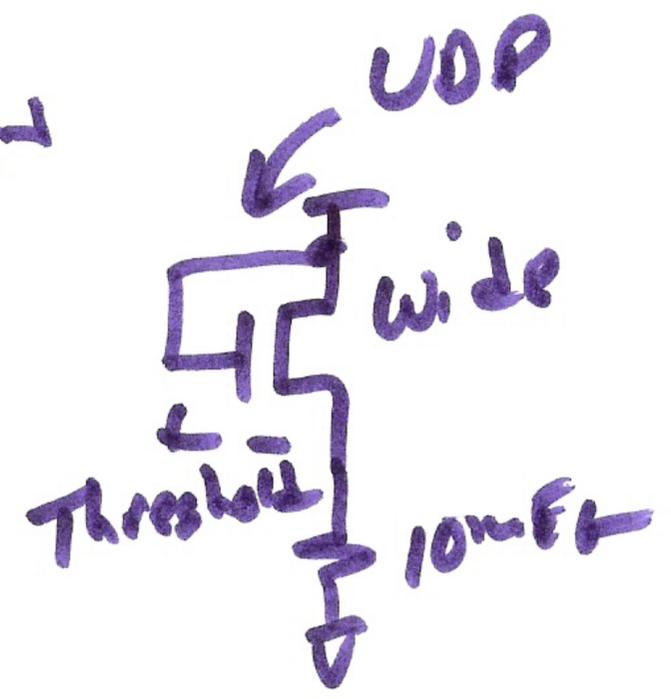
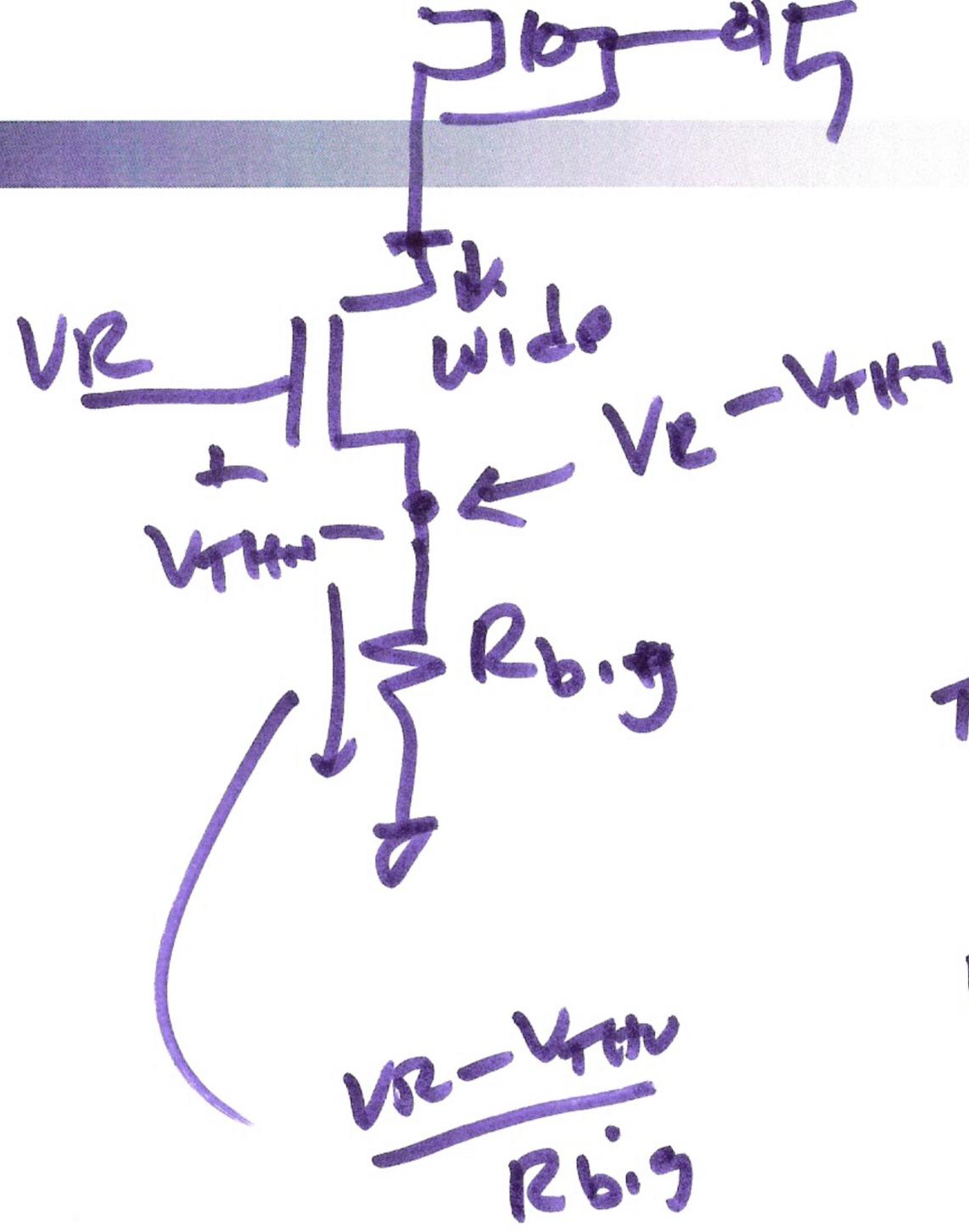


CMOS Imagers

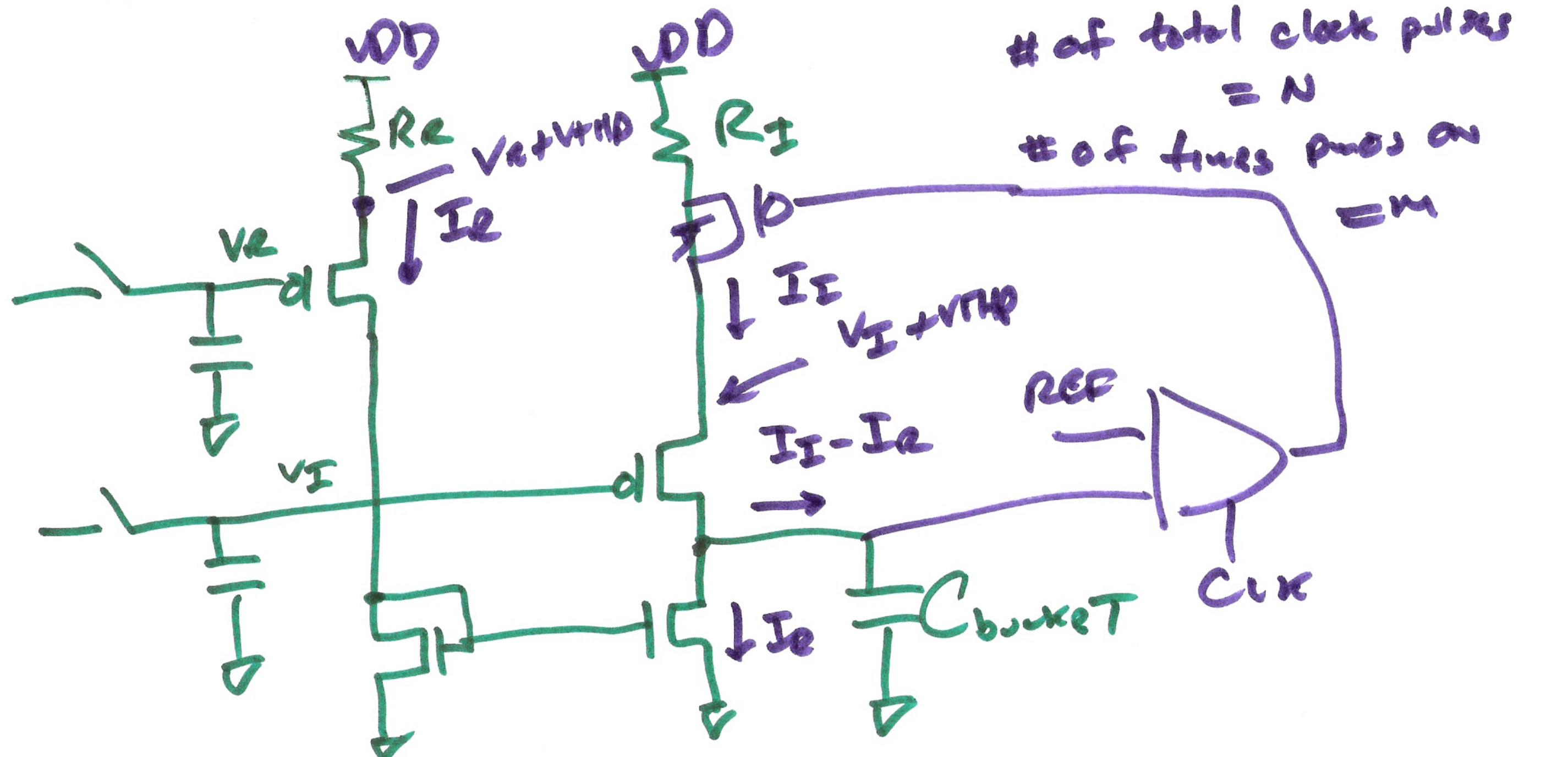




3)



4)



of total clock pulses = N
 # of times pulses on = M

$$DSM = \frac{V_{DD} - (V_{R2} + V_{THP})}{R_R} = \frac{V_{DD} - (V_I + V_{THP})}{R_I} \cdot \frac{M}{N}$$

5)