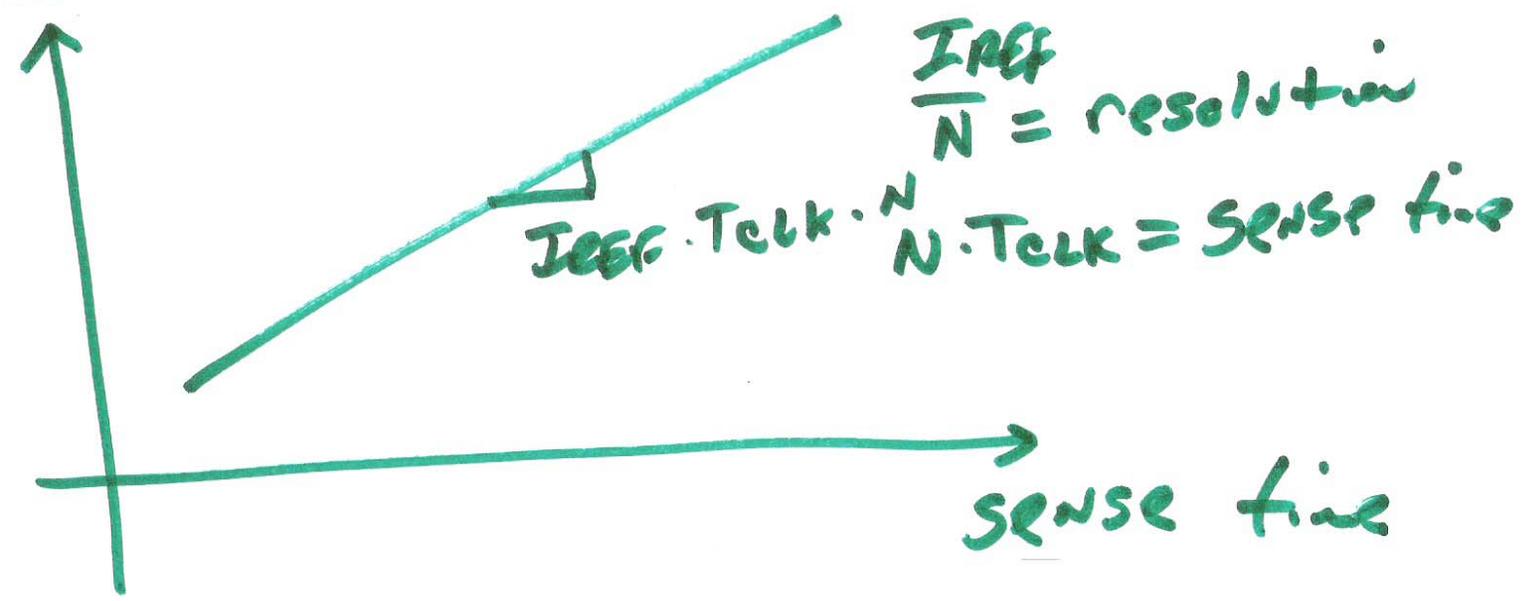


Lecture 24

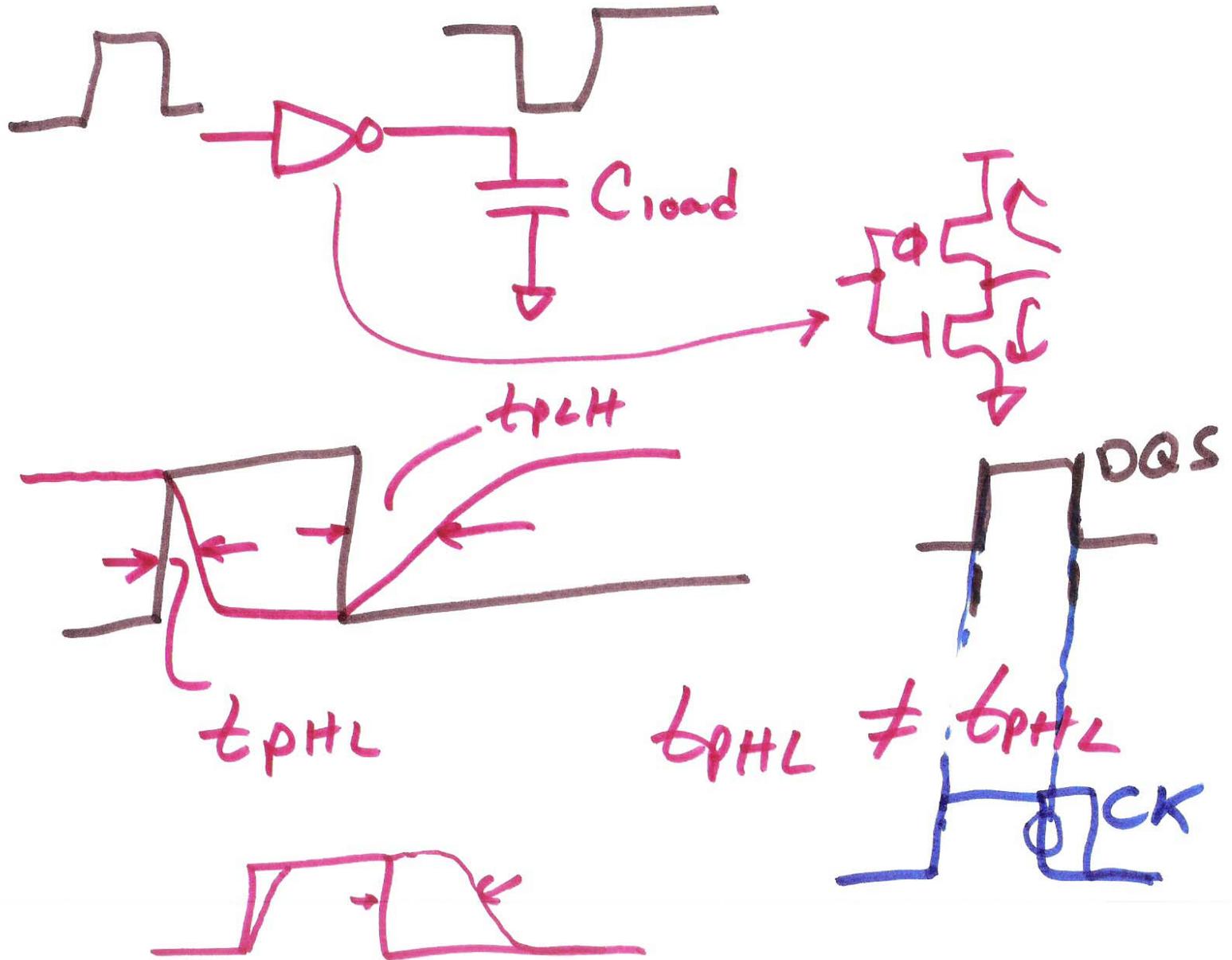
May 2, 2011

Resolution⁻¹

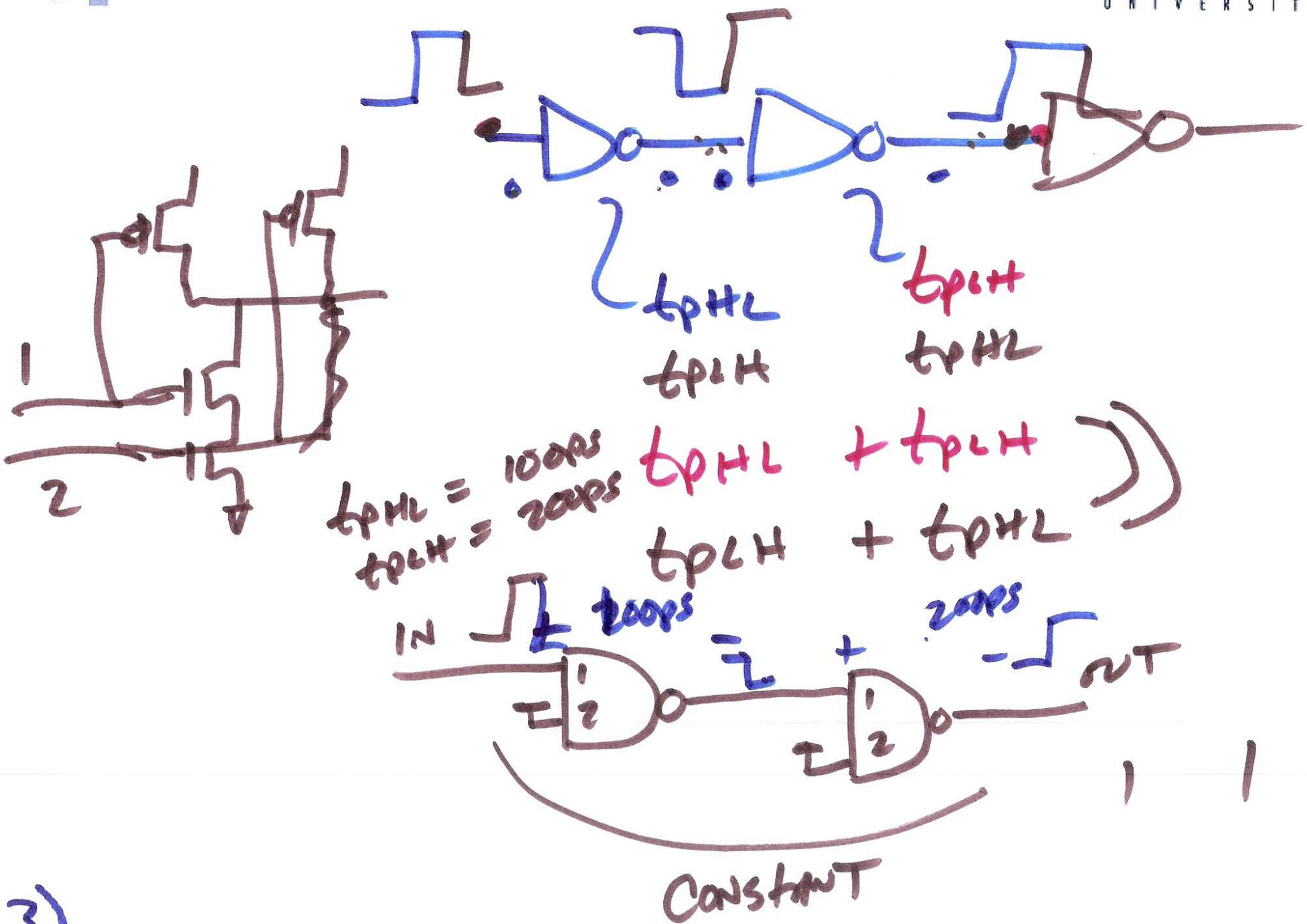


1)

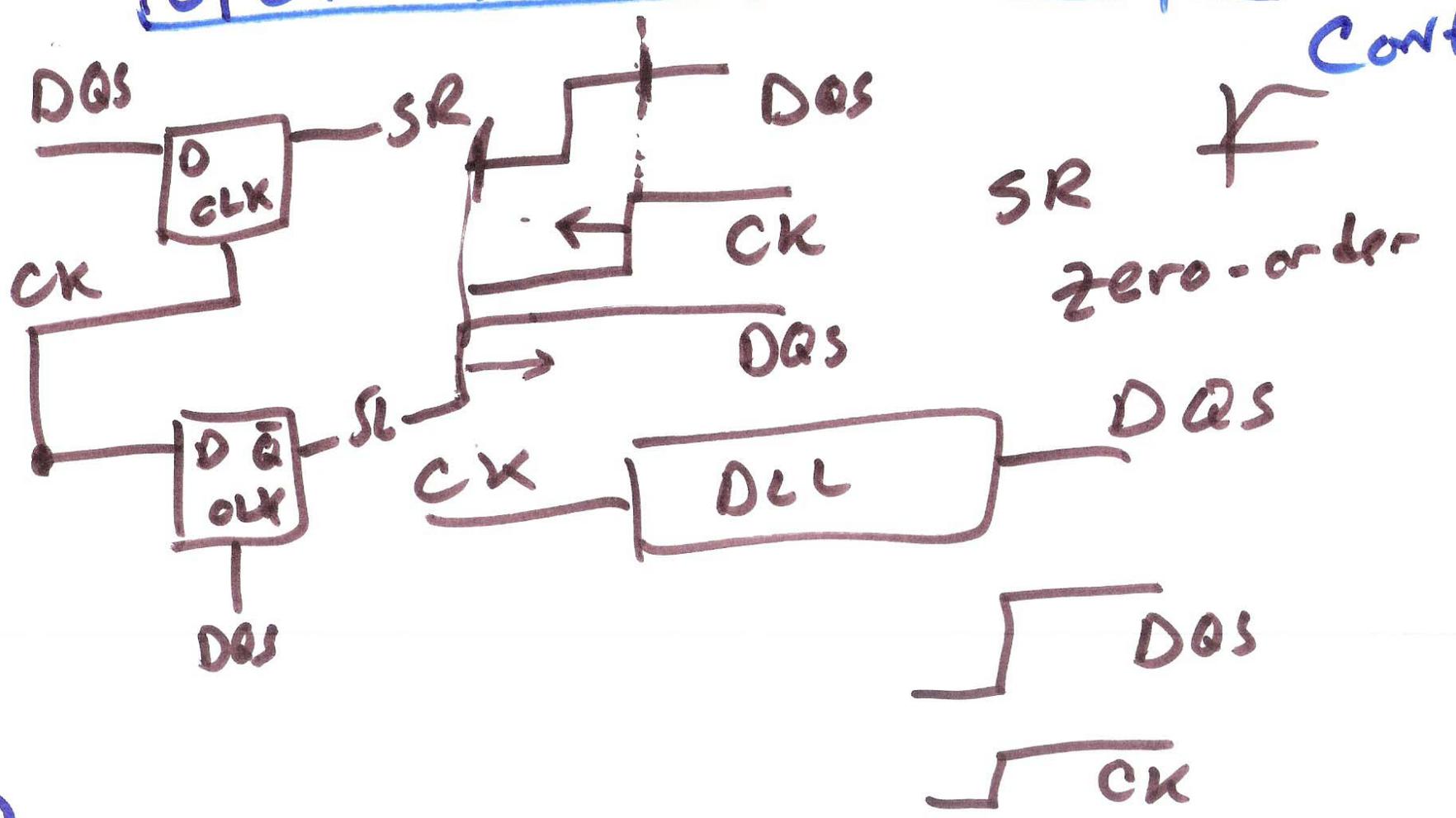
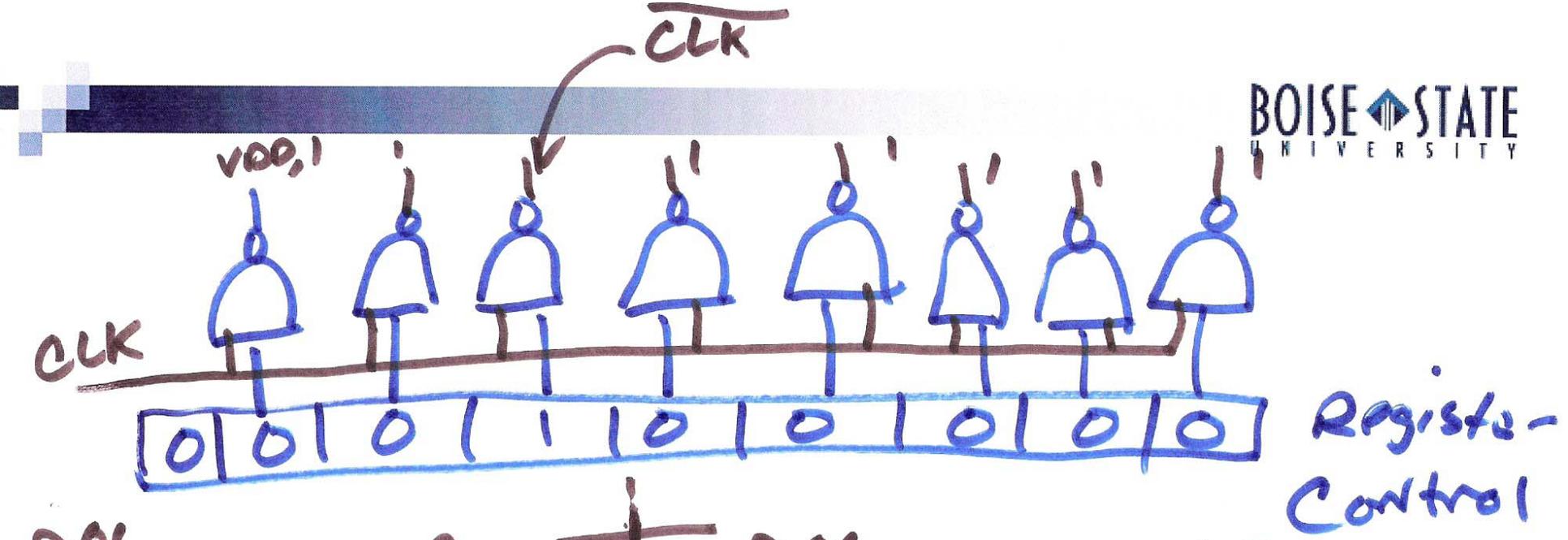
Digital DLLs



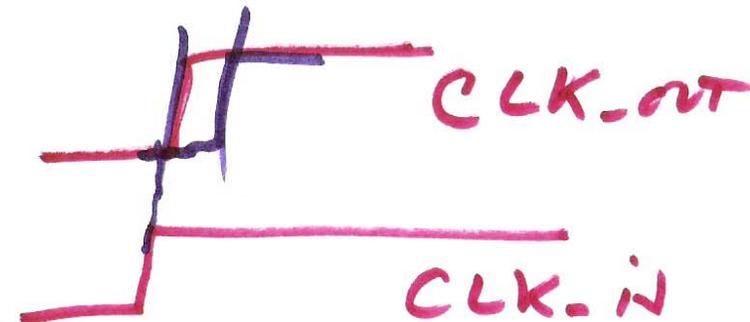
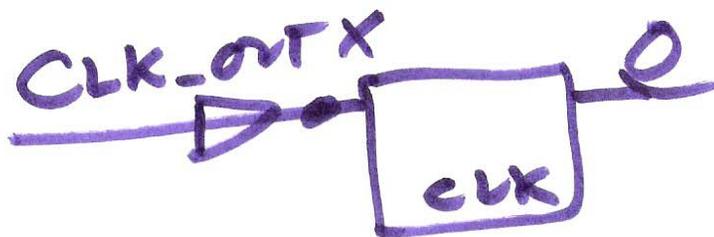
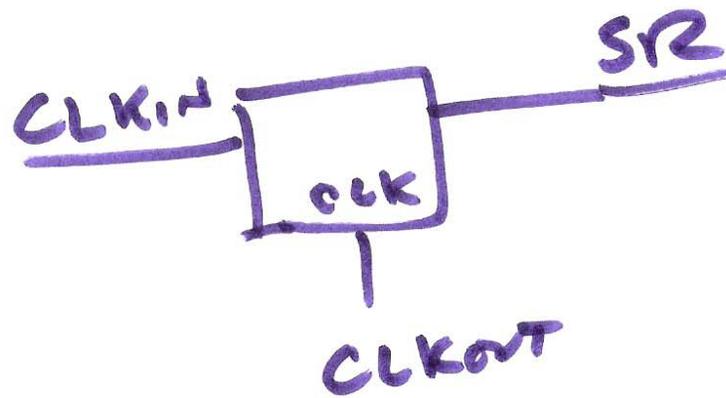
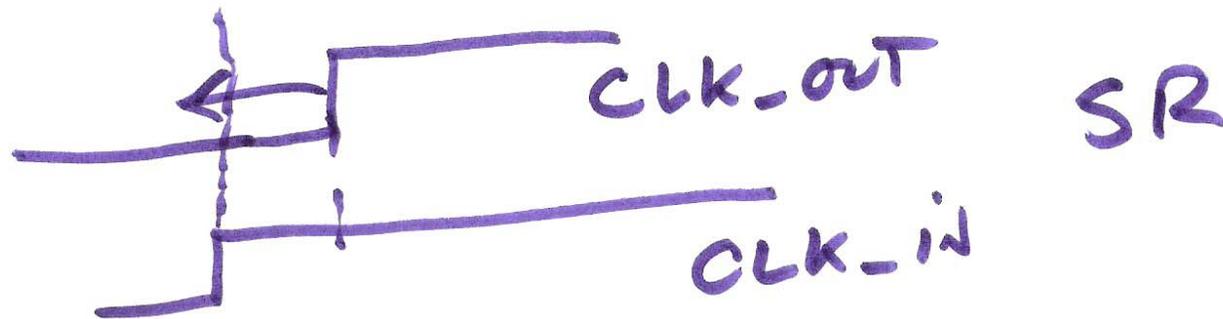
2)



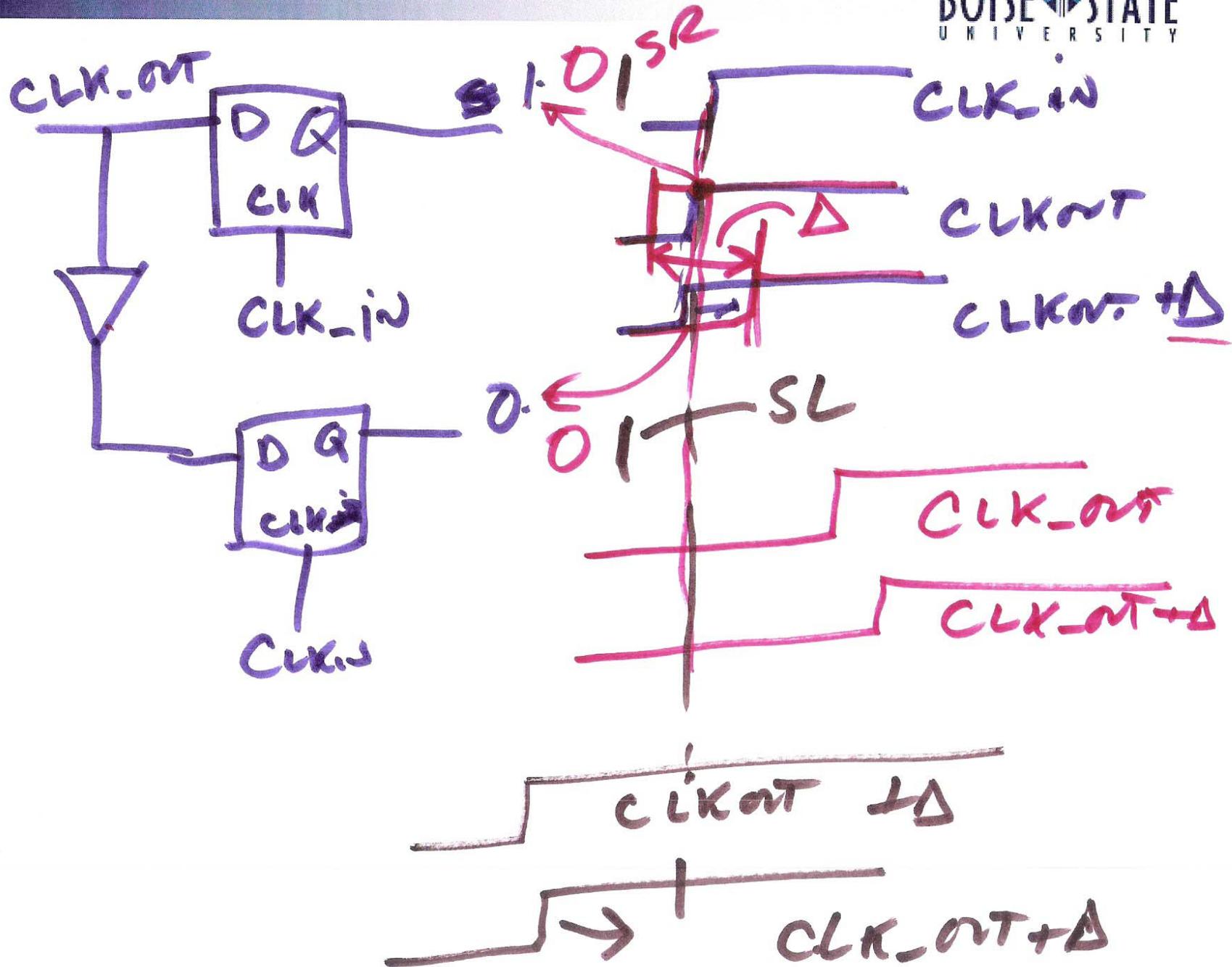
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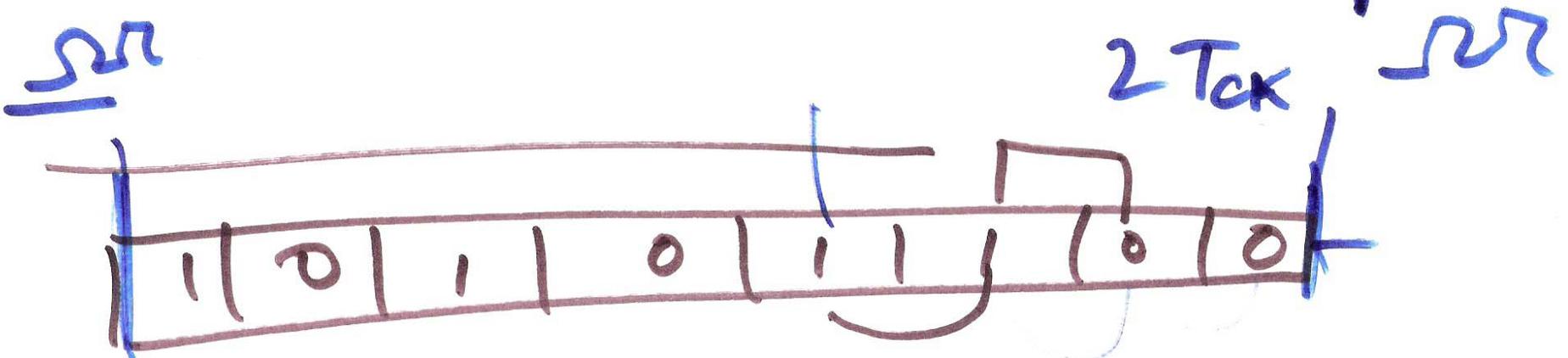
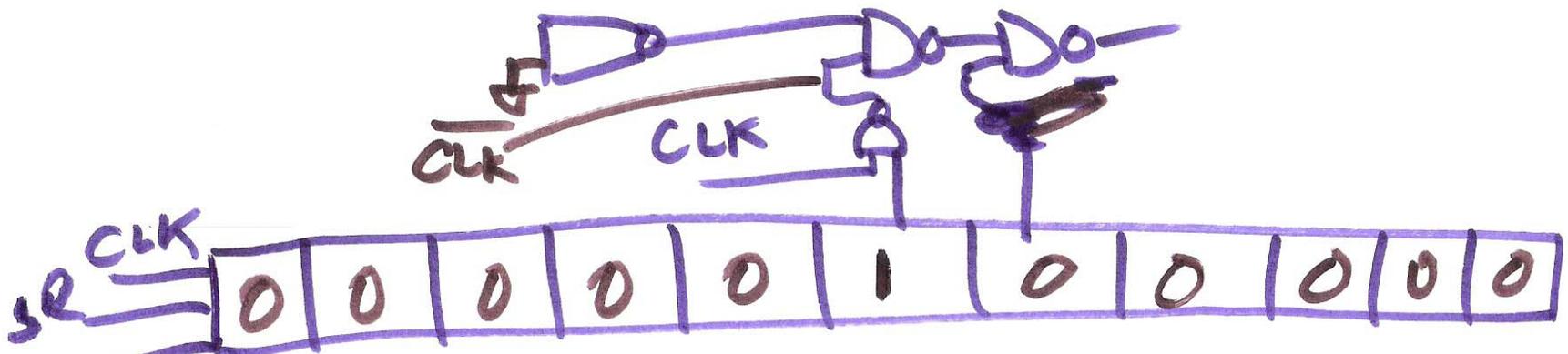
4)



s)



6)



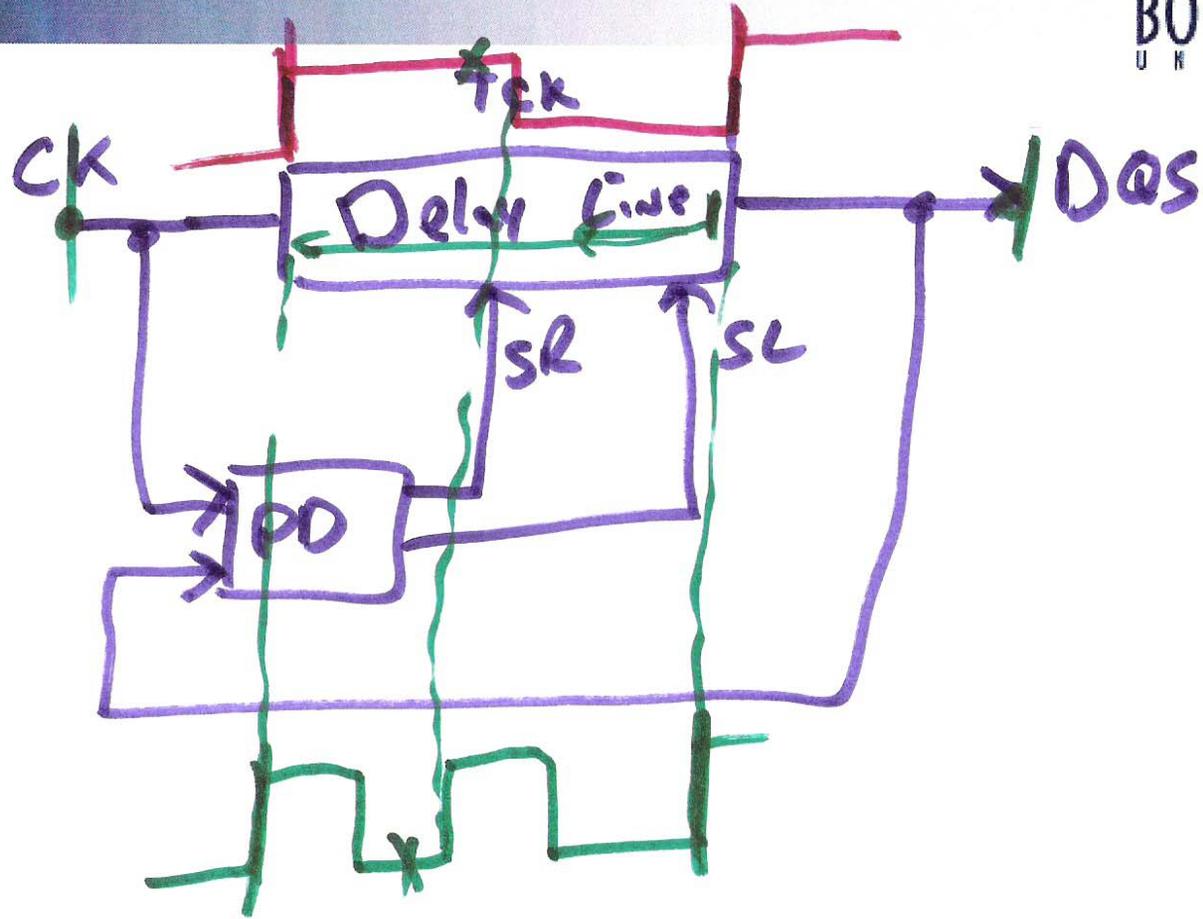
0 1 0 1 1 0 1 0 0 0

100 delay elements

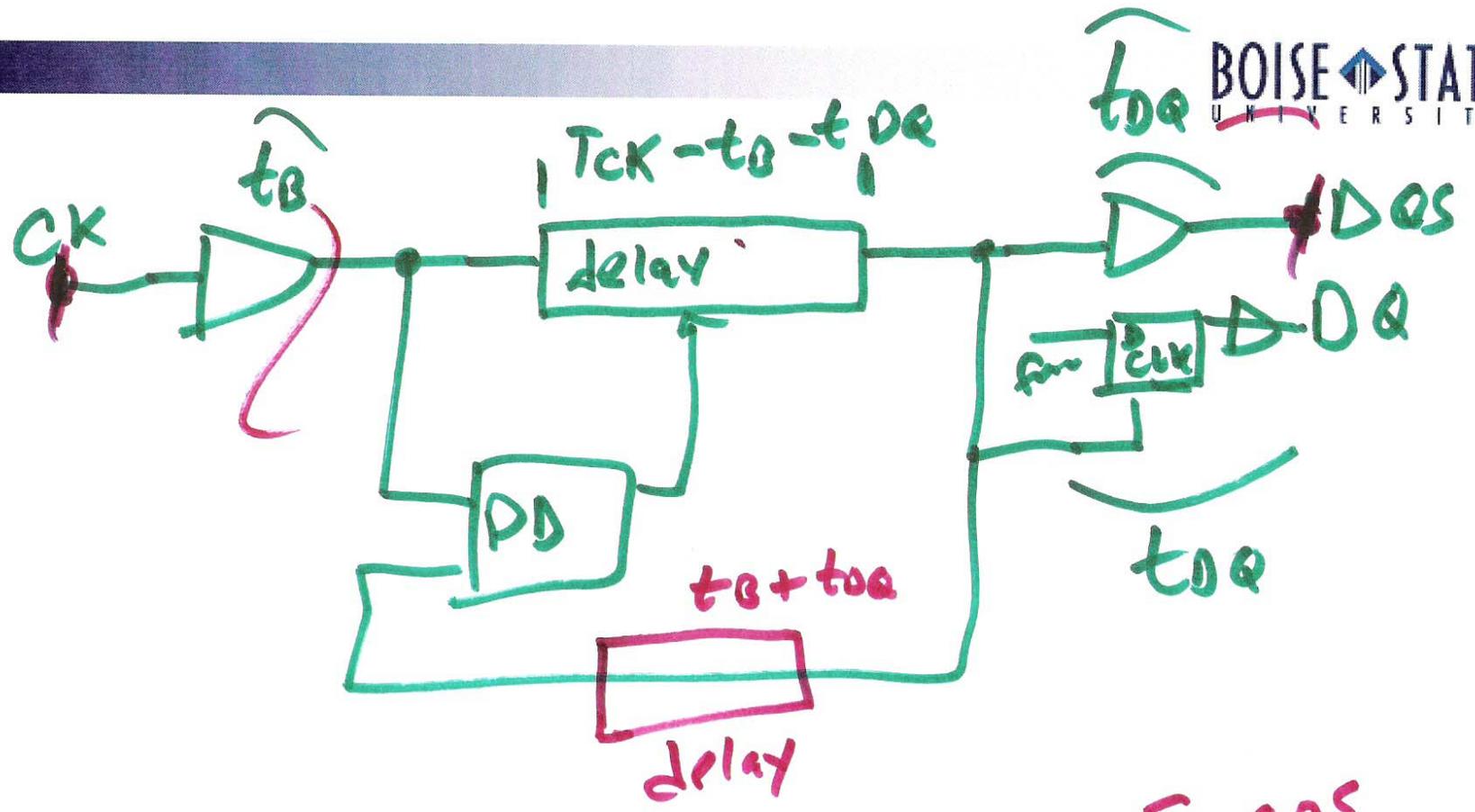
50ps

5ns

7)

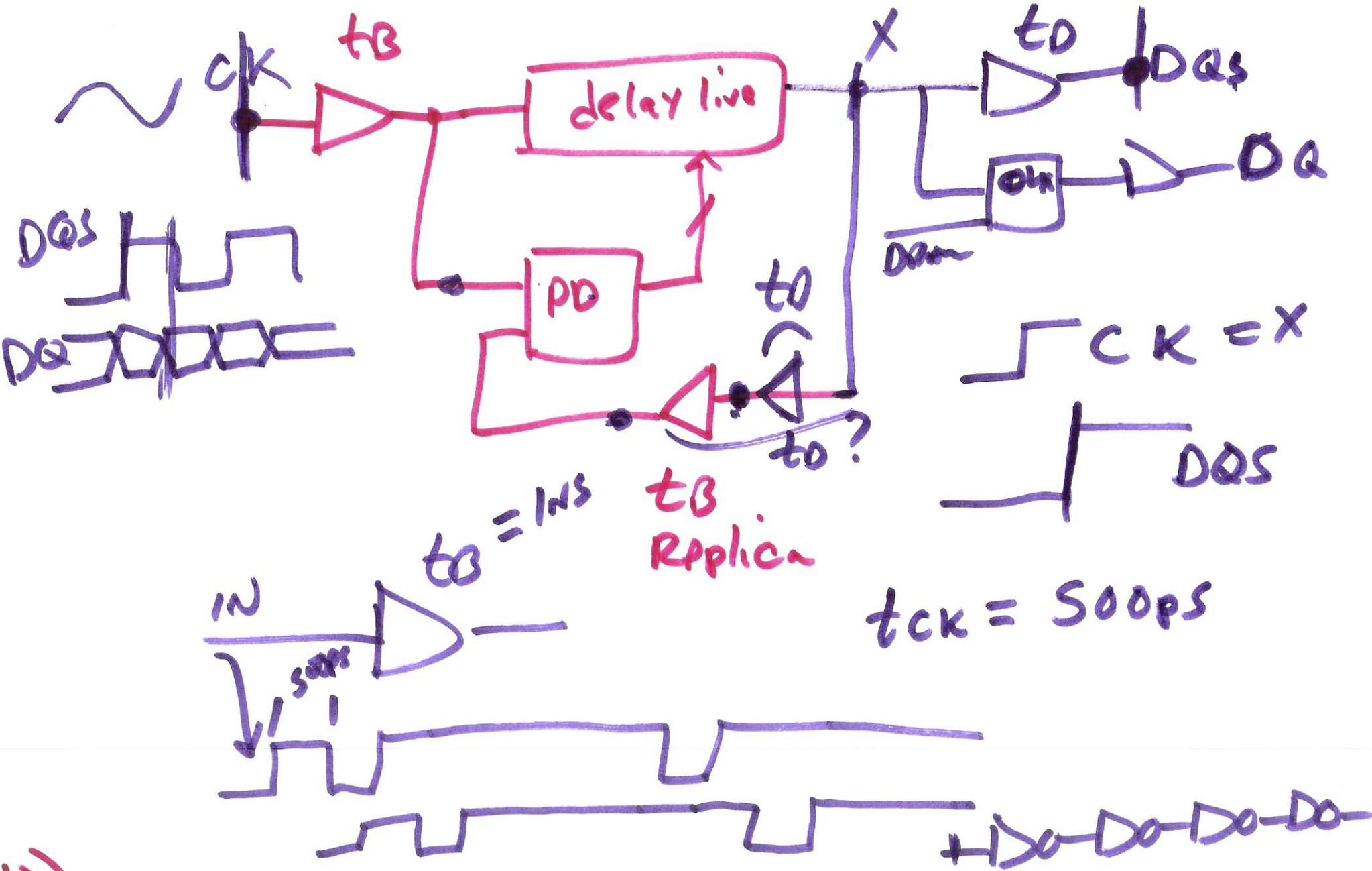


5)



$$t_{CK} = 500ps$$
$$f = 2Gb/s$$

9)



10)