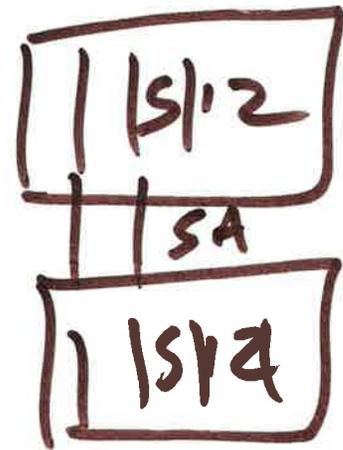


Lecture 7

ECE 5/418 memory circuit Design



2/9/11



512 bits

page size
is 8kb

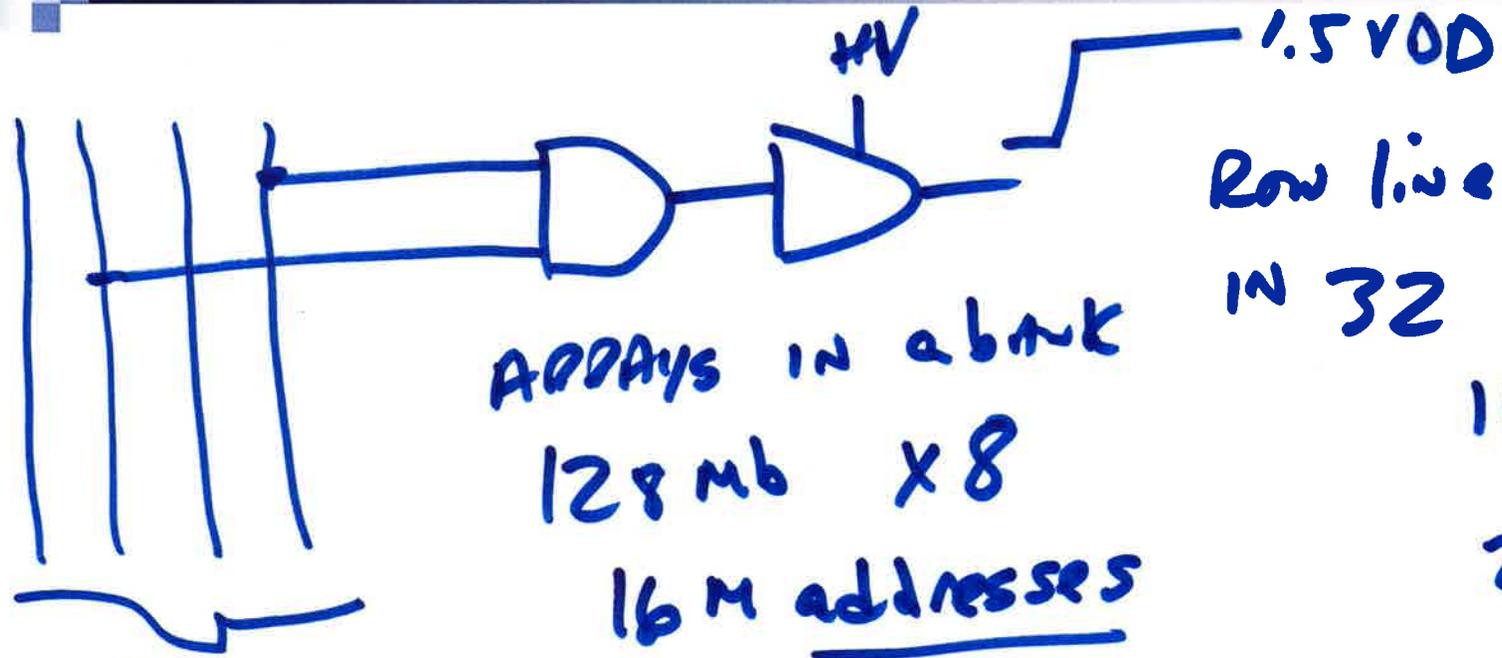


16 ARRAYS

8K x 1

4K x 2

2K x 4



ARRAYS in a bank
 128 Mb x 8
 16 M addresses
BANK

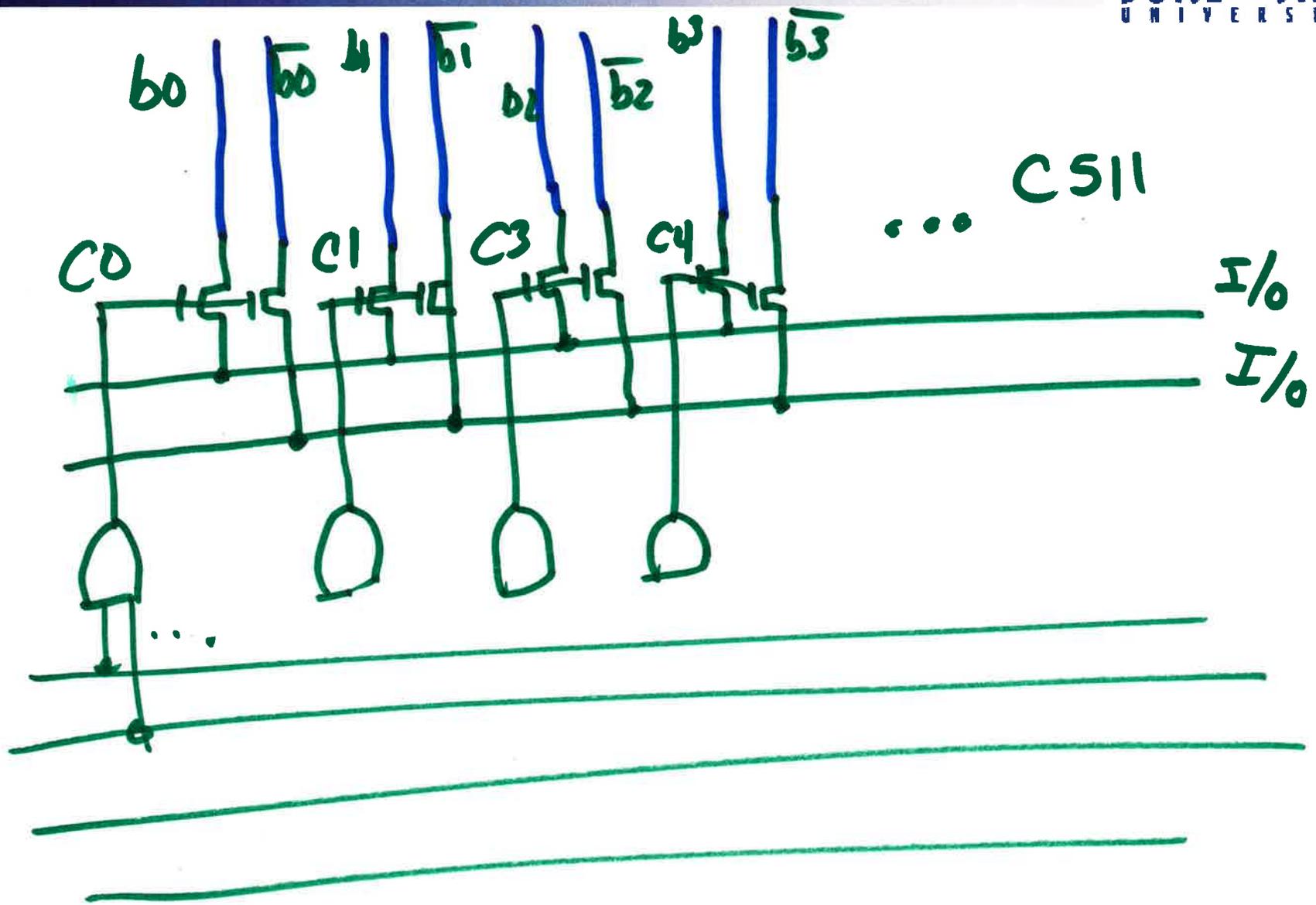
Row line goes high
 IN 32 ARRAYS

16 Kb page size
 2048 x 8
 ↑
 column address

Row Address
 13-pairs
 8,192
 addresses

Row addresses?
 8k (again)

$2^{13} = 8k$ addresses



4)