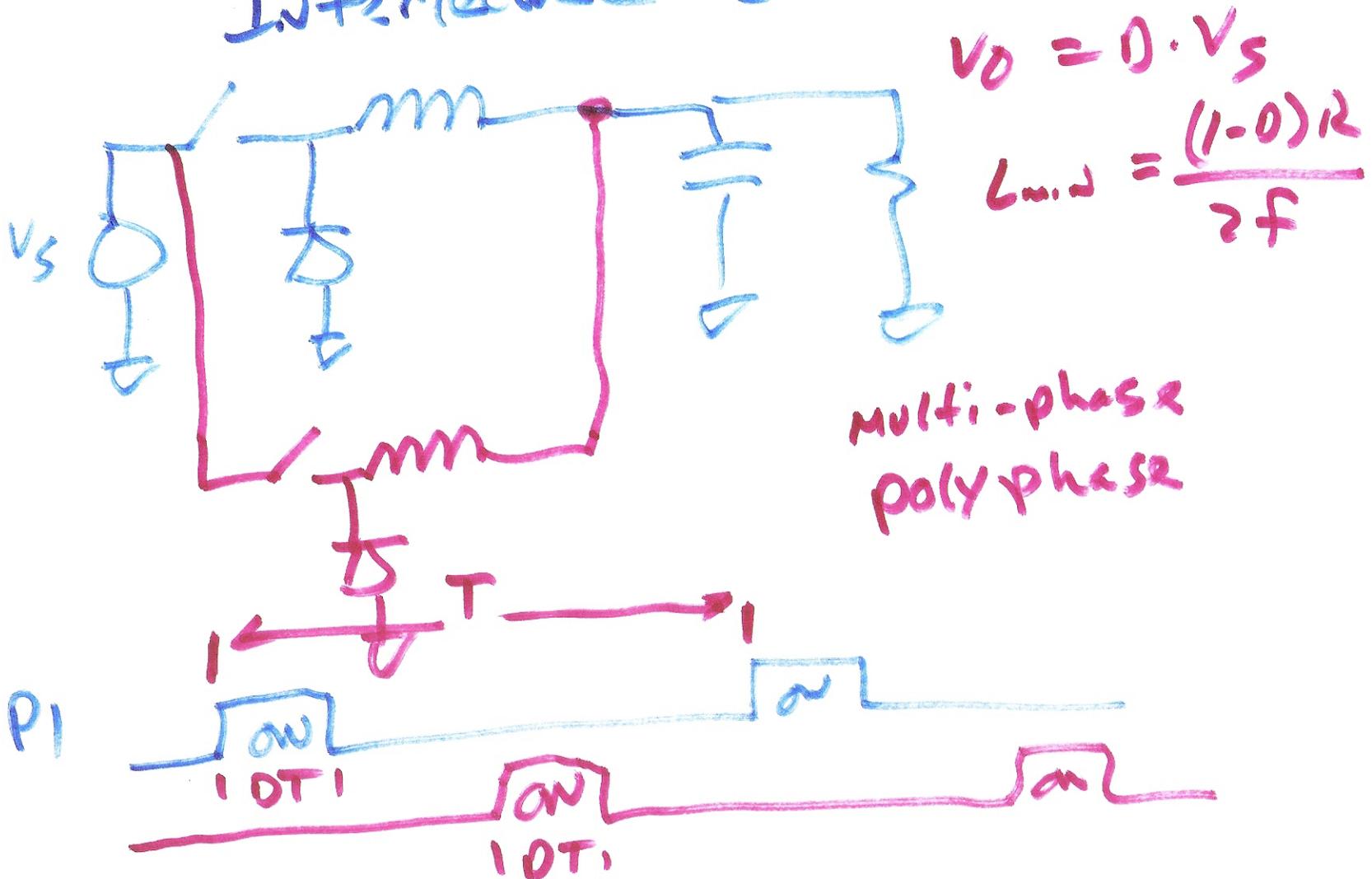
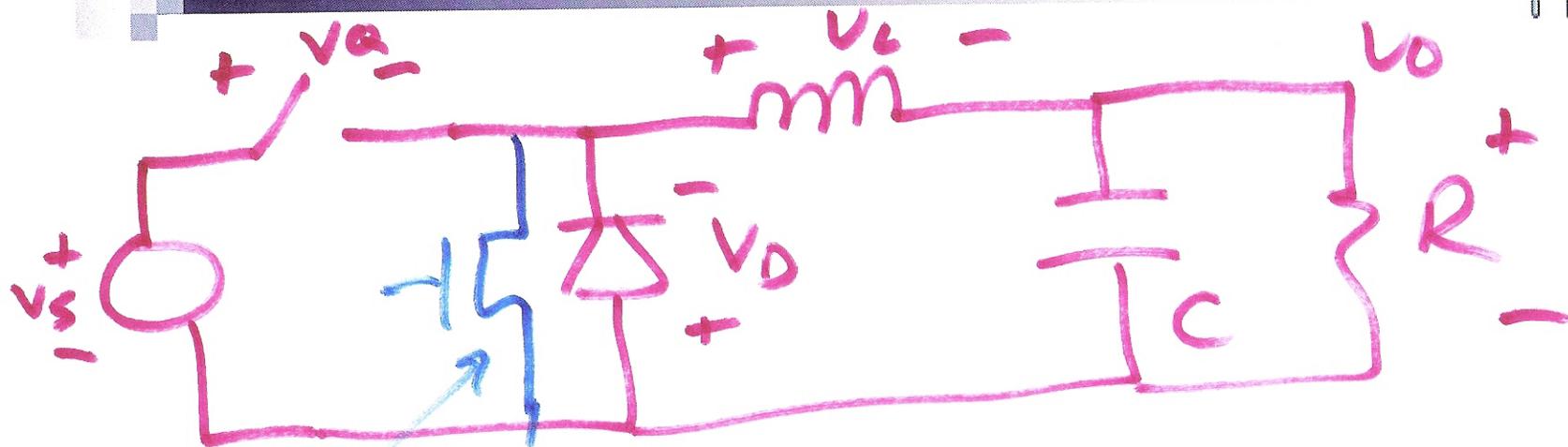


Lecture 17

OCT. 4, 2010

Interleaved Converters





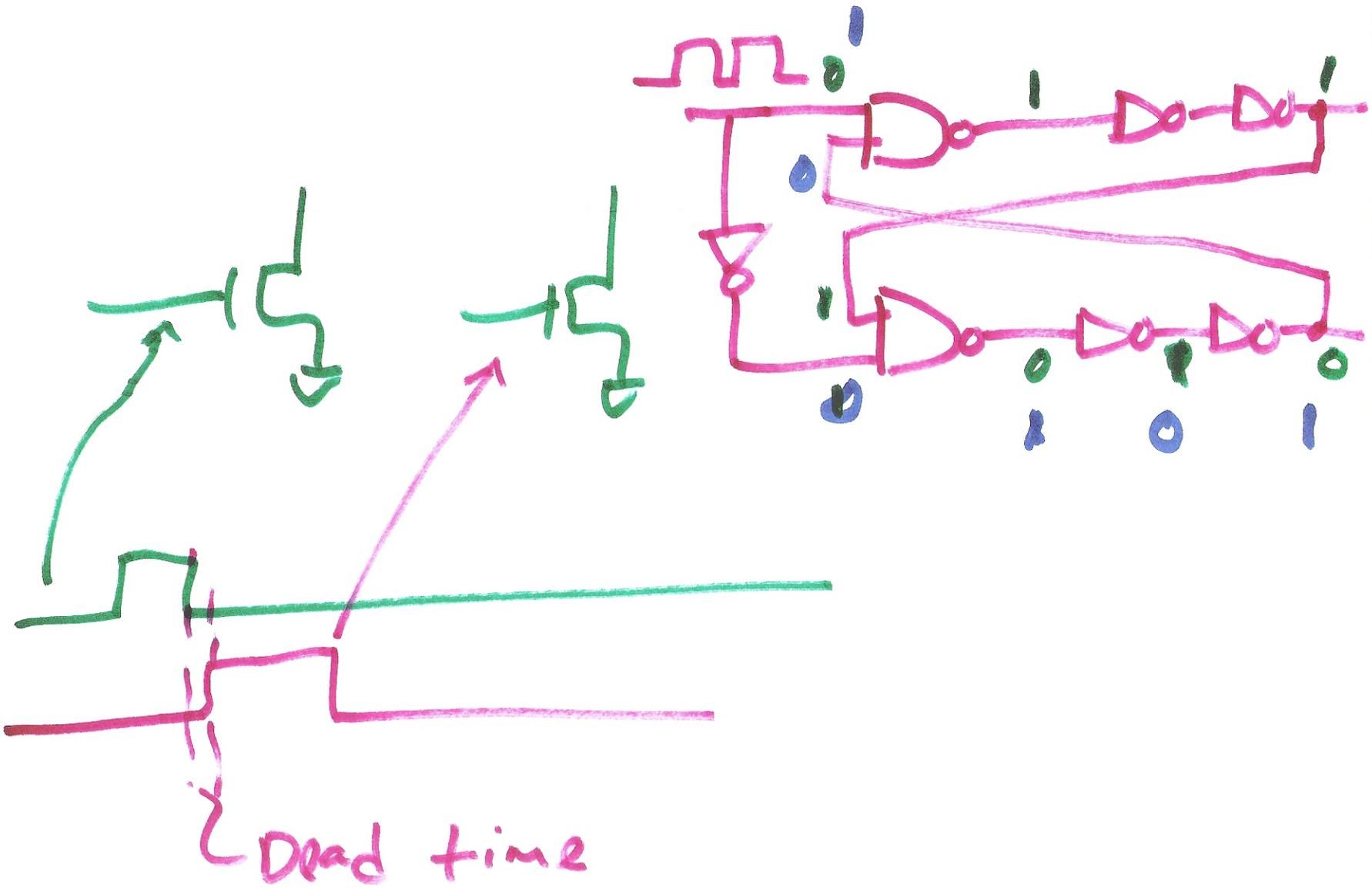
Replace diode with transistor called SYNC.

$V_s - V_Q - V_L - V_O = 0$
 $V_L = V_s - V_O - V_Q$ closed
 $V_L = -V_O - V_D$ open

$$(V_s - V_O - V_Q)D = (1-D)(-1)(-V_O - V_D)$$

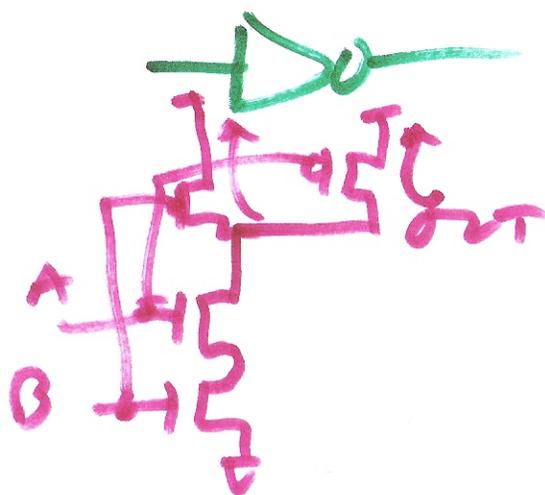
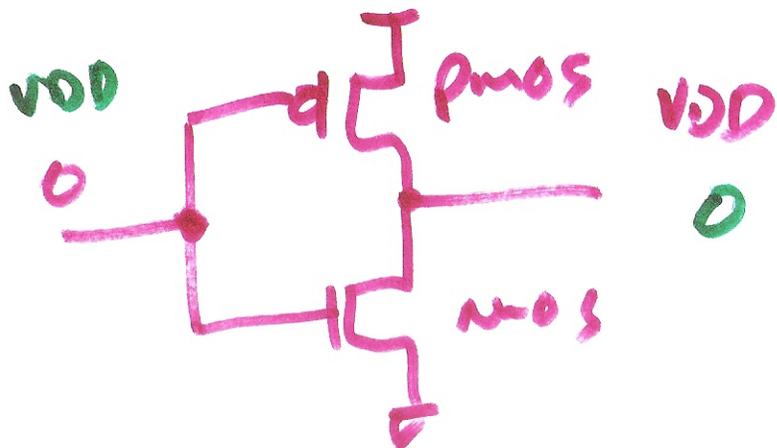
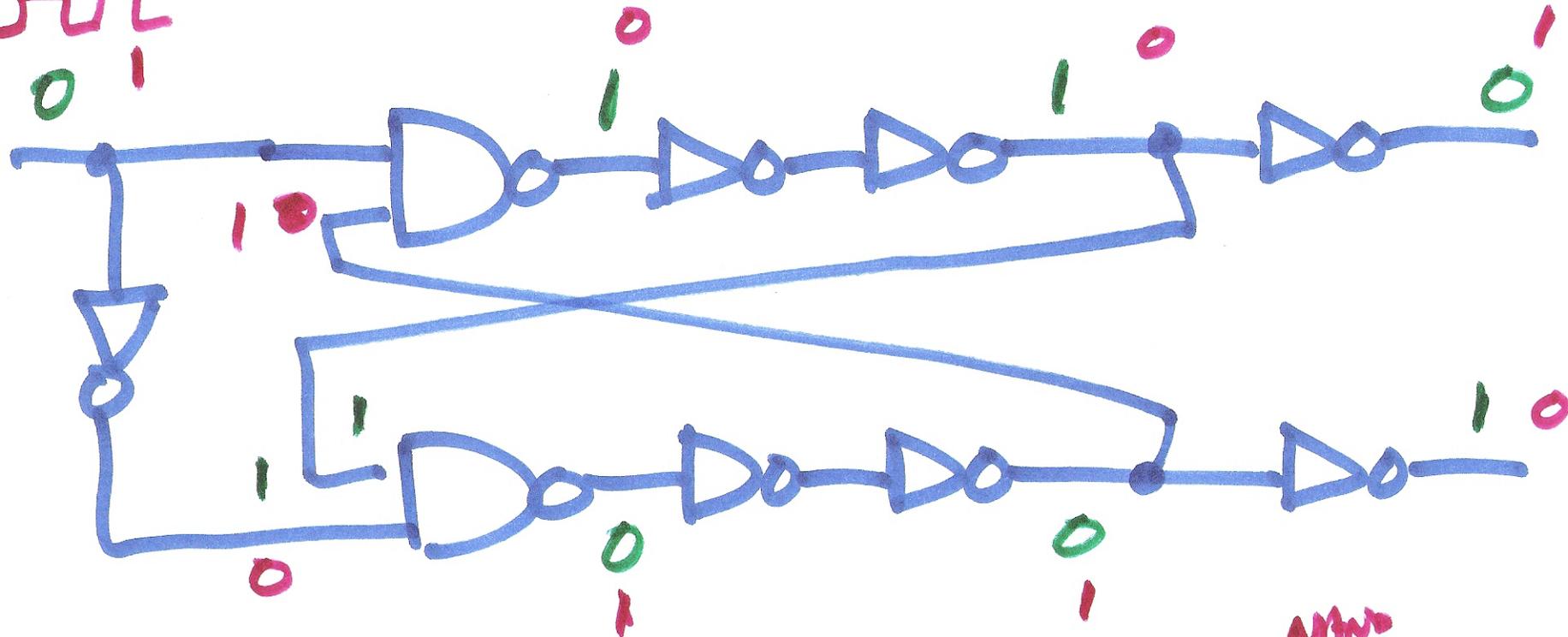
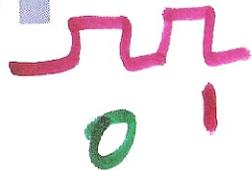
$$V_O = V_s D - V_Q D - V_D(1-D)$$

2)



3)

Non-overlap clock generation



NAND

A	B	NAND
0	0	1
0	1	1
1	0	1
1	1	0

4)