Quiz #15 EE 220 Spring 2017   Name:_____
Closed book and notes.
Show your work for credit!

1. What is wrong with the following LTspice schematic? Note that this schematic was
    copied from one of the questions on the CMOSedu Google Group. (2 points, no partial
    credit)

Vr

IL
10mH

R1
1k

.ic I(L1)=5mA
.tran 50u